NEC TECHNOLOGIES, INC.

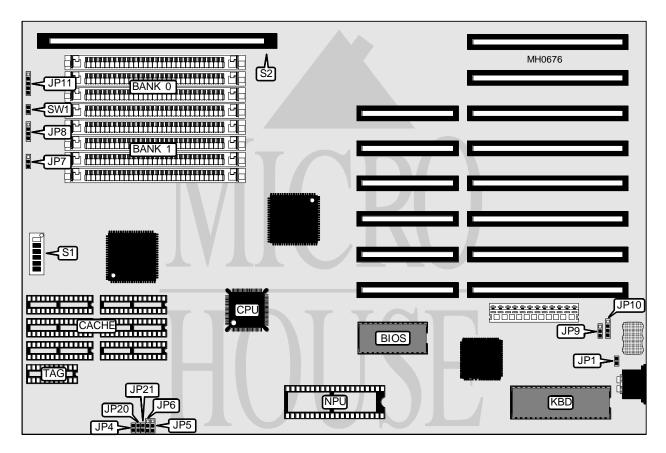
POWERMATE 386SXMB

80386SX **Processor Processor Speed** 12/16MHz **Chip Set ETEQ** Max. Onboard DRAM 32MB Cache 16/32KB **BIOS** AMI

Dimensions 330mm x 218mm

I/O Options Proprietary external memory card slot

NPU Options 80287



CONNECTIONS					
Purpose	Location	Purpose	Location		
Speaker	JP8	Proprietary external memory card	S2		
External battery	JP10	Reset switch	SW1		
Power LED & keylock	JP11				

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USER CONFIGURABLE SETT	INGS	
Function	Jumper/Switch	Position
í Monitor type select color	JP1	closed
Monitor type select monochrome	JP1	open
í Auxiliary Reference Burst enabled	JP7	pins 1 & 2 closed
Auxiliary Reference Burst disabled	JP7	pins 2 & 3 closed
Battery select internal	JP9	pins 1 & 2 closed
Battery select external	JP9	pins 2 & 3 closed
í Proprietary system logic enabled	JP31	closed
í Proprietary system logic enabled	JP31	closed
í Main Reference Burst enabled	JP45	pins 1 & 2 closed
Main Reference Burst disabled	JP45	pins 3 & 4 closed
í Factory configured - do not alter	JP48	open
í Factory configured - do not alter	S1/switch 1	open
í Factory configured - do not alter	S1/switch 2	open
í Redundancy circuit duplication enabled	S1/switch 3	closed
Redundancy circuit duplication disabled	S1/switch 3	open
í Cache tag bit size select 16-bit	S1/switch 4	open
Cache tag bit size select 32-bit	S1/switch 4	closed
í Board power select Direct Current	S1/switch 5	open
Board power select Alternating Current (onboard converter)	S1/switch 5	closed
í Flash ROM programming disabled	S1/switch 6	open
Flash ROM programming enabled	S1/switch 6	closed
í Factory configured - do not alter	J1	open
í Factory configured - do not alter	J2	open
í Factory configured - do not alter	J3	open
í Factory configured - do not alter	J4	open
í Factory configured - do not alter	J5	open
í Factory configured - do not alter	J7	open
í Factory configured - do not alter	J10	open
í Factory configured - do not alter	J23	open
í Factory configured - do not alter	J68	open
í Factory configured - do not alter	J101	open
í Factory configured - do not alter	J102	open
í Factory configured - do not alter	J103	open
í Factory configured - do not alter	J206	open
í Factory configured - do not alter	J207	open
í Factory configured - do not alter	J208	open
í Factory configured - do not alter	J209	open
í Factory configured - do not alter	J212	open
í Factory configured - do not alter	J213	open
Note: Locations of jumpers J1 - J213 are unknown.		

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DRAM CONFIGURATION				
Size	Bank 0 Bank 1			
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1 M x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
17MB	MB (4) 256K x 9 (4) 4M			
20MB	(4) 1M x 9 (4) 4M x 9			
32MB	(4) 4M x 9	(4) 4M x 9 (4) 4M x 9		

CACHE CONFIGURATION				
Size	Cache SRAM	TAG		
16KB	(4) 8K x 4	(1) 2K x 2		
32KB	(8) 8K x 4	(1) 2K x 2		

CACHE JUMPER CONFIGURATION					
Jumper	JP4	JP5	JP6	JP20	JP21
16KB	Closed	pins 1 & 2 closed	pins 2 & 3 closed	Open	Closed
32KB	Open	pins 2 & 3 closed	pins 1 & 2 closed	Closed	Open