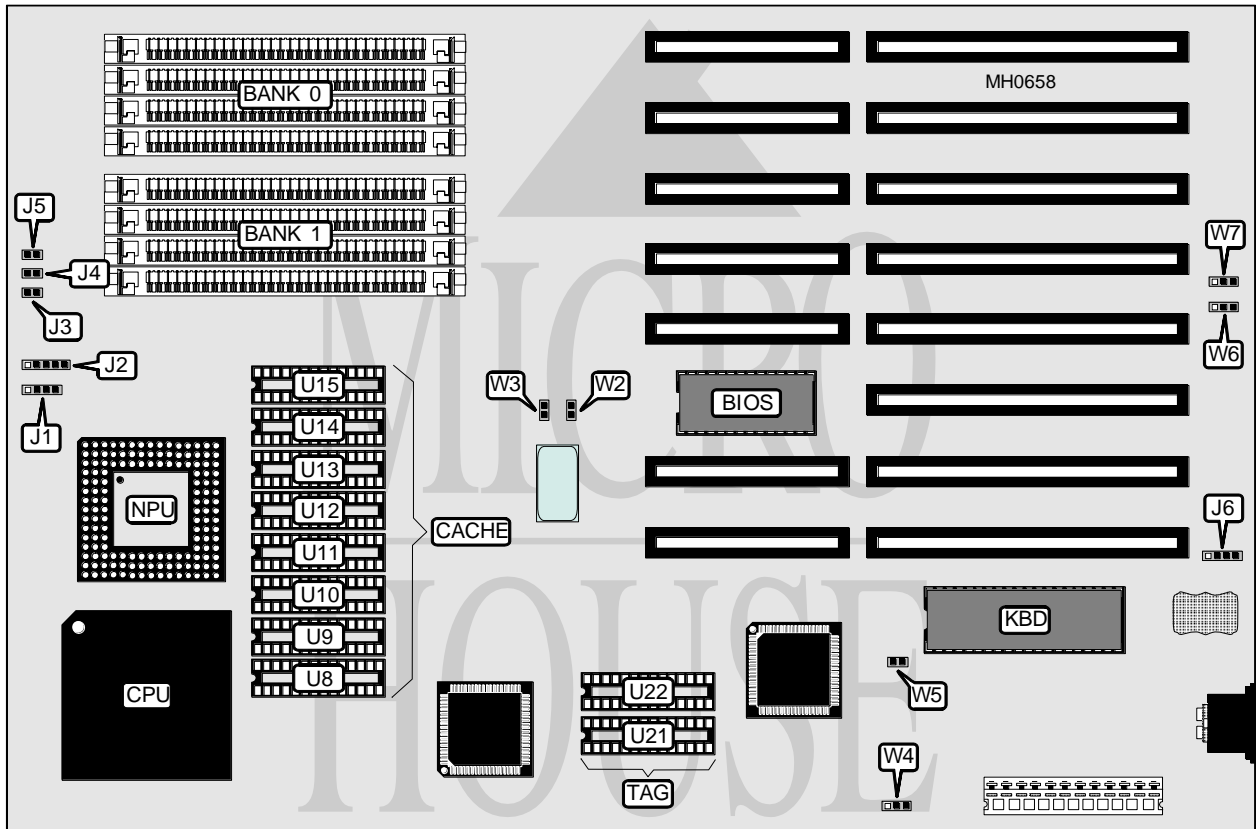


BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-1420C-CH/1433C-CH

Processor	80486SX/80486DX
Processor Speed	20/33MHz
Chip Set	C & T
Max. Onboard DRAM	32MB
Cache	64/128KB
BIOS	AMI
Dimensions	327mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Turbo LED	J4
Power LED & keylock	J2	Turbo switch	J5
Reset switch	J3	External battery	J6

Continued on next page . . .

BIOSTAR MICROTECH INTERNATIONAL CORPORATION
MB-1420C-CH/1433C-CH

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	W4	pins 2 & 3 closed
CMOS memory clear	W4	pins 1 & 2 closed
í Monitor type select color	W5	Closed
Monitor type select monochrome	W5	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U21)	TAG (U22)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8	NONE

Note: Bank 0 = U8, U10, U12, & U14. With 16MB or more of DRAM, 128KB of cache must be installed.

CACHE JUMPER CONFIGURATION	
Size	W1
64KB	pins 2 & 3 closed
128KB	pins 1 & 2 closed

CPU TYPE CONFIGURATION		
Type	W2	W3
80486SX-20	Open	Closed
80487SX-20	Closed	Open
80486DX-33	Open	Open

POWER GOOD CONFIGURATION		
Type	W6	W7
Signal detect from power supply	pins 1 & 2 closed	pins 1 & 2 closed
Signal detect from board	pins 2 & 3 closed	pins 2 & 3 closed