## JAMICON

KMC-33K 80386-33

| Processor | 80386 DX |
| :--- | :--- |
| Processor Speed | 33 MHz |
| Chip Set | Unknown |
| Max. Onboard DRAM | 8 MB |
| Cache | None |
| BIOS | AMI/Award |
| Dimensions | $330 \mathrm{~mm} \times 218 \mathrm{~mm}$ |
| I/O Options | $32-$ bit external memory card slot |
| NPU Options | $80387 / 3167$ |



| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| External battery | JP10 | Power LED \& keylock | Speaker |
| Turbo LED | JP11 | 32-bit external memory card | JP13 |
| Turbo switch | JP12 |  | S1 |
| Reset switch |  |  |  |
| Note: All cache conections, jumpers and sizes are unidentified. |  |  |  |

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| Uunction |  | Jumper |
| :--- | :---: | :---: |
| Position |  |  |
| í Battery type select external | W 1 | pins 1 \& 2 closed |
| Battery type select internal | W 1 | pins 2 \& 3 closed |
| í CMOS memory normal operation | W 2 | pins 1 \& 2 closed |
| CMOS memory clear | W 2 | pins 2 \& 3 closed |
| í Monitor type select color | W 3 | Closed |
| Monitor type select monochrome | W 3 | Open |
| í BIOS type select AMI | W 5 | pins 1 \& 2 closed |
| BIOS type select AWARD | W 5 | pins 2 \& 3 closed |
| í Parity enabled | W 9 | Open |
| Parity disabled | W 9 | Closed |
| í 128-byte internal cache enabled, external cache disabled | W 10 to W13 | Closed |
| 128-byte internal cache disabled, external cache enabled | W 10 to W13 | Open |
| í NPU mode select asynchronous with CPU | W 14 | Closed |
| NPU mode select synchronous with CPU | W 14 | Open |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 1 MB | (4) $256 \mathrm{~K} \times 9$ | NONE |
| 2 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 4 MB | (4) $1 \mathrm{M} \times 9$ | NONE |
| 8 MB | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |


| CPU SPEED CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Speed | JP11 |  |
| 25 MHz | Closed |  |
| 33 MHz | Open |  |

