MICRONICS COMPUTERS, INC.
BABY GEMINI-386 ISA/LB

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	Micronics
Max. Onboard DRAM	32MB
Cache	64KB
BIOS	Phoenix
Dimensions	215mm x 337mm
I/O Options	32-bit external memory card slot, 32-bit VESA local bus slot
NPU Options	80387/3167
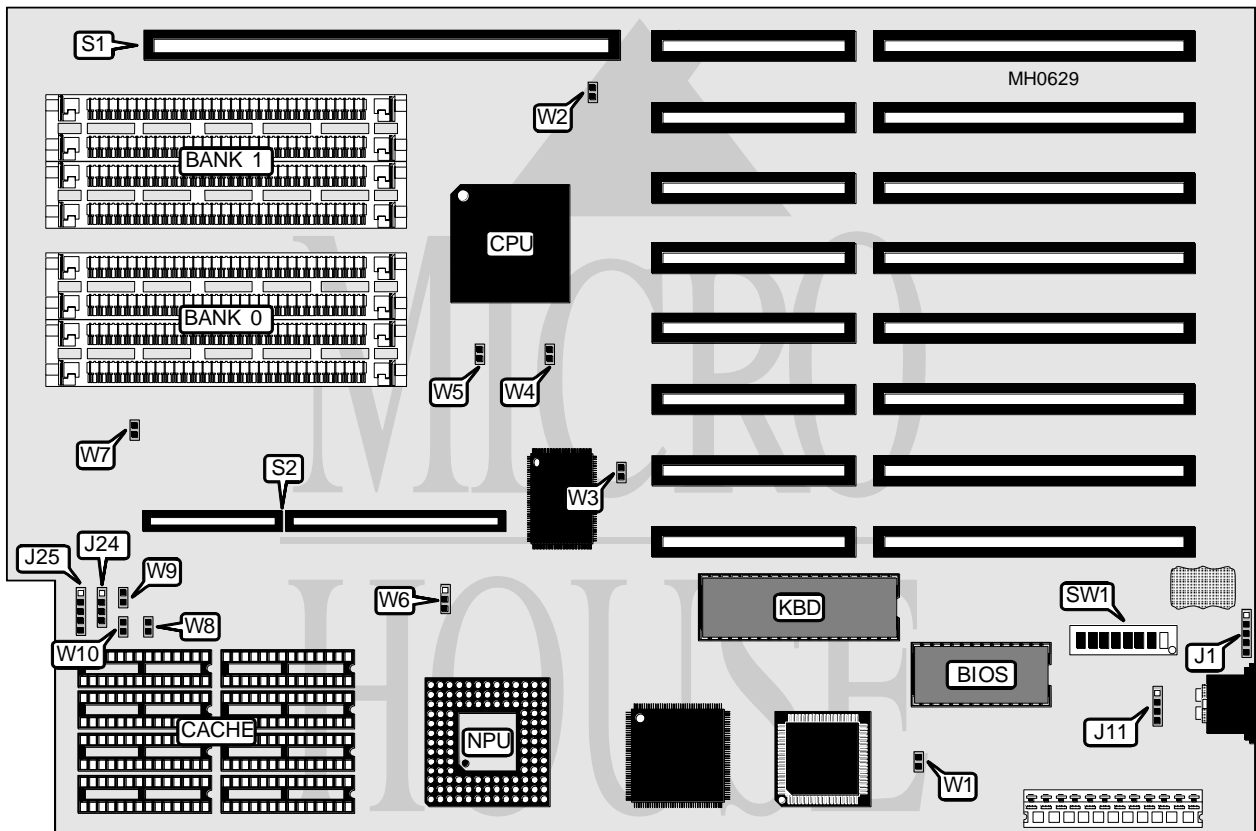
CONNECTIONS			
Purpose	Location	Purpose	Location
External keyboard	J1	Turbo LED	W9
External battery	J11	Turbo switch	W10
Speaker	J24	32-bit external memory card	S1
Power LED & keylock	J25	32-bit VESA local bus slot	S2
Reset switch	W8		

MICRONICS COMPUTERS, INC.
BABY GEMINI-386 ISA/LB

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	W1	Closed
Monitor type select monochrome	W1	Open
í Factory configured - do not alter	W5	Open
í Factory configured - do not alter	W7	Open
í Factory configured - do not alter	SW1/6	On
í Factory configured - do not alter	SW1/7	Off
í Factory configured - do not alter	SW1/8	On

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1 M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

MICRONICS COMPUTERS, INC.
BABY GEMINI-386 ISA/LB

DRAM SWITCH CONFIGURATION					
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5
1MB	On	On	On	On	On
2MB	Off	On	On	On	On
3MB	On	Off	On	On	On
4MB[1]	Off	Off	On	On	On
4MB[2]	On	On	Off	On	On
5MB	On	On	Off	Off	On
6MB	On	Off	On	Off	On
7MB	Off	On	On	Off	On
8MB	Off	On	Off	On	On
9MB	On	Off	Off	Off	On
10MB	Off	Off	On	Off	On
12MB	On	Off	Off	On	On
13MB	Off	Off	Off	Off	On
16MB[1]	Off	Off	Off	On	On
16MB[3]	On	On	Off	On	Off
20MB	On	On	Off	Off	Off
24MB	On	Off	On	Off	Off
28MB	Off	On	On	Off	Off
32MB	Off	On	Off	On	Off
36MB	On	Off	Off	Off	Off
40MB	Off	Off	On	Off	Off
48MB	On	Off	Off	On	Off
52MB	Off	Off	Off	Off	Off
64MB	Off	Off	Off	On	Off

Note [1]: Use this setting when banks are populated with (4) 256K x 9 SIMMs.
Note [2]: Use this setting when banks are populated with (4) 1M x 9 SIMMs.
Note [3]: Use this setting when banks are populated with (4) 4M x 9 SIMMs.

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8

CACHE JUMPER CONFIGURATION		
Size	W2	W3
Cache up to 16MB of DRAM	Off	Off
Cache up to 64MB of DRAM	Closed	Closed

CPU SPEED CONFIGURATION		
Speed	W4	W6
25MHz	Open	pins 2 & 3 closed
33MHz	Open	pins 1 & 2 closed
40MHz	Closed	pins 2 & 3 closed