## MICRONICS COMPUTERS, INC.

## 80386 ASIC

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

80386DX
25 MHz
Micronics
8MB
None
Phoenix
$330 \mathrm{~mm} \times 218 \mathrm{~mm}$
32-bit external memory card slot
80387


| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Power LED \& keylock | J5 | Turbo LED | W4 |
| External keyboard | J6 | Turbo switch | W5 |
| External battery | J9 | 32-bit external memory card | J700 |
| Reset switch | W1 | Speaker | J701 |

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| USER CONFIGURABLE SETTINGS |  |  |
| :---: | :---: | :---: |
| Function | Jumper/Switch | Position |
| í Factory configured - do not alter | SW1/1 | Off |
| í NPU disabled | SW1/2 | Off |
| NPU enabled | SW1/2 | On |
| í Factory configured - do not alter | SW1/3 | Off |
| í Shadow BIOS disabled | SW1/4 | Off |
| Shadow BIOS enabled | SW1/4 | On |
| í Factory configured - do not alter | SW1/5 | Off |
| í Monitor type select monochrome | SW1/6 | Off |
| Monitor type select color | SW1/6 | On |
| í Factory configured - do not alter | SW1/7 | Off |
| í Factory configured - do not alter | SW1/8 | Off |
| í External memory card select normal operation | W700 | Open |
| External memory card select extended memory over 15MB | W700 | Closed |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 1 MB | (4) $256 \mathrm{~K} \times 9$ | NONE |
| 2 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 4 MB | (4) $1 \mathrm{M} \times 9$ | NONE |
| 5 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 8 MB | (4) $1 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |


| DRAM JUMPER CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | W98 | W100 |
| 1 MB | pins $1 \& 2$ closed | pins $1 \& 2$ closed |
| 2 MB | pins $1 \& 2$ closed | pins $1 \& 2$ closed |
| 4 MB | pins $2 \& 3$ closed | pins $1 \& 2$ closed |
| 5 MB | pins $1 \& 2$ closed | pins $2 \& 3$ closed |
| 8 MB | pins $2 \& 3$ closed | pins $2 \& 3$ closed |

