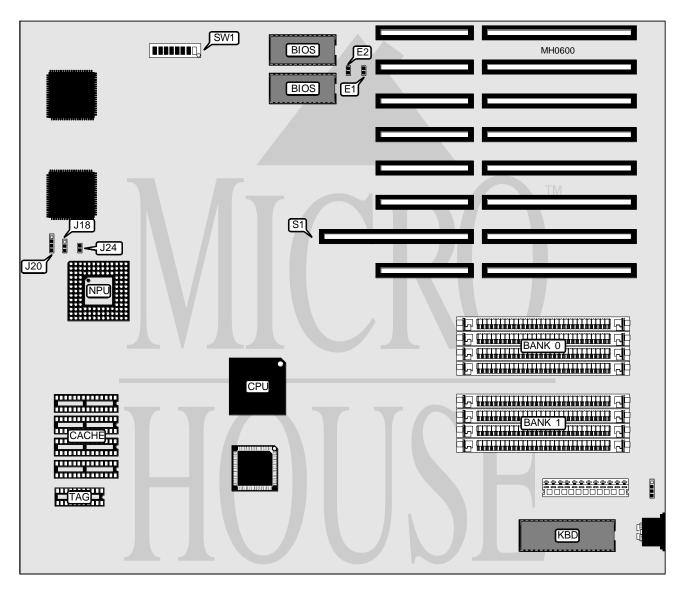
HYUNDAI ELECTRONICS, INC. S U P E R - 3 8 6 N

Processor 80386DX
Processor Speed 25MHz
Chip Set VLSI
Max. Onboard DRAM 8MB
Cache 32KB
BIOS Phoenix

Dimensions 330mm x 218mm

I/O Options 32-bit external memory card slot

NPU Options 80387/3167



Continued on next page . . .

HYUNDAI ELECTRONICS, INC. SUPER-386N

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CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J18	Speaker	J24
Keylock & Reset switch	J20	32-bit external memory card	S1
External battery J21			
Note: The locations of J18, J20, J21, & J24 are unidentified.			

USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í NPU disabled	SW1/1	Off
NPU enabled	SW1/1	On
í Extended memory (384KB) enabled	SW1/4	On
Extended memory (384KB) disabled	SW1/4	Off
í Monitor type select color	SW1/5	On
Monitor type select monochrome	SW1/5	Off
í Cache enabled	SW1/6	On
Cache disabled	SW1/6	Off
í Factory configured - do not alter	SW1/7	N/A
í Factory configured - do not alter	SW1/8	N/A

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9

DRAM SWITCH CONFIGURATION		
Size	SW1/2	SW1/3
1MB	Off	Off
2MB	Off	On
4MB	On	Off
8MB	On	On

CACHE CONFIGURATION			
	Size	Bank 0	TAG
	32KB	(4) 8K x 8	(1) 16K x 4
Note:	Note: The location and size of cache/tag chips are unidentified.		

BIOS CONFIGURATION		
Туре	E1	E2
27256	pins 1 & 2 closed	pins 1 & 2 closed
27512	pins 2 & 3 closed	pins 2 & 3 closed