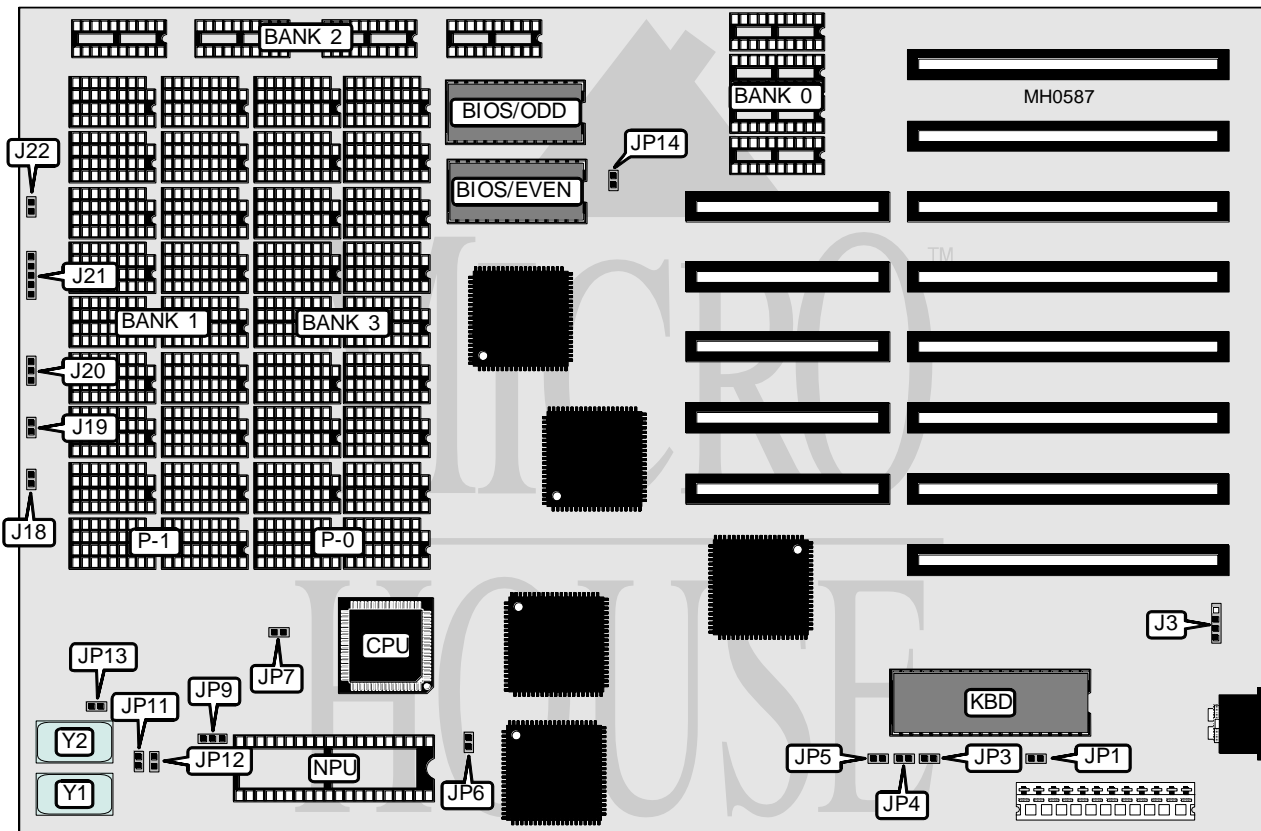


# UNIDENTIFIED

## VLSI - 286

<b>Processor</b>	80286
<b>Processor Speed</b>	12MHz
<b>Chip Set</b>	VLSI
<b>Max. Onboard DRAM</b>	4MB
<b>Cache</b>	None
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Turbo switch	J20
Speaker	J18	Power LED & keylock	J21
Reset switch	J19	Turbo LED	J22

Continued on next page . . .

# UNIDENTIFIED

## VLSI-286

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í Battery type select internal	J3	pins 2 & 3 closed
Battery type select external	J3	Closed
í BIOS wait state select 1 wait state	JP6	Open
BIOS wait states select 2 wait states	JP6	Closed
í Oscillator select Y2	JP9	pins 1 & 2 closed
Oscillator select Y1	JP9	pins 2 & 3 closed
í BIOS type select 27256	JP14	Closed
BIOS type select 27128	JP14	Open
í DRAM wait state select 0 wait states	JP13	Closed
DRAM wait state select 1 wait states	JP13	Open
í CPU speed select switchable at keyboard	J20	pins 1 & 2 closed
CPU speed select forced high	J20	pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(16) 4164	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	NONE	(2) 41256	(4) 44256	NONE
2MB	NONE	(2) 411000	NONE	NONE	NONE	(16) 411000
4MB	NONE	(2) 411000	(16) 411000	(2) 411000	NONE	(16) 411000

DRAM JUMPER CONFIGURATION			
Size	JP3	JP4	JP5
512KB	Closed	Closed	Closed
640KB	Closed	Closed	Open
1MB	Closed	Open	Closed
2MB	Closed	Open	Open
4MB	Open	Closed	Closed