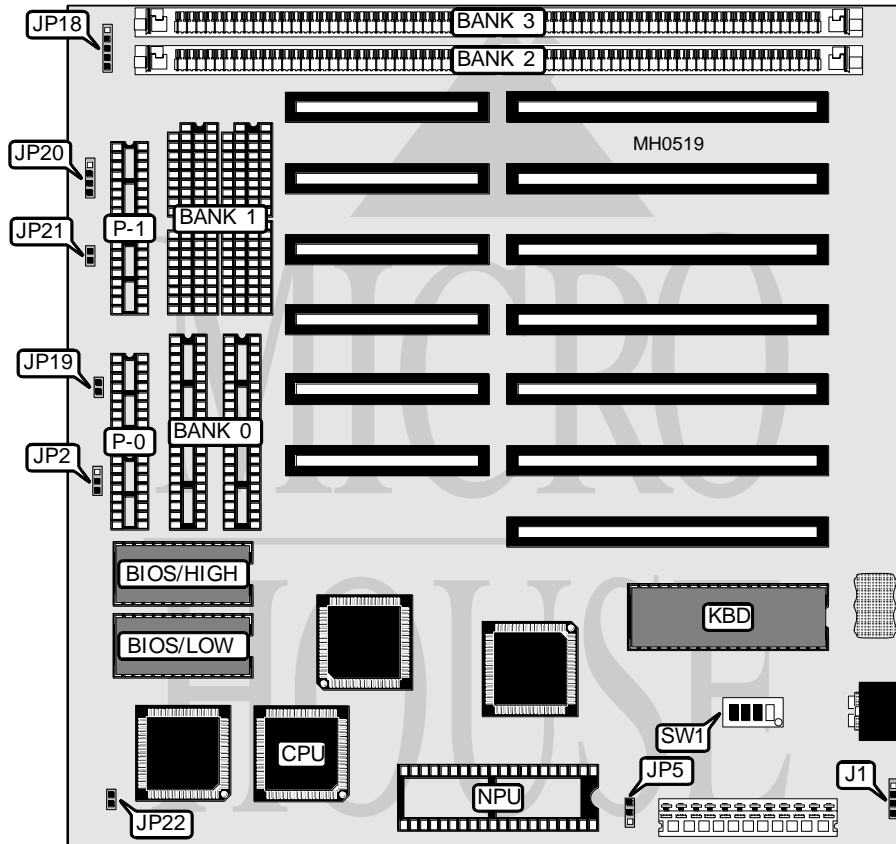


UNIDENTIFIED BABY - 286

| | |
|--------------------------|---------------|
| Processor | 80286 |
| Processor Speed | 16MHz |
| Chip Set | Unidentified |
| Max. Onboard DRAM | 4MB |
| Cache | None |
| BIOS | AMI |
| Dimensions | 220mm x 220mm |
| I/O Options | None |
| NPU Options | 80287 |



| CONNECTIONS | | | |
|---------------------|----------|------------------|----------|
| Purpose | Location | Purpose | Location |
| Power LED & keylock | JP18 | Reset switch | JP21 |
| Turbo LED | JP19 | External battery | J1 |
| Speaker | JP20 | | |

Continued on next page . . .

UNIDENTIFIED

B A B Y - 2 8 6

... continued from previous page

| USER CONFIGURABLE SETTINGS | | |
|--|---------------|-------------------|
| Function | Jumper/Switch | Position |
| í Wait states select 0 | JP2 | pins 2 & 3 closed |
| Wait state select 1 | JP2 | pins 1 & 2 closed |
| í NPU mode select synchronous with CPU | JP5 | pins 2 & 3 closed |
| NPU mode select asynchronous with CPU | JP5 | pins 1 & 2 closed |
| í Parity check enabled | JP22 | Closed |
| Parity check disabled | JP22 | Open |
| í Monitor type select color | SW1/1 | On |
| Monitor type select monochrome | SW1/1 | Off |

| DRAM CONFIGURATION | | | | | | |
|--------------------|-----------|-----------|-----------|-----------|------------|------------|
| Size | Bank 0 | Bank 1 | P-0 | P-1 | Bank 2 | Bank 3 |
| 512KB | (4) 44256 | NONE | (2) 41256 | NONE | NONE | NONE |
| 640KB | (4) 44256 | (4) 4464 | (2) 41256 | (2) 4164 | NONE | NONE |
| 1MB | (4) 44256 | (4) 44256 | (2) 41256 | (2) 41256 | NONE | NONE |
| 2MB | NONE | NONE | NONE | NONE | (2) 1M x 9 | NONE |
| 4MB | NONE | NONE | NONE | NONE | (2) 1M x 9 | (2) 1M x 9 |

| DRAM SWITCH CONFIGURATION | | | |
|---------------------------|-------|-------|-------|
| Size | SW1/2 | SW1/3 | SW1/4 |
| 512KB | On | On | Off |
| 640KB | On | Off | On |
| 1MB | On | Off | Off |
| 2MB | Off | Off | On |
| 4MB | Off | Off | Off |