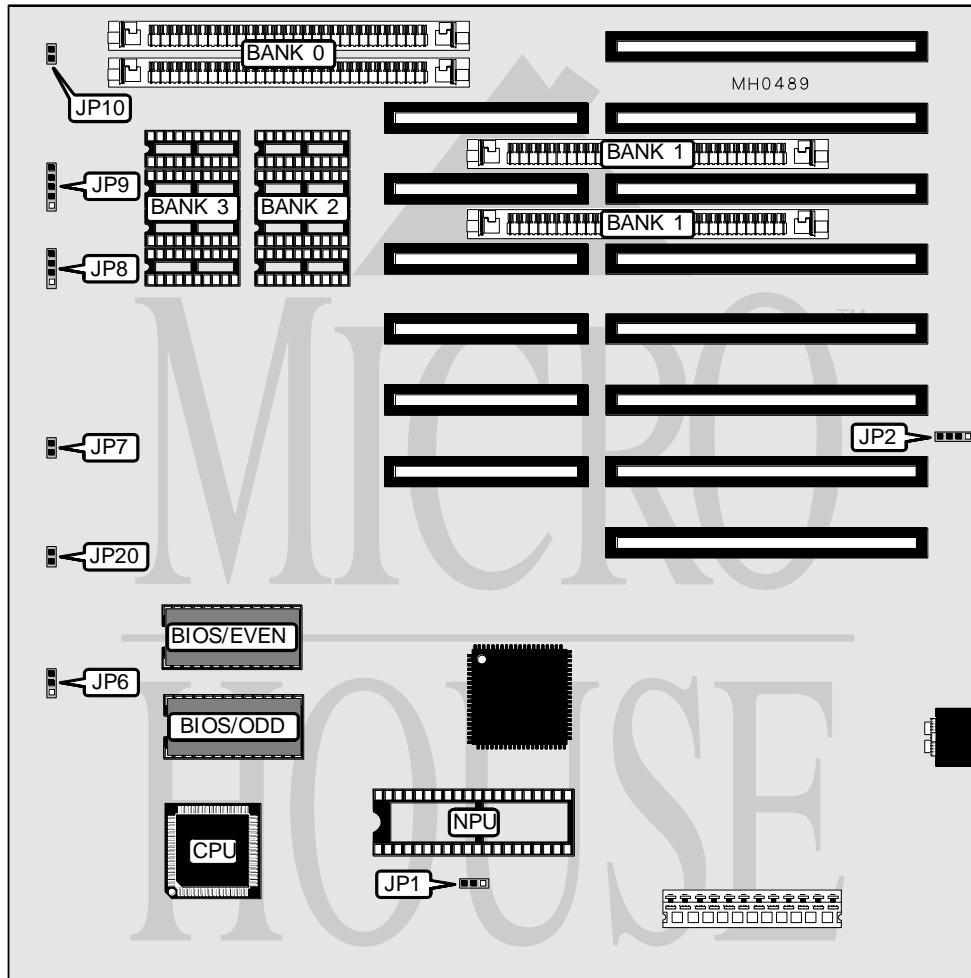


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YL - A 1 6

Processor	80286
Processor Speed	16/20MHz
Chip Set	Headland
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Power LED & keylock	JP9
Reset switch	JP7	Turbo LED	JP10
Speaker	JP8	Turbo switch	JP20

Continued on next page . . .

UNIDENTIFIED

YL - A 16

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	JP1	pins 1 & 2 closed
NPU mode select asynchronous with CPU	JP1	pins 2 & 3 closed
í Monitor type select color	JP6	pins 1 & 2 closed
Monitor type select monochrome	JP6	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	NONE	NONE	(4) 44256	NONE
512KB	(2) 256K x 9	NONE	NONE	NONE
640KB	NONE	NONE	(4) 44256	(4) 4464
1MB	NONE	NONE	(4) 44256	(4) 44256
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 1M x 9	NONE	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE

Note: SIMM and DIP memory may not be used concurrently.