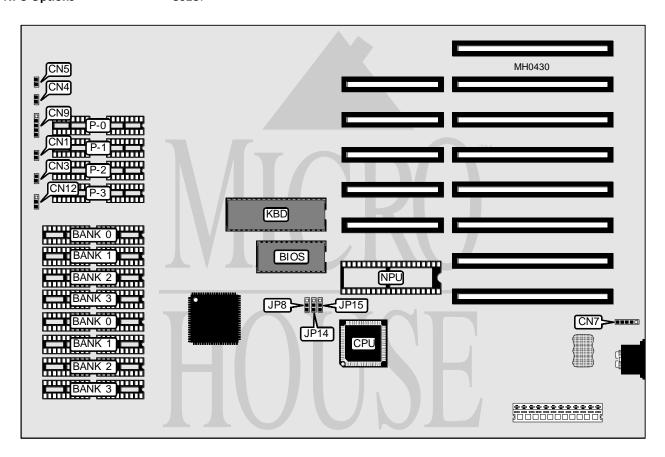
HEDAKA

HED-986 (DIP VERSION)

Processor 80286 **Processor Speed** 12/16/20MHz Hedaka **Chip Set** Max. Onboard DRAM 2MB Cache None

BIOS AMI **Dimensions** 330mm x 218mm

I/O Options None **NPU Options** 80287



CONNECTIONS					
Purpose	Location	Purpose	Location		
Turbo LED	CN1	Auxiliary keyboard	CN7		
Turbo switch	CN3	Power LED & keylock	CN9		
Speaker	CN4	Speed display	CN12		
Reset switch	CN5				

Continued on next page . . .

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HED-986 (DIP VERSION)

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USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í Monitor type select color	JP8	pins 2 & 3 closed			
Monitor type select monochrome	JP8	pins 1 & 2 closed			
í NPU clock select internal oscillator	JP15	pins 2 & 3 closed			
NPU clock select external oscillator	JP15	pins 1 & 2 closed			
í NPU speed select OSC/1	JP14	pins 1 & 2 closed			
NPU speed select OSC/3	JP14	pins 2 & 3 closed			

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3		
512KB	(4) 44256	NONE	NONE	NONE		
1MB	(4) 44256	(4) 44256	NONE	NONE		
1.5MB	(4) 44256	(4) 44256	(4) 44256	NONE		
2MB	(4) 44256	(4) 44256	(4) 44256	(4) 44256		

DRAM PARITY CONFIGURATION						
Size	P-0	P-1	P-2	P-3		
512KB	(2) 41256	NONE	NONE	NONE		
1MB	(2) 41256	(2) 41256	NONE	NONE		
1.5MB	(2) 41256	(2) 41256	(2) 41256	NONE		
2MB	(2) 41256	(2) 41256	(2) 41256	(2) 41256		