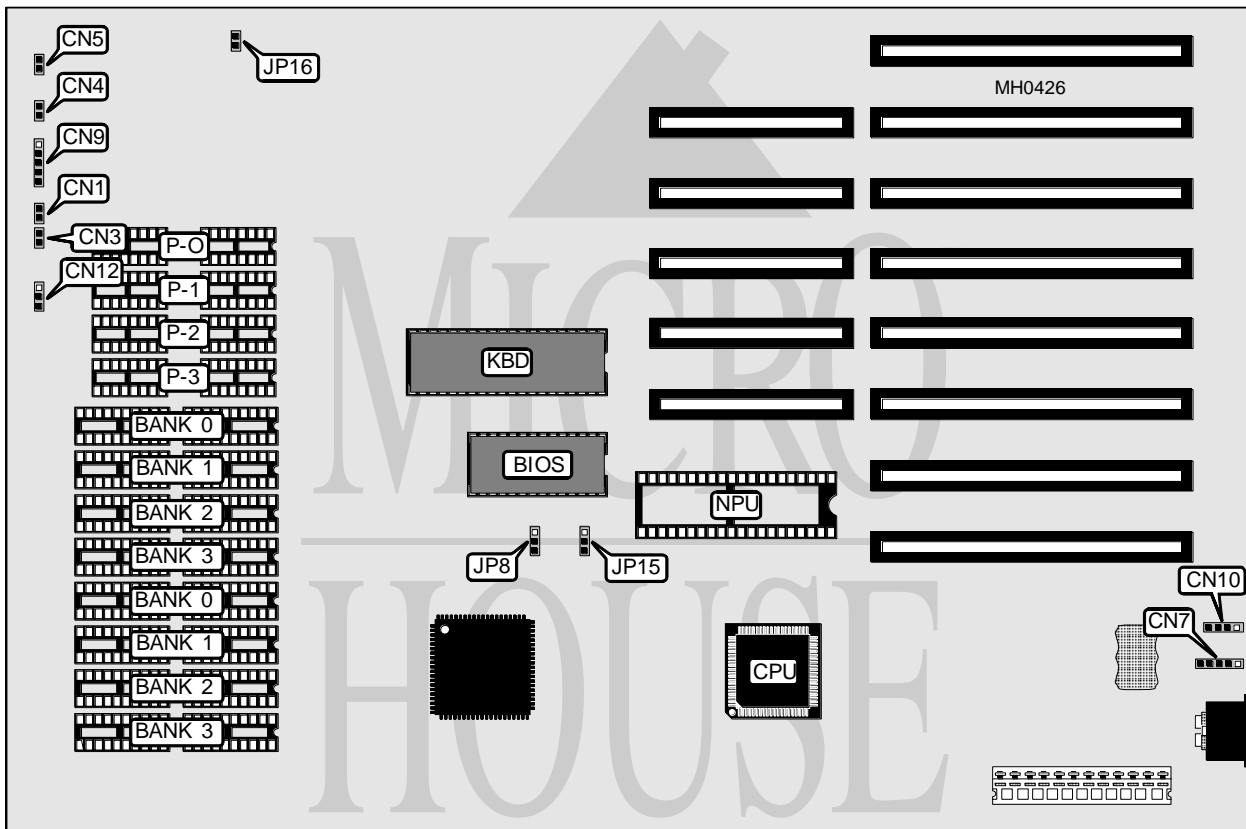


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M - 2 0 5

Processor	80286
Processor Speed	12/16/20MHz
Chip Set	C & T
Max. Onboard DRAM	2MB
Cache	None
BIOS	AMI
Dimensions	305mm x 217mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	CN1	Auxiliary keyboard	CN7
Turbo switch	CN3	Power LED & keylock	CN9
Speaker	CN4	External battery	CN10
Reset switch	CN5	Speed display	CN12

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP6	pins 1 & 2 closed
CMOS memory clear	JP6	pins 2 & 3 closed
í Monitor type select color	JP8	pins 2 & 3 closed
Monitor type select monochrome	JP8	pins 1 & 2 closed
í NPU mode select synchronous with CPU	JP15	pins 2 & 3 closed
NPU mode select asynchronous with CPU	JP15	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(4) 44256	NONE	NONE	NONE
1MB	(4) 44256	(4) 44256	NONE	NONE
1.5MB	(4) 44256	(4) 44256	(4) 44256	NONE
2MB	(4) 44256	(4) 44256	(4) 44256	(4) 44256

DRAM PARITY CONFIGURATION				
Size	P 0	P 1	P 2	P 3
512KB	(2) 41256	NONE	NONE	NONE
1MB	(2) 41256	(2) 41256	NONE	NONE
1.5MB	(2) 41256	(2) 41256	(2) 41256	NONE
2MB	(2) 41256	(2) 41256	(2) 41256	(2) 41256