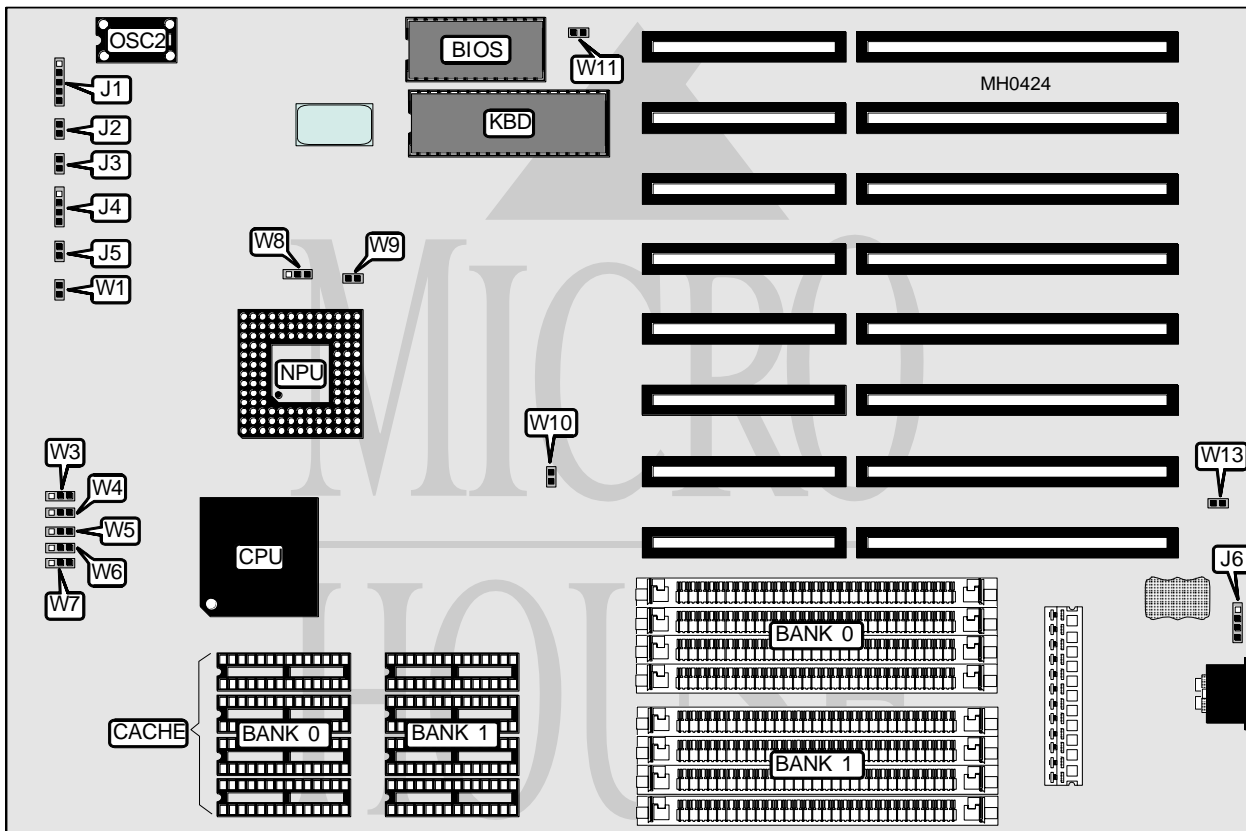


ADVANCED INTEGRATION RESEARCH, INC.

386US-40/33/25

Processor	80386DX
Processor Speed	25/33/40MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Speaker	J4
Turbo switch	J2	Reset switch	J5
Turbo LED	J3	External battery	J6

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	W11	Closed
Monitor type select monochrome	W11	Open
í Battery select internal	W13	Closed
Battery select external	W13	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

Note: Pins designated should be in the closed position.

CACHE JUMPER CONFIGURATION					
Size	W3	W4	W5	W6	W7
32KB	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
64KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
128KB	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

NPU CONFIGURATION		
Size	W8	W9
Synchronous	pins 1 & 2 closed	Open
Asynchronous	pins 2 & 3 closed	Closed

NPU CONFIGURATION		
Setting	W1	W10
Disabled	Open	Open
Enabled	Closed	Closed