KOUWELL ELECTRONIC CORPORATION

FR-486

80486SX/80487SX/80486DX **Processor**

Processor Speed 20/25/33/50MHz

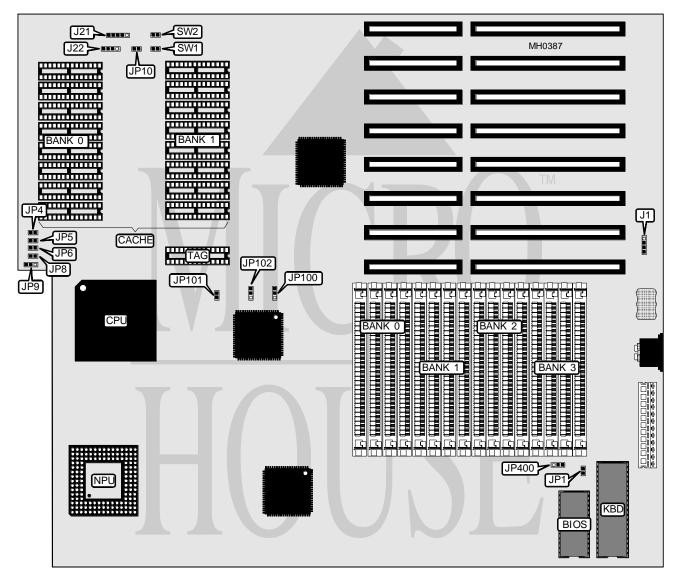
OPTI **Chip Set** Max. Onboard DRAM 64MB

Cache 64/128/256/512KB

BIOS AMI

355mm x 304mm **Dimensions**

I/O Options None **NPU Options** 4167



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| CONNECTIONS | | | |
|---------------------|----------|--------------|----------|
| Purpose | Location | Purpose | Location |
| External battery | J1 | Turbo LED | JP10 |
| Power LED & keylock | J21 | Reset switch | SW1 |
| Speaker | J22 | Turbo switch | SW2 |

| USER CONFIGURABLE SETTINGS | | | | |
|--|--------|-------------------|--|--|
| Function | Jumper | Position | | |
| í Monitor type select color | JP1 | Open | | |
| Monitor type select monochrome | JP1 | Closed | | |
| í Power good signal detect from power supply | JP400 | pins 1 & 2 closed | | |
| Power good signal detect from board | JP400 | pins 2 & 3 closed | | |

| DRAM CONFIGURATION | | | | |
|--------------------|--------------|--------------|------------|------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (4) 256K x 9 | NONE | NONE | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 | NONE | NONE |
| 4MB | (4) 1M x 9 | NONE | NONE | NONE |
| 5MB | (4) 256K x 9 | (4) 1M x 9 | NONE | NONE |
| 6MB | (4) 256K x 9 | (4) 256K x 9 | (4) 1M x 9 | NONE |
| 8MB | (4) 1M x 9 | (4) 1M x 9 | NONE | NONE |
| 9MB | (4) 256K x 9 | (4) 1M x 9 | (4) 1M x 9 | NONE |
| 10MB | (4) 256K x 9 | (4) 256K x 9 | (4) 1M x 9 | (4) 1M x 9 |
| 12MB | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 | NONE |
| 13MB | (4) 256K x 9 | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE | NONE | NONE |
| 20MB | (4) 1M x 9 | (4) 4M x 9 | NONE | NONE |
| 24MB | (4) 1M x 9 | (4) 1M x 9 | (4) 4M x 9 | NONE |
| 28MB | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 | (4) 4M x 9 |
| 32MB | (4) 4M x 9 | (4) 4M x 9 | NONE | NONE |
| 36MB | (4) 1M x 9 | (4) 4M x 9 | (4) 4M x 9 | NONE |
| 40MB | (4) 1M x 9 | (4) 1M x 9 | (4) 4M x 9 | (4) 4M x 9 |
| 48MB | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 | NONE |
| 52MB | (4) 1M x 9 | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 |
| 64MB | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 |

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| CACHE CONFIGURATION | | | | |
|---------------------|-------------|-------------|-------------|--|
| Size | Bank 0 | Bank 1 | TAG | |
| 64KB | (8) 8K x 8 | NONE | (1) 8K x 8 | |
| 128KB | (8) 8K x 8 | (8) 8K x 8 | (1) 8K x 8 | |
| 256KB | (8) 32K x 8 | NONE | (1) 32K x 8 | |
| 512KB | (8) 32K x 8 | (8) 32K x 8 | (1) 32K x 8 | |

| CACHE JUMPER CONFIGURATION | | | | | |
|---|--------|--------|--------|--------|-------|
| Size | JP4 | JP5 | JP6 | JP8 | JP9 |
| 64KB | Open | Open | Open | Open | 2 & 3 |
| 128KB | Open | Open | Closed | Closed | 2 & 3 |
| 256KB | Open | Closed | Closed | Closed | 2 & 3 |
| 512KB | Closed | Closed | Closed | Closed | 1 & 2 |
| Note: Pins designated should be in the closed position. | | | | | |

| CPU TYPE CONFIGURATION | | | | |
|------------------------|-------------------|--------|-------------------|--|
| Type JP100 JP101 JP102 | | | | |
| 80486DX | pins 1 & 2 closed | Closed | pins 2 & 3 closed | |
| 80487SX | pins 1 & 2 closed | Closed | pins 1 & 2 closed | |
| 80486SX | pins 2 & 3 closed | Open | Open | |