KOUWELL ELECTRONIC CORPORATION
K W - 2 4 8 A

Processor	80286
Processor Speed	12/16MHz
Chip Set	Headland
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI/Award
Dimensions	218mm x 218mm
I/O Options	None
NPU Options	80287
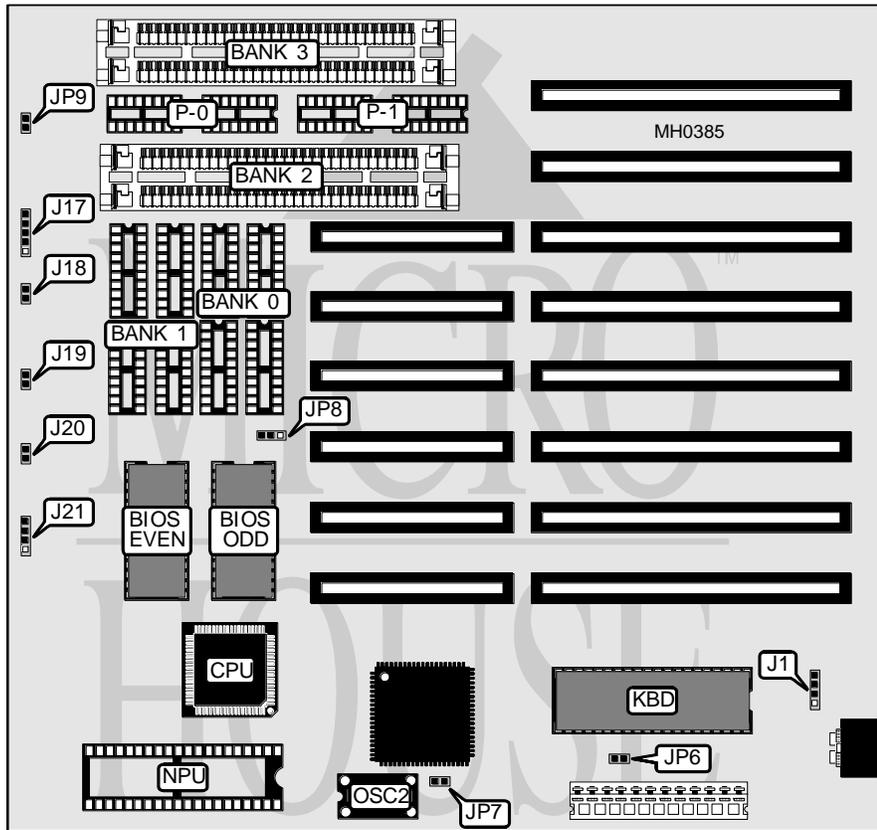
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J19
Power LED & keylock	J17	Reset switch	J20
Turbo LED	J18	Speaker	J21

KOUWELL ELECTRONIC CORPORATION
KW-248A

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP6	Open
í Bus speed select synchronous with CPU clock	JP7	Closed
Bus speed select synchronous with clock at OSC2	JP7	Open
í BIOS type select 27256	JP8	pins 1 & 2 closed
BIOS type select 27128	JP8	pins 2 & 3 closed
í Monitor type select monochrome	JP9	Open
Monitor type select color	JP9	Closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
640KB	NONE	NONE	(4) 4464	(2) 4164	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
1MB	NONE	NONE	(4) 44256	(2) 41256	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	NONE	NONE	NONE	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1MBx 9	NONE
2.5MB	NONE	NONE	(4) 44256	(2) 41256	(2) 1MBx 9	NONE
2.5MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 1MBx 9
4MB	NONE	NONE	NONE	NONE	(2) 1MBx 9	(2) 1MBx 9