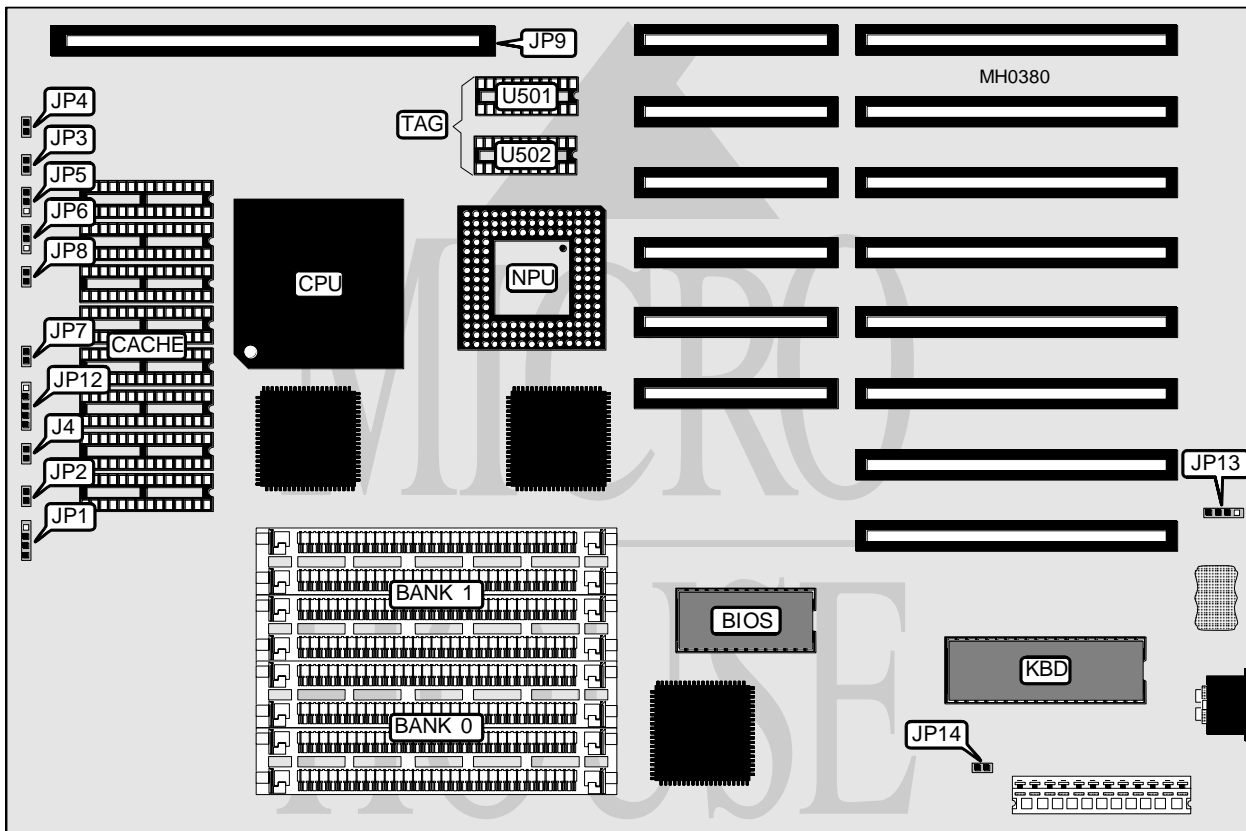


# UNIDENTIFIED CY - 601

<b>Processor</b>	80486DX
<b>Processor Speed</b>	25/33MHz
<b>Chip Set</b>	ETEQ
<b>Max. Onboard DRAM</b>	40MB
<b>Cache</b>	64/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	331mm x 218mm
<b>I/O Options</b>	32-bit external memory card slot
<b>NPU Options</b>	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J4	32-bit external memory card	JP9
Speaker	JP1	Power LED & keylock	JP12
Turbo LED	JP2	External battery	JP13
Turbo switch	JP7		

Note: Location of pin 1s are unidentified.

Continued on next page . . .

# UNIDENTIFIED

## CY-601

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal	JP13	pins 1 & 2 closed
Battery select external	JP13	Closed
í Monitor type select monochrome	JP14	Open
Monitor type select color	JP14	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 1M x 9	(4) 256K x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
37MB	(4) 256K x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
49MB	(4) 256K x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

Note: Banks 2 and 3 are used when a 32-bit external memory card is installed at JP9.

CACHE CONFIGURATION			
Size	Bank 0	TAG (U501)	TAG (U502)
64KB	(8) 8K x 8	(1) 16K x 4	(1) 16K x 4
256KB	(8) 32K x 8	(1) 16K x 4	(1) 16K x 4

CACHE JUMPER CONFIGURATION					
Size	JP3	JP4	JP5	JP6	JP8
64KB	Open	Open	2 & 3	2 & 3	Open
256KB	Closed	Closed	1 & 2	1 & 2	Closed

Note: Pins designated should be in the closed position.

