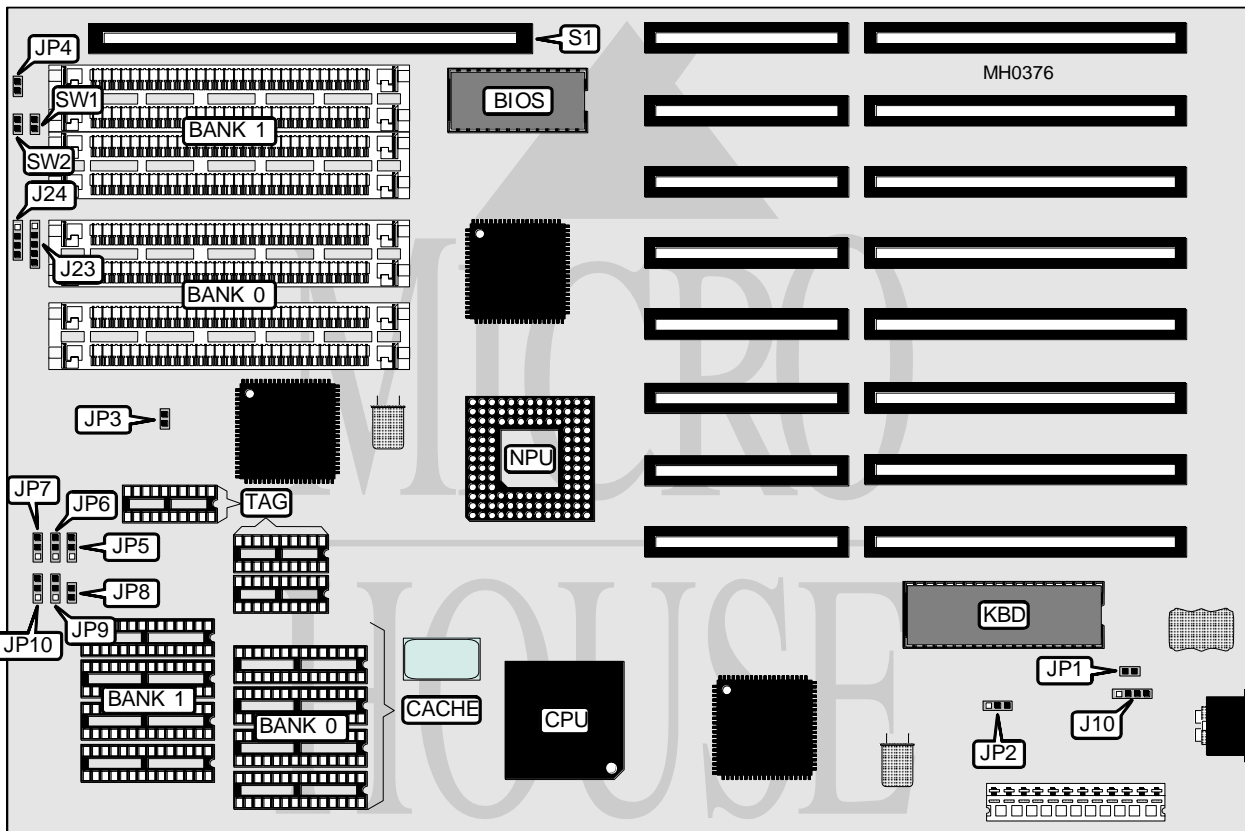


# KOUWELL ELECTRONIC CORPORATION

## K W - 3 2 6

<b>Processor</b>	80386DX
<b>Processor Speed</b>	33/40MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	32/64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	331mm x 218mm
<b>I/O Options</b>	32-bit external memory card slot
<b>NPU Options</b>	80387/3167



### CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J10	32-bit external memory card	S1
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
Turbo LED	JP4		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP1	Open
Monitor type select color	JP1	Closed
í CMOS memory normal operation (internal battery)	JP2	pins 1 & 2 closed
CMOS memory clear	JP2	pins 2 & 3 closed
CMOS memory normal operation (external battery)	JP2	Open
í Bus speed select ATCLK/8	JP3	Closed
Bus speed select ATCLK/6	JP3	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

Note: Banks 2 and 3 are used when a 32-bit external memory card is installed at S1.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(3) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(3) 16K x 4
128KB	(4) 32K x 8	NONE	(3) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(3) 16K x 4

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	1 & 2	1 & 2	1 & 2	Open	2 & 3	2 & 3
64KB	2 & 3	1 & 2	1 & 2	Open	2 & 3	1 & 2
128KB	2 & 3	1 & 2	2 & 3	Closed	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.