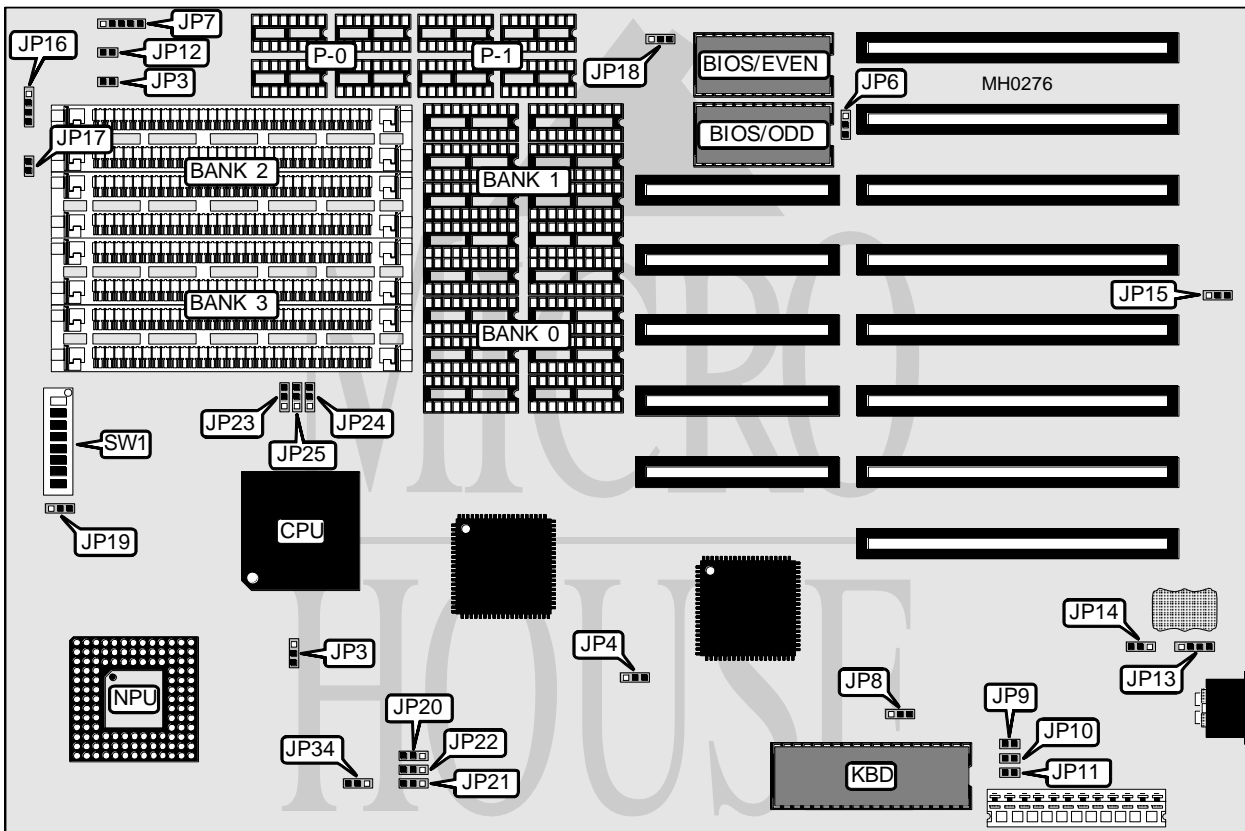


# ADVANCED COMPUTER TECHNOLOGY, LTD.

## T 3 8 6 - 2 5

**Processor** 80386DX  
**Processor Speed** 25MHz  
**Chip Set** ACT  
**Max. Onboard DRAM** 16MB  
**Cache** None  
**BIOS** AMI  
**Dimensions** 330mm x 218mm  
**I/O Options** None  
**NPU Options** 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP3	External battery	JP13
Power LED & Keylock	JP7	Speaker	JP16
Reset switch	JP12	Turbo switch	JP17

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í BIOS type select 27512	JP6	pins 1 & 2 closed
BIOS type select 27256	JP6	pins 2 & 3 closed
í Monitor type select color	JP8	pins 2 & 3 closed
Monitor type select monochrome	JP8	pins 1 & 2 closed
í Battery select internal	JP14	pins 2 & 3 closed
Battery select external	JP14	pins 1 & 2 closed
í Power good signal detect from power supply	JP15	pins 1 & 2 closed
Power good signal detect from board	JP15	pins 2 & 3 closed
í Refresh timing select normal	JP18	pins 1 & 2 closed
Refresh timing select fast	JP18	pins 2 & 3 closed
í Parity check enabled	JP19	pins 1 & 2 closed
Parity check disabled	JP19	pins 2 & 3 closed
í Page mode select normal 1, 2 wait state	JP20	pins 1 & 2 closed
Page mode select page interleave	JP20	pins 2 & 3 closed
í SIMM wait state select 2 wait states (non-page mode)	JP21	pins 2 & 3 closed
SIMM wait state select 1 wait state (non-page mode)	JP21	pins 1 & 2 closed
í DIP RAM wait state select 2 wait states (non-page mode)	JP22	pins 2 & 3 closed
DIP RAM wait state select 1 wait state (non-page mode)	JP22	pins 1 & 2 closed
í Interleave factor banks 0-3 four way interleave disabled	SW1/3	Off
Interleave factor banks 0-3 four way interleave enabled	SW1/3	On
í Interleave factor banks 2 and 3 interleave disabled	SW1/4	Off
Interleave factor banks 2 and 3 two way interleave enabled	SW1/4	On
í Interleave factor banks 0 and 1 interleave disabled	SW1/5	Off
Interleave factor banks 0 and 1 two way interleave enabled	SW1/5	On
Note: SW1/switch 5 should be open if 1MB is installed, if memory is greater it should be closed. SW1/switch 4 should be closed if 10MB is installed. SW1/switch 3 should be closed if 4MB is installed. JP20 pins 1 & 2 must be closed if 4M x 9 SIMMs are used.		

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3	P-0	P-1
1MB	(8) 44256	NONE	NONE	NONE	(4) 41256	NONE
2MB	(8) 44256	(8) 44256	NONE	NONE	(4) 41256	(4) 41256
3MB	(8) 44256	(8) 44256	(4) 256K x 9	NONE	(4) 41256	(4) 41256
4MB	(8) 44256	(8) 44256	(4) 256K x 9	(4) 256K x 9	(4) 41256	(4) 41256
6MB	(8) 44256	(8) 44256	(4) 1M x 9	NONE	(4) 41256	(4) 41256
10MB	(8) 44256	(8) 44256	(4) 1M x 9	(4) 1M x 9	(4) 41256	(4) 41256
16MB	NONE	NONE	(4) 4M x 9	(4) 4M x 9	NONE	NONE

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DRAM JUMPER/SWITCH CONFIGURATION				
Size	SW1/6	SW1/7	SW1/8	JP23
1MB	Off	Off	Off	pins 2 & 3 closed
2MB	Off	Off	On	pins 2 & 3 closed
3MB	Off	On	Off	pins 2 & 3 closed
4MB	Off	On	On	pins 2 & 3 closed
6MB	On	On	Off	pins 2 & 3 closed
10MB	On	On	On	pins 2 & 3 closed
16MB	Off	Off	Off	pins 1 & 2 closed

DRAM REFRESH RATE CONFIGURATION		
Size	SW1/1	SW1/2
Refreshes per hold cycle select 1	Off	Off
Refreshes per hold cycle select 2	Off	On
Refreshes per hold cycle select 4	On	Off
Refreshes per hold cycle select 8	On	On

NPU TYPE CONFIGURATION		
Type	JP2	JP4
80387	pins 1 & 2 closed	pins 2 & 3 closed
3167	pins 2 & 3 closed	pins 1 & 2 closed