## ADVANCED COMPUTER TECHNOLOGY, LTD. LAT-25/4M, LAT-16/4M, LAT-12/4M

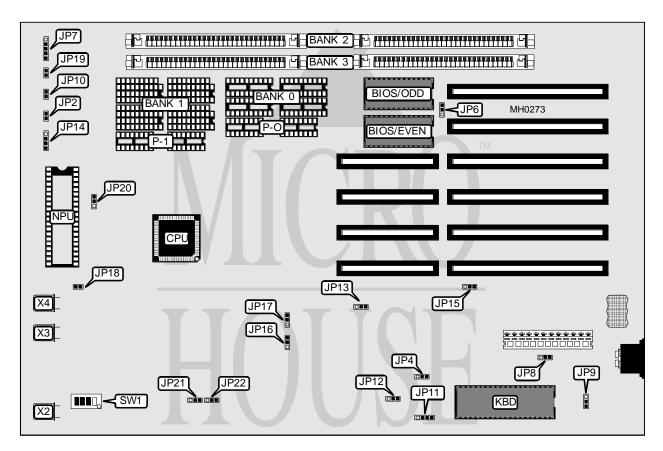
**Processor** 80286

**Processor Speed** 12/16/25MHz

**Chip Set** ACT Max. Onboard DRAM 4MB Cache None **BIOS** AMI

330mm x 218mm **Dimensions** 

I/O Options None **NPU Options** 80287



CONNECTIONS					
Purpose	Location	Purpose	Location		
Turbo LED	JP2	External battery	JP11		
Power LED & Keylock	JP7	Speaker	JP14		
Reset switch	JP10	Turbo switch	JP19		

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USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
í Factory configured - do not alter	JP4	N/A		
í BIOS type select 27256	JP6	pins 1 & 2 closed		
ROM type select 27512	JP6	pins 2 & 3 closed		
í Factory configured - do not alter	JP8	N/A		
í Monitor type select color	JP9	pins 2 & 3 closed		
Monitor type select monochrome	JP9	pins 1 & 2 closed		
í Battery select internal	JP12	pins 2 & 3 closed		
Battery select external	JP12	pins 1 & 2 closed		
í Power good signal detect from power supply	JP13	pins 1 & 2 closed		
Power good signal detect from board	JP13	pins 2 & 3 closed		
í Factory configured - do not alter	JP15	N/A		
í Factory configured - do not alter	JP16	N/A		
í Bus speed select 8MHz	JP17	pins 1 & 2 closed		
Bus speed select 6MHz	JP17	pins 2 & 3 closed		
í Factory configured - do not alter	JP18	N/A		
í Parity check enabled	JP20	pins 1 & 2 closed		
Parity check disabled	JP20	pins 2 & 3 closed		
í Factory configured - do not alter	JP21	N/A		
í Factory configured - do not alter	JP22	N/A		
í RAM speed select 0 wait states	SW1/4	Off		
RAM speed select 1 wait state	SW1/4	On		
Note: SW1/4 must be on for the LAT-25/4M model.				

	DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	P-0	P-1
512KB	(4) 44256	NONE	NONE	NONE	(2) 41256	NONE
512KB	NONE	NONE	(2) 256K x 9	NONE	NONE	NONE
640KB	(4) 44256	(4) 4464	NONE	NONE	(2) 41256	(2) 4164
640KB	NONE	(4) 4464	(2) 256K x 9	NONE	NONE	(2) 4164
1MB	(4) 44256	(4) 44256	NONE	NONE	(2) 41256	(2) 41256
1MB	(4) 44256	NONE	NONE	(2) 256K x 9	(2) 41256	NONE
1MB	NONE	NONE	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	NONE	NONE	(2) 1MBx 9	NONE	NONE	NONE
4MB	NONE	NONE	(2) 1MBx 9	(2) 1MBx 9	NONE	NONE

DRAM SWITCH CONFIGURATION					
Size	SW1/1	SW1/2	SW1/3		
512KB	On	On	On		
640KB	On	Off	Off		
1MB	On	Off	On		
2MB	Off	On	On		
4MB	Off	Off	On		