## PACKARD BELL

## PB686 SERIES

## (AXCEL I/LEGEND I/PACK-MATE I)

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
1/O Options
NPU Options

80286
12 MHz
C \& T
1MB
None
Phoenix
$330 \mathrm{~mm} \times 218 \mathrm{~mm}$
Serial ports (2), parallel port, floppy interface
80287


| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Speaker | J2 | Floppy interface | J9 |
| Turbo LED | J3 | Parallel port | P1 |
| Reset switch | J 4 | Serial port (25-pin) | P2 |
| External battery | J 5 | Serial port (9-pin) | P3 |

Continued on next page . .

## PACKARD BELL

## PB686 SERIES

(AXCEL I/LEGEND I/PACK-MATE I)

| USER CONFIGURABLE SETTINGS |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Function |  |  |  | Jumper | Position |
| í Factory configured - do not alter | JP1 | pins 1 \& 2 closed |  |  |  |
| í Monitor type select color | JP2 | pins 2 \& 3 closed |  |  |  |
| Monitor type select monochrome | JP2 | pins 1 \& 2 closed |  |  |  |


| DRAM CONFIGURATION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Size | Piggyback board (PB1) | Bank 0 (BO) | Parity 0 (PO) | Bank 1 (B1) | Parity 1 (P1) |
| 640 KB | $(4) 4464+(2) 4164$ | $(4) 44256$ | $(2) 41256$ | NONE | NONE |
| 1 MB | NONE | $(4) 44256$ | $(2) 41256$ | $(4) 44256$ | $(2) 41256$ |


| DRAM JUMPER CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | JP5 |  |
| 640 KB | Closed |  |
| 1 MB | Open |  |


| PARALLEL PORT CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Port | JP23 | JP27 |
| LPT1 | pins 2 \& 3 closed | pins 2 \& 4 closed |
| LPT2 | pins 1 \& 2 closed | pins 1 \& 3 closed |


| SERIAL PORT CONFIGURATION |  |  |  |
| :---: | :---: | :---: | :---: |
| P2 | P3 | JP24 | JP33 |
| COM1 | COM2 | pins 1 \& 3, 2 \& 4 closed | pins 1 \& 3, 2 \& 4 closed |
| COM2 | COM1 | pins 1 \& 2, 3 \& 4 closed | pins 1 \& 2, 3 \& 4 closed |
| COM1 | Disabled | pins 2 \& 4 closed | pins $1 \& 3$ closed |
| COM2 | Disabled | pins 1 \& 2 closed | pins 1 \& 2 closed |
| Disabled | COM 1 | pins 3 4 closed | pins 3 \& 4 closed |
| Disabled | COM2 | pins 1 \& 3 closed | pins 2 \& 4 closed |

