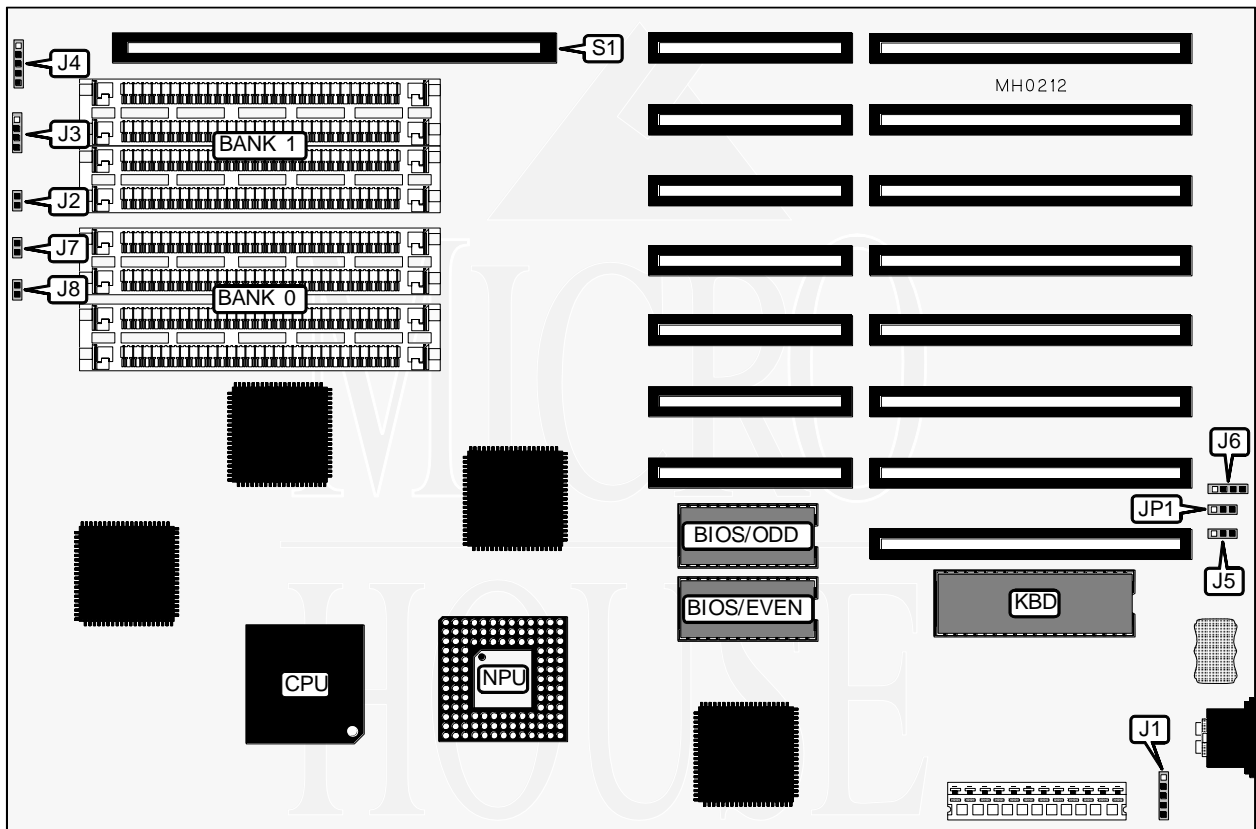


CACHING TECH CORPORATION

P 3 8 6 S - 2 5

Processor	80386DX
Processor Speed	25MHz
Chip Set	Unidentified
Max. Onboard DRAM	8MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Auxiliary keyboard	J1	External battery	J6
Reset switch	J2	Turbo switch	J7
Speaker	J3	Turbo LED	J8
Power LED & keylock	J4	32-bit external memory card	S1

Continued on next page . . .

CACHING TECH CORPORATION

P 3 8 6 S - 2 5

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	J5	pins 1 & 2 closed
Monitor type select monochrome	J5	pins 2 & 3 closed
í CMOS memory normal operation (internal battery)	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
CMOS memory normal operation (external battery)	JP1	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
Note: Banks 2 & 3 are used when a 32-bit external memory card is installed at S1.				