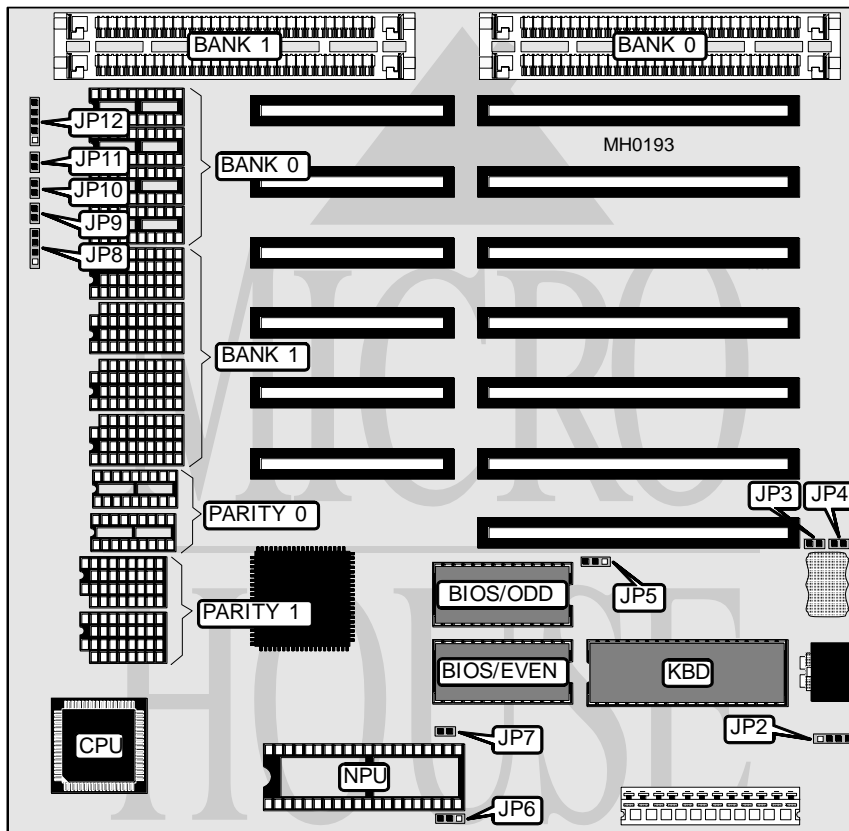


JPN CORPORATION 286LS

Processor	80286
Processor Speed	12/16MHz
Chip Set	Headland
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo switch	JP10
Speaker	JP8	Reset switch	JP11
Turbo LED	JP9	Power LED & keylock	JP12

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
í CMOS memory normal operation	JP4	Open
CMOS memory clear	JP4	Closed
í BIOS type select 27256	JP5	pins 2 & 3 closed
BIOS type select 27128	JP5	pins 1 & 2 closed
í NPU clock speed select 10.4MHz	JP6	pins 1 & 2 closed
NPU clock speed select 4.8MHz	JP6	pins 2 & 3 closed
í NPU mode select synchronous with CPU	JP7	Closed
NPU mode select asynchronous with CPU	JP7	Open

DRAM CONFIGURATION						
Size	Bank 0 (D)	Bank 1 (D)	Parity 0 (D)	Parity 1 (D)	Bank 0 (S)	Bank 1 (S)
512KB	(4) 44256	NONE	(2) 41256	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(4) 4464	(2) 41256	(2) 4164	NONE	NONE
1MB	(4) 44256	(4) 44256	(2) 41256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(4) 1M x 9	(2) 1M x 9

Notes: Model 286LS is available with either SIMM or SIPP sockets. DIP and SIMM/SIPP memory must be used separately.