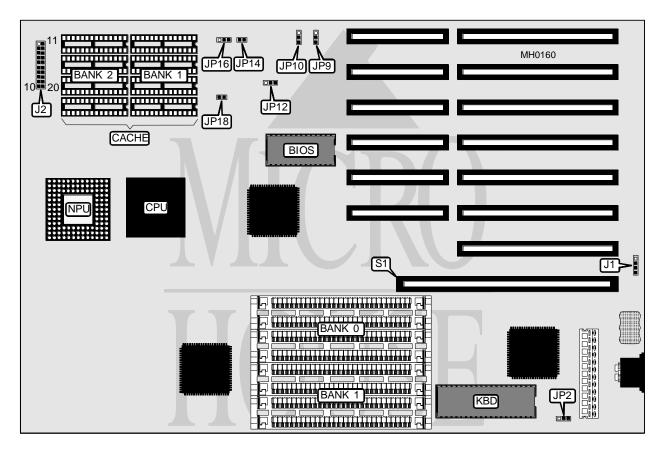
## AREA ELECTRONICS SYSTEMS, INC. ETEQ-386

Processor 80386DX **Processor Speed** 25/33MHz **Chip Set ETEQ** Max. Onboard DRAM 32MB Cache 64/128KB **BIOS** AMI/MR

330mm x 218mm **Dimensions** 

I/O Options 32-bit external memory card slot

**NPU Options** 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J2 pins15 - 17
Power LED & keylock	J2 pins 1 - 5	Reset switch	J2 pins 19 & 20
Speaker	J2 pins 7 - 10	32-bit external memory card	S1
Turbo LED	J2 pins 12 & 13		

Continued on next page . . .

## AREA ELECTRONICS SYSTEMS, INC. ETEQ-386

. . . continued from previous page

USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Battery select internal	J1	pins 2 & 3 closed	
Battery select external	J1	Closed	
í Monitor type select color	JP2	pins 1 & 2 closed	
Monitor type select monochrome	JP2	pins 2 & 3 closed	

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	
1MB	(4) 256K x 9	NONE	
2MB	(4) 256K x 9	(4) 256K x 9	
4MB	(4) 1M x 9	NONE	
5MB	(4) 1 M x 9	(4) 256K x 9	
8MB	(4) 1M x 9	(4) 1M x 9	
16MB	(4) 4M x 9	NONE	
17MB	(4) 4M x 9	(4) 256K x 9	
20MB	(4) 4M x 9	(4) 1M x 9	
32MB	(4) 4M x 9	(4) 4M x 9	

CACHE JUMPER CONFIGURATION						
Size	JP9	JP10	JP12	JP14	JP16	JP18
64KB	Open	1 & 2	1 & 2	Open	2 & 3	Open
128KB	2 & 3	Open	2 & 3	Closed	Open	Closed
Note: Pins designated should be in the closed position.						

CACHE CONFIGURATION			
Size	Bank 1	Bank 2	
64KB	(4) 8K x 8	(4) 8K x 8	
128KB	(4) 32K x 8	NONE	