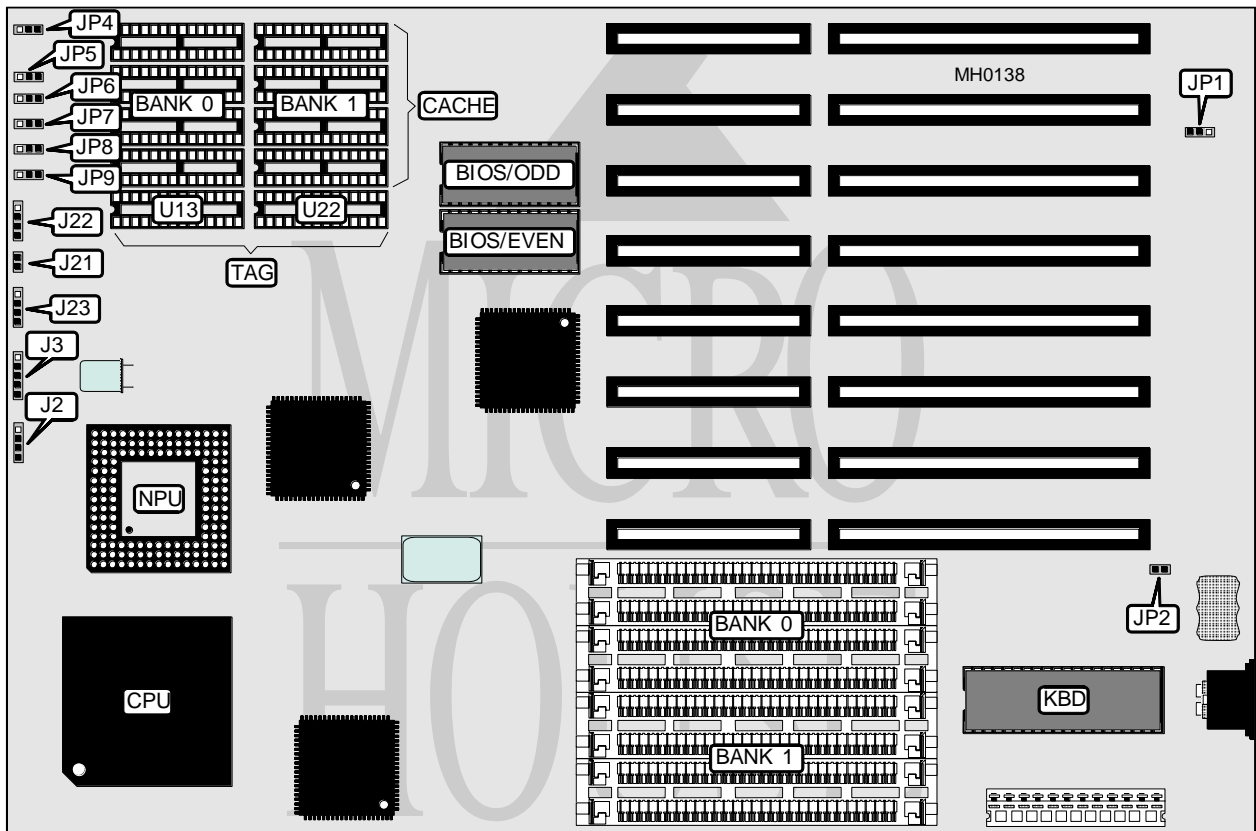


PIONEER COMPUTER, INC. VANTAGE 486DX CACHE

Processor	80486DX
Processor Speed	25/33MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	128/256KB
BIOS	AMI/Award
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo LED	J22
Power LED & keylock	J3	Speaker	J23
Reset switch	J21		

Continued on next page . . .

PIONEER COMPUTER, INC.
VANTAGE 486 25/33

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP2	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U13)	TAG (U22)
128KB	(4) 32K x 8	NONE	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION						
Size	JP4	JP5	JP6	JP7	JP8	JP9
128KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.