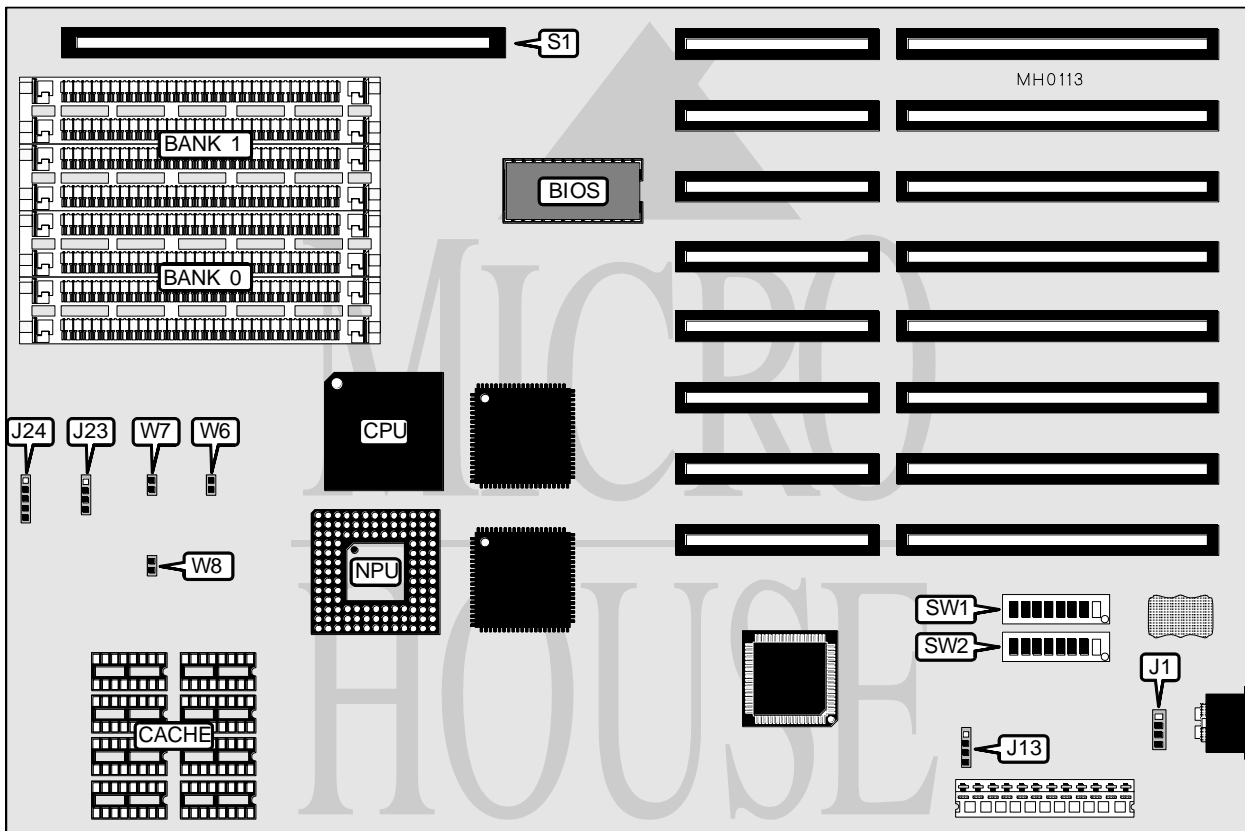


MICRONICS COMPUTERS, INC.

BABY GEMINI-386DX

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Micronics
Max. Onboard DRAM	32MB
Cache	64KB
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	32-Bit external memory card slot
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External keyboard	J1	32-Bit external memory card	S1
External battery	J13	Turbo switch	W6
Speaker	J23	Turbo LED	W7
Power LED & keylock	J24	Reset switch	W8

Continued on next page . . .

MICRONICS COMPUTERS, INC.

BABY GEMINI-386DX

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Factory configured - do not alter	SW1/6	Off
í Factory configured - do not alter	SW1/7	Off
í Factory configured - do not alter	SW2/1	Off
í Factory configured - do not alter	SW2/3	Off
í Factory configured - do not alter	SW2/4	Off
í Factory configured - do not alter	SW2/5	Off
í Factory configured - do not alter	SW2/6	Off
í Factory configured - do not alter	SW2/7	Off
í Factory configured - do not alter	SW2/8	Off

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

DRAM SWITCH CONFIGURATION					
Size	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5
1MB	On	On	On	On	On
2MB	Off	On	On	On	On
4MB	On	On	Off	On	On
5MB	On	On	Off	Off	On
8MB	Off	On	Off	On	On
16MB	On	On	Off	On	Off
20MB	On	On	Off	Off	Off
32MB	Off	On	Off	On	Off

CACHE CONFIGURATION	
Size	Bank 0
64KB	(8) 8K x 8

CPU SPEED CONFIGURATION		
Speed	SW1/8	SW2/2
33MHz	On	On
40MHz	Off	Off