MICRONICS COMPUTERS, INC.
B A B Y   G E M I N I - 4 8 6 S X

Processor	80486SX
Processor Speed	25MHz
Chip Set	Micronics
Max. Onboard DRAM	32MB
Cache	64/128/256KB (on external cache/NPU board)
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit external cache/4167 card
NPU Options	4167(on external cache/NPU board)

CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J19	Reset switch	W4
Speaker	J22	Turbo switch	W5
Power LED & keylock	J23	Turbo LED	W6
32-bit external cache/NPU card	S1		

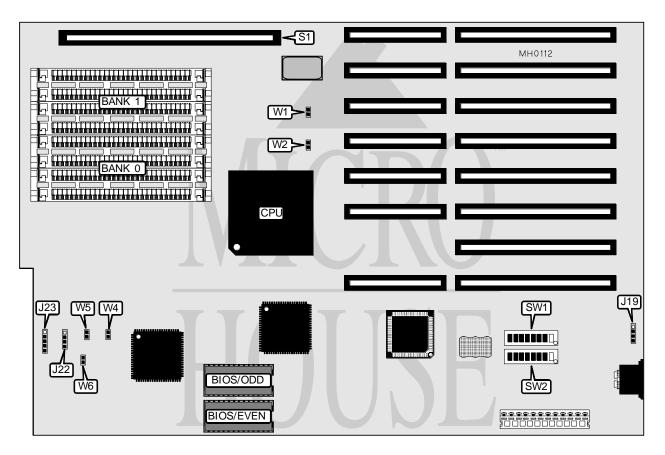*Continued on next page . . .*

MICRONICS COMPUTERS, INC.
BABY GEMINI-486SX

USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Factory configured - do not alter	SW1/1	Off
í Factory configured - do not alter	SW1/2	Off
í Factory configured - do not alter	SW1/3	Off
í Factory configured - do not alter	SW1/4	Off
í Factory configured - do not alter	SW1/5	Off
í Factory configured - do not alter	SW1/6	Off
í Factory configured - do not alter	SW1/7	Off
í Factory configured - do not alter	SW1/8	Off
í Factory configured - do not alter	SW2/6	Off
í Factory configured - do not alter	SW2/7	Off
í Monitor type select color	SW2/8	On
Monitor type select monochrome	SW2/8	Off

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1 M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

DRAM SWITCH CONFIGURATION					
Size	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5
1MB	On	On	On	On	On
2MB	Off	On	On	On	On
4MB	On	On	Off	On	On
5MB	On	On	Off	Off	On
8MB	Off	On	Off	On	On
16MB	On	On	Off	On	Off
20MB	On	On	Off	Off	Off
32MB	Off	On	Off	On	Off

NPU CONFIGURATION		
Setting	W1	W2
Disabled	Open	Closed
Enabled	Closed	Open