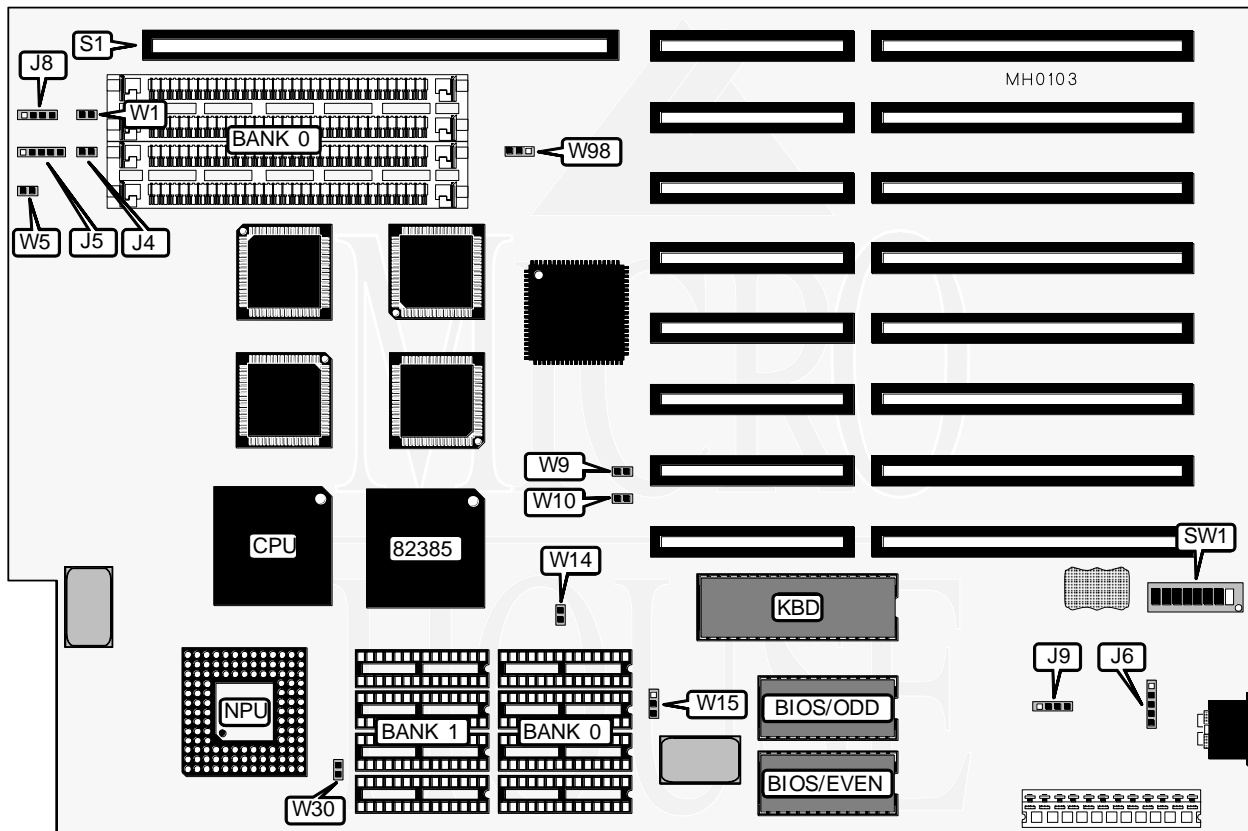


MICRONICS COMPUTERS, INC.

80386 SMT ASIC, 386 ASIC CACHE VER. 4.0

Processor 80386DX
Processor Speed 25/33MHz
Chip Set Micronics
Max. Onboard DRAM 4MB
Cache 64KB
BIOS AMI
Dimensions 215mm x 337mm
I/O Options 32-bit external memory card
NPU Options 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J5	32-bit external memory card	S1
External keyboard	J6	Reset switch	W1
Speaker	J8	Turbo LED	W4
External battery	J9	Turbo switch	W5

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USER CONFIGURABLE SETTINGS		
Function	Switch	Position
í Factory configured - do not alter	SW1/1	On
í NPU enabled	SW1/2	Off
NPU disabled	SW1/2	On
í BIOS shadowing enabled	SW1/4	Off
BIOS shadowing disabled	SW1/4	On
í Factory configured - do not alter	SW1/5	Off
í Monitor type select color	SW1/6	On
Monitor type select monochrome	SW1/6	Off
í Cache enabled	SW1/7	On
Cache disabled	SW1/7	Off

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4
1MB	(4) 256K x 9	NONE	NONE	NONE	NONE
1MB	NONE	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	NONE	(4) 256K x 9	NONE	NONE
2MB	NONE	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE	NONE
4MB	NONE	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	NONE	(4) 1M x 9	NONE	NONE
5MB	NONE	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	NONE	(4) 256K x 9	(4) 1M x 9	NONE
6MB	NONE	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
8MB	(4) 1M x 9	NONE	(4) 1M x 9	NONE	NONE
8MB	NONE	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	NONE	(4) 1M x 9	(4) 1M x 9	NONE
9MB	NONE	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	NONE	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
10MB	NONE	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	NONE	(4) 1M x 9	(4) 1M x 9	NONE
12MB	NONE	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
13MB	(4) 256K x 9	NONE	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
13MB	NONE	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 1M x 9	NONE	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	NONE	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

Note: Banks 1, 2, 3, & 4 are utilized when a 32-bit external memory card is installed at S1.
Banks 0 & 1 can not be used simultaneously

CACHE CONFIGURATION					
Size	Organization	Cache	Location	W14	W15
32KB	Direct mapped	(4) 8K x 8	Bank 0	closed	pins 1 & 2 closed
64KB	2-way set associative	(8) 8K x 8	Banks 0 & 1	open	pins 2 & 3 closed

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CACHE JUMPER CONFIGURATION		
Size	W14	W15
32KB	closed	pins 1 & 2 closed
64KB	open	pins 2 & 3 closed

BUS SPEED CONFIGURATION		
Speed	SW1/3	SW1/8
i iOSC/3	On	Off
iOSC/4	Off	On