

# **USER'S MANUAL**

**ProX-1750**

**Pentium® 4**

**Motherboard**

**With VGA / Sound / 2LAN**

**Prox-1750 M0**

***Prox-1750 Pentium® 4  
Main Board  
With VGA / SOUND / Dual LAN***

***OPERATION MANUAL***

**COPYRIGHT NOTICE**

This operation manual is meant to assist both Embedded Computer manufacturers and end users in installing and setting up the system. The information contained in this document is subject to change without any prior notice.

This manual is copyrighted February 2003. You may not reproduce or transmit in any form or by any means, electronic, or mechanical, including photocopying and recording.

**ACKNOWLEDGEMENTS**

All trademarks and registered trademarks mentioned herein are the property of their respective owners.

**CE NOTICE**

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

---

---

**FCC NOTICE**

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approve by the party responsible for compliance could void your authority to operate such equipment.



**TABLE OF CONTENTS**

**CHAPTER 1 INTRODUCTION**

1-1 About This Manual ..... 1-2  
 1-2 System Specifications ..... 1-3  
 1-3 Safety Precautions ..... 1-6

**CHAPTER 2 HARDWARE CONFIGURATION**

2-1 Jumper & Connector Quick Reference Table ..... 2-2  
 2-2 Component Locations ..... 2-3  
 2-3 How to Set the Jumpers ..... 2-4  
 2-4 COM Port Connector ..... 2-6  
 2-5 RS232/422/485 (COM2) Selection ..... 2-8  
 2-6 JCOM1 & COM2 (JCOM2) RI & Voltage Selection ... 2-9  
 2-7 COM3 & COM4 RI & Voltage Selection ..... 2-11  
 2-8 Keyboard & PS/2 Mouse Connector ..... 2-13  
 2-9 Reset Connector ..... 2-13  
 2-10 External Speaker Connector ..... 2-14  
 2-11 Hard Disk Drive LED Connector ..... 2-14  
 2-12 Power LED Connector ..... 2-14  
 2-13 EXTSIM# Connector.. ..... 2-15  
 2-14 ATX Power Button ..... 2-15  
 2-15 Keylock Connector ..... 2-15  
 2-16 IrDA Connector ..... 2-16  
 2-17 Printer Connector ..... 2-16  
 2-18 ATX Power Connector ..... 2-17  
 2-19 Clear CMOS Data Selection ..... 2-18  
 2-20 VGA Connector ..... 2-19  
 2-21 USB & LAN Connector ..... 2-20  
 2-22 USB Power Setting ..... 2-22  
 2-23 LAN2 Enable/Disable Connector ..... 2-23  
 2-24 Wake-on-modem Connector ..... 2-24  
 2-25 CPU Fan Connector ..... 2-24  
 2-26 System Fan Connector ..... 2-24  
 2-27 Reset/NMI/Clear Watchdog Selection ..... 2-25  
 2-28 Hard Disk Drive Connector ..... 2-26  
 2-29 Floppy Disk Drive Connector ..... 2-28  
 2-30 Sound Connector ..... 2-29

2-31	CD Audio-In Connector .....	2-30
2-32	Memory Installation .....	2-31
2-33	PPCI Connector .....	2-32
2-34	AGP Connector .....	2-33

**CHAPTER 3 SOFTWARE UTILITIES**

3-1	Introduction .....	3-2
3-2	VGA Driver Utility .....	3-2
3-3	Flash BIOS Update .....	3-3
3-4	LAN Driver Utility .....	3-5
3-5	Sound Driver Utility .....	3-7
3-6	Watchdog Timer Configuration .....	3-8

**CHAPTER 4 AWARD BIOS SETUP**

4-1	Introduction .....	4-2
4-2	Entering Setup .....	4-3
4-3	The Standard CMOS Features .....	4-4
4-4	The Advanced BIOS Features .....	4-8
4-5	Advanced Chipset Feature .....	4-11
4-6	Integrated Peripherals .....	4-14
4-7	Power Management Setup .....	4-18
4-8	PNP/PCI Configuration .....	4-20
4-9	PC Health Status .....	4-22
4-10	Frequency / Voltage Control .....	4-23
4-11	Load Fail-Safe Defaults .....	4-24
4-12	Load Optimized Defaults .....	4-24
4-13	Password Setting .....	4-25
4-14	Save & Exit Setup .....	4-26
4-15	Exit Without Saving .....	4-27

**APPENDIX A EXPANSION BUS**

ISA Bus Pin Assignment .....	A-2
PCI Bus Pin Assignment .....	A-3

---

**APPENDIX B TECHNICAL SUMMARY**

Block Diagram .....	B-2
Interrupt Map .....	B-3
RTC (Standard) RAM Bank .....	B-4
Timer & DMA Channels Map .....	B-5
I/O & Memory Map .....	B-6

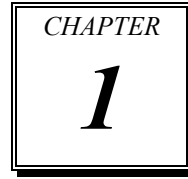
**APPENDIX C TROUBLE SHOOTING**

Trouble Shooting for Error Messages .....	C-2
Trouble Shooting for POST Codes .....	C-8





# ***INTRODUCTION***



This chapter gives you the information for Prox-1750. It also outlines the System specifications.

Section includes:

- About This Manual
- System Specifications
- Safety Precautions

**Experienced users can skip to chapter 2 on page 2-1 for a Quick Start.**



## **1-1. ABOUT THIS MANUAL**

Thank you for purchasing our Prox-1750 Socket 478 Mainboard enhanced with VGA/Sound/Dual LAN, which is fully PC / AT compatible. The Prox-1750 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the system. It contains four chapters. The user can apply this manual for configuration according to the following chapters:

### ***Chapter 1 Introduction***

This chapter introduces you to the background of this manual, and the specifications for this system. The final page of this chapter will indicate how to avoid damaging this board.

### ***Chapter 2 Hardware Configuration***

This chapter outlines the component locations and their functions. In the end of this chapter, you will learn how to set jumper and how to configure this card to meet your own needs.

### ***Chapter 3 Software Utilities***

This chapter contains helpful information for proper installations of the VGA utility, LAN utility, Sound utility, and Flash BIOS Update. It also describes the Watchdog-timer configuration.

### ***Chapter 4 Award BIOS Setup***

This chapter indicates you how to set up the BIOS configurations.

### ***Appendix A Expansion Bus***

This Appendix introduces you the expansion bus for ISA BUS and PCI BUS.

### ***Appendix B Technical Summary***

This section gives you the information about the Technical maps.

### ***Appendix C Trouble Shooting***

This section outlines the error messages and offers you the methods to solve the problems.

## 1-2. SYSTEM SPECIFICATIONS

- **CPU (mPGA478):**
  - Intel® Pentium 4 in mPGA478 Socket.
  - 1.5~3.06 GHz clock generator.
  - Front Side Bus 533/400 MHZ.
  - Auto detect voltage regulator.
  - Intel 845G B version support H-T Tech.
  
- **MEMORY :**
  - Supports up to 2GB DDR SDRAM.
  - Two 184-pin DDR DIMM sockets on board.
  
- **CACHE :**
  - Built-in CPU.
  
- **REAL-TIME CLOCK :**
  - 256-byte battery backed CMOS RAM.
  - Hardware implementation to indicate century rollover.
  
- **BIOS :**
  - Phoenix-AwardBIOS™ for plug & play function.
  - Easy update 512KB flash EEPROM.
  - Support S/IO Setup.
  
- **KEYBOARD CONNECTOR :**
  - Mini DIN connector.
  - Supports for AT/PS2 keyboard.
  
- **MOUSE CONNECTOR :**
  - Mini DIN connector.
  - Supports for PS/2 Mouse.
  
- **BUS SUPPORT :**
  - External 1 ISA, 6 PCI, 1 AGP Bus
  - External 1 PPCI Bus
  
- **DISPLAY :**
  - Built-in Intel 845G, support for CRT and DVO.
  - Onboard 15-pin VGA D-SUB connector, support for resolution on SVGA

Monitor.

● **WATCHDOG :**

I / O port 0443H to Enable watchdog  
I / O port 0441H to Disable watchdog  
Selectable for NMI or Reset function by jumper setting  
Time-out timing select 16 / 24 / 32 / 40 / 48 / 56 / 64 / 72 / 80 / 88 / 96 / 104 /  
112 / 120 sec +/- 25%.

● **IDE INTERFACE :**

Two IDE ports support up to four IED devices.  
Supports Ultra DMA 33/66/100.

● **FLOPPY DISK DRIVER INTERFACE :**

Support up to two Floppy Disk Drives, 3.5" and 5.25" (360K / 720K / 1.2M /  
1.44M / 2.88M).

● **USB CONNECTOR :**

Three sets of USB connector & USB power (+5.0V/+5.0V Stand By)  
selection.  
Support up to six USB 2.0 ports.

● **LAN ADAPTER :**

Dual ports, Intel® 82562ET PHY & Intel® 82551QM or Intel 82540EM  
(Giga Lan)  
Two RJ-45 Jack onboard  
LAN1: 10BaseT/100 BaseTx Ethernet  
LAN2: 10/100/1MB (82551QM) or 10/100/1GB (82540EM)  
Support wake-on-LAN function.

● **SERIAL PORT :**

Four high speed 16550 Compatible UARTs with Send / Receive 16 Byte  
FIFOs; COM1/3/4:RS-232; COM2:RS-232/422/485  
MIDI Compatible  
Programmable Baud Rate Generator

● **SOUND :**

Realtek ALC201A (AC'97 Codec).  
Fully Compliant AC'97 Analog I/O Component  
16-Bit Stereo Full-Duplex Codec  
Four Analog Line-level Stereo Inputs for Connection.  
High Quality CD Input with Ground Sense  
Stereo Line-Level Output  
Interface: Line-In, Line-Out, Microphone, and CD Audio-In.

● **HARDWARE MONITORING FUNCTION :**

Monitor Voltage, CPU temperature, & Cooling fan.  
If CPU Temperature is over setting, the buzzer will send out a warning.

● **IRDA PORT :**

5-pin Infrared port, support IrDA v1.0 SIR protocol

● **PARALLEL PORT :**

SPP / ECP / EPP Function.  
1 port, bi-directional parallel port.

● **GREEN FUNCTION :**

Software supported by BIOS setup

● **LED INDICATOR :**

System power  
Hard Disk access  
LAN LED indicator

● **DMA CONTROLLER :**

82C37 x 2

● **DMA CHANNELS :**

7

● **INTERRUPT CONTROLLERS :**

82C59 x 2

● **INTERRUPT LEVELS :**

15

● **OPERATING TEMPERATURE :**

0 to 60°C.

● **SYSTEM POWER REQUIREMENT :**

Meet ATX-12V standard power supply.

DC Voltage: +5V, minimum +4.75V, maximum 5.25V. DC Ampere: 3.4A.

DC Voltage: +12V, minimum +11.4V, maximum 12.6V.

DC Ampere: 7.3A.

DC Voltage: +3.3V, minimum +3.14V, maximum +3.46V. DC Ampere: 4A.

DC Voltage: -5V, minimum -4.75V, maximum +5.25V. DC Ampere: 1A.

DC Voltage: -12V, minimum -11.4V, maximum -12.6V. DC Ampere: 1A.

● **BOARD DIMENSIONS :**

305mm x 244mm, 12" x 9.61"

● **BOARD NET WEIGHT :**

0.67 Kgs.

**1-3. SAFETY PRECAUTIONS**

Follow the messages below to avoid your systems from damage:

1. Keep your system away from static electricity on all occasions.
2. Prevent electric shock. Don't touch any components of this card when the card is power-on. Always disconnect power when the system is not in use.
3. Disconnect power when you change any hardware devices. For instance, when you connect a jumper or install any cards, a surge of power may damage the electronic components or the whole system.

# ***HARDWARE CONFIGURATION***

CHAPTER

**2**

## **\*\* *QUICK START* \*\***

Helpful information describes the jumper & connector settings, and component locations.

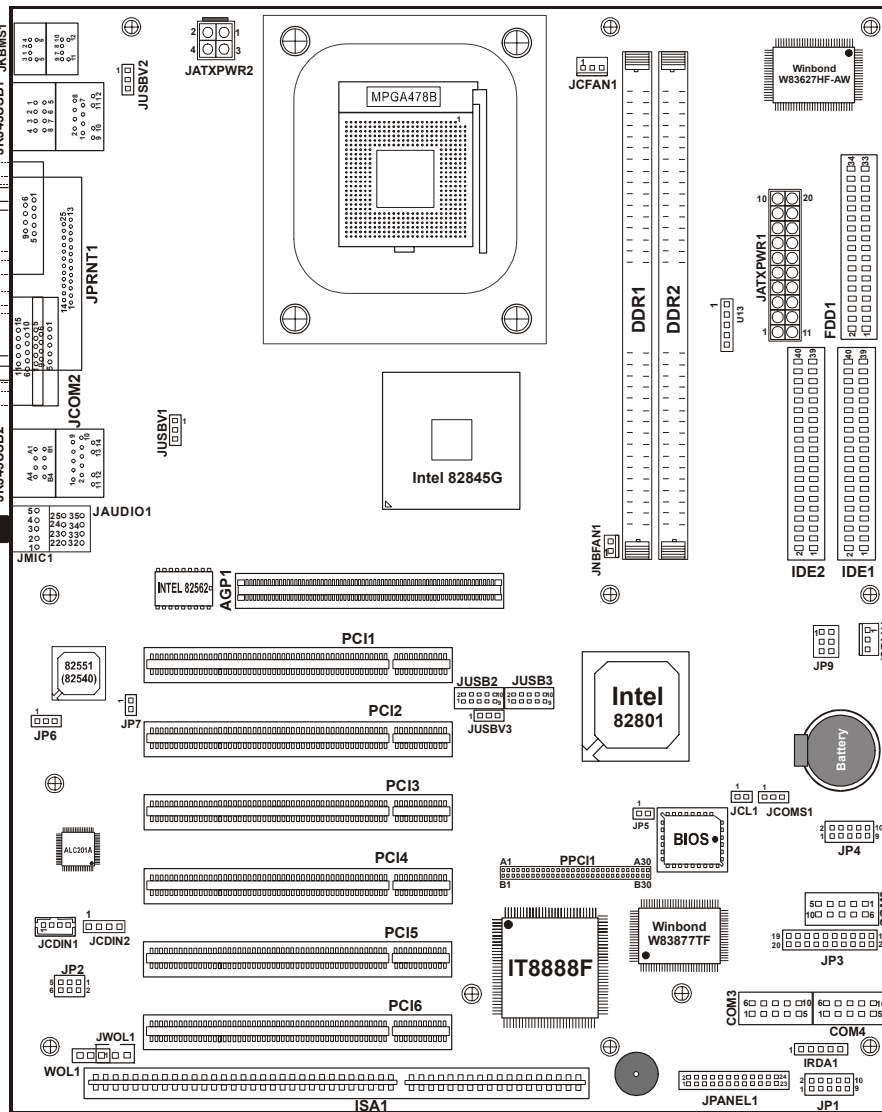
This section includes:

- Jumper & Connector Quick Reference Table
- Component Locations
- Configuration and Jumper settings
- Connector's Pin Assignments

## 2-1. JUMPER & CONNECTOR QUICK REFERENCE TABLE

COM Connector .....	JCOM1, COM2 COM3, COM4
RS232/422/485 (COM2) Selection .....	JP3
COM1 & COM2 RI & Voltage Selection .....	JP4
COM3 & COM4 RI & Voltage Selection .....	JP1
Keyboard & PS/2 Mouse Connector .....	JKBMS1
Reset Connector .....	JPANEL1 (13, 15)
External Speaker Connector .....	JPANEL1 (1, 3, 5, 7)
Hard Disk Drive LED Connector .....	JPANEL1 (9, 11)
Power LED Connector .....	JPANEL1 (8, 10, 12)
EXTSMI# Connector .....	JPANEL1 (2, 4)
ATX Power Button .....	JPANEL1 (14, 16)
KEYLOCK .....	JPANEL1 (17, 18)
IRDA Connector .....	IRDA1
Printer Connector .....	JPRNT1
ATX Power Connector .....	JATXPWR1, JATXPWR2
Clear CMOS Data Selection .....	JCMOS1
VGA Connector .....	JVGA1
USB & LAN Connector .....	JRJ45USB1, JRJ45USB2
Front USB Connector .....	JUSB3, JUSB2 (Optional)
USB Power Setting .....	JUSBV1, JUSBV2, JUSBV3
LAN2 Enable/Disable Selection .....	JP6
Wake-On-Modem Connector .....	WOL1 (JWOL1)
CPU Fan Connector .....	JCFAN1
System Fan Connector .....	JSFAN1
Reset/NMI/Clear Watchdog Selection .....	JP2
Hard Disk Drive Connector .....	IDE1, IDE2
Floppy Disk Drive Connector .....	FDD1
Sound Connector .....	JAUDIO1
CD Audio-In Connector .....	JCDIN1, JCDIN2
Memory Installation .....	DDR1, DDR2
PPCI Connector .....	PPCI
AGP Connector .....	AGP1
Reserved Pin .....	JP9,JP5,JP7,JCL1

2-2. COMPONENT LOCATIONS



Prox-1750 Connector, Jumper and Component locations

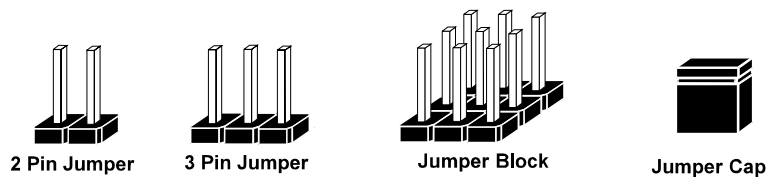


## 2-3. HOW TO SET THE JUMPERS

You can configure your board by setting the jumpers. Jumper is consists of two or three metal pins with a plastic base mounted on the card, and by using a small plastic "cap", Also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can set-up your hardware configuration by "opening" or "closing" pins.

The jumper can be combined into sets that called jumper blocks. When the jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows how this looks like.

### JUMPERS AND CAPS

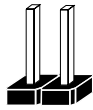


If a jumper has three pins (for example, labelled PIN1, PIN2, and PIN3), You can connect PIN1 & PIN2 to create one setting and shorting. You can either connect PIN2 & PIN3 to create another setting. The same jumper diagrams are applied all through this manual. The figure below shows what the manual diagram looks like and what they represent.

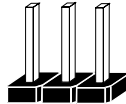
**JUMPER DIAGRAMS**



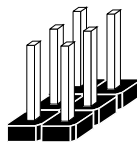
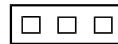
Jumper Cap looks like this



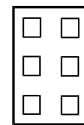
2 pin Jumper looks like this



3 pin Jumper looks like this



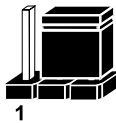
Jumper Block looks like this



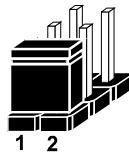
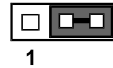
**JUMPER SETTINGS**



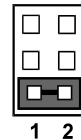
2 pin Jumper close(enabled)  
looks like this



3 pin Jumper  
2-3 pin close(enabled)  
looks like this



Jumper Block  
1-2 pin close(enabled)  
looks like this



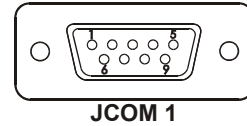
## 2-4. COM PORT CONNECTOR

Four COM ports are enhanced in this board namely: JCOM1, COM2, COM3 and COM4. JCOM1, COM3 and COM4 are fixed for RS-232 while COM2 is selectable for RS-232/422/485.

**JCOM1** : COM1 Connector, DB9 male connector

The JCOM1 Connector assignments are as follows :

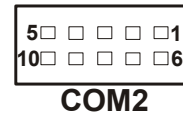
PIN	ASSIGNMENT
1	NDCD1
2	RXD1
3	TXD1
4	NDTR1
5	GND
6	NDSR1
7	NRTS1
8	NCTS1
9	NRI1



**COM2** : COM2 Connector

The COM2 Connector assignments are as follows :

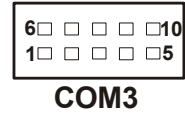
PIN	ASSIGNMENT		
	RS-232	RS-422	RS-485
1	NDCD2	TX-	TX-
2	RXD2	TX+	TX+
3	TXD2	RX+	RX+
4	NDTR2	RX-	RX-
5	GND	GND	GND
6	NDSR2	RTS-	NC
7	NRTS2	RTS+	NC
8	NCTS2	CTS+	NC
9	NRI2	CTS-	NC
10	NC	NC	NC



**COM3** : COM3 Connector

The COM3 Connector assignments are as follows :

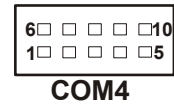
PIN	ASSIGNMENT
1	NDCD3
2	RXD3
3	TXD3
4	NDTR3
5	GND
6	NDSR3
7	NRTS3
8	NCTS3
9	NRI3
10	NC



**COM4** : COM4 Connector

The COM4 Connector assignments are as follows :

PIN	ASSIGNMENT
1	NDCD4
2	RXD4
3	TXD4
4	NDTR4
5	GND
6	NDSR4
7	NRTS4
8	NCTS4
9	NRI4
10	NC



## 2-5. RS232/422/485 (COM2) SELECTION

**JP3** : RS-232/422/485 Selection

COM1, COM3, & COM4 is fixed for RS-232 function only.

COM2 is selectable for RS-232, 422, 485 function.

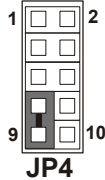
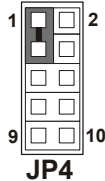
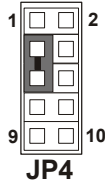
The jumper settings are as follows :

COM 2 Function	Jumper Setting (pin closed)	Jumper Illustration
RS-232	Open	
RS-422	1-2, 5-6, 7-8, 9-10 11-12,13-14,15-16 17-18, 19-20	
RS-485	1-3, 4-6, 7-8, 9-10 11-12,13-14,15-16 17-18, 19-20	

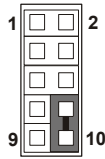
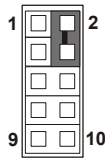
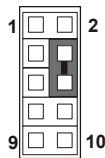
\*\*\* Manufactory default --- RS-232.

**2-6. JCOM1 & COM2(JCOM2) RI & VOLTAGE SELECTION**

**JP4:** JCOM1 & COM2(JCOM2) RI & Voltage Selection  
 The selections are as follows:

COM PORT	SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
JCOM1	RI	7-9	 <p>JP4</p>
	5V	1-3	 <p>JP4</p>
	12V	3-5	 <p>JP4</p>

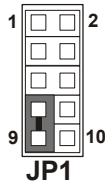
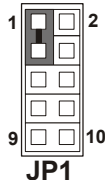
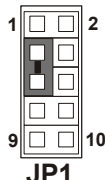
\*\*\* Manufactory default -- RI.

COM PORT	SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
COM2 (JCOM2)	RI	8-10	 <p>JP4</p>
	5V	2-4	 <p>JP4</p>
	12V	4-6	 <p>JP4</p>

\*\*\* 1. Manufactory default – RI.  
 2. JCOM2 is shared with COM2.

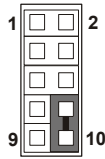
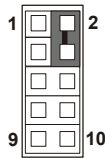
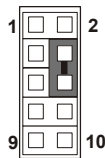
## 2-7. COM3 & COM4 RI & VOLTAGE SELECTION

**JP1:** COM3 & COM4 RI & Voltage Selection  
 The selections are as follows:

COM PORT	SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
COM3	RI	7-9	 <p>JP1</p>
	5V	1-3	 <p>JP1</p>
	12V	3-5	 <p>JP1</p>

\*\*\* Manufactory default -- RI.



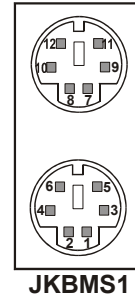
COM PORT	SELECTION	JUMPER SETTINGS	JUMPER ILLUSTRATION
COM4	RI	8-10	 <p>Diagram of JP1 jumper with pins 8, 9, and 10 connected.</p>
	5V	2-4	 <p>Diagram of JP1 jumper with pins 2 and 4 connected.</p>
	12V	4-6	 <p>Diagram of JP1 jumper with pins 4 and 6 connected.</p>

\*\*\* Manufactory default -- RI.

## 2-8. KEYBOARD & PS/2 MOUSE CONNECTOR

**JKBMS1** : Keyboard & PS/2 Mouse Connector  
 The pin assignments are as follows :

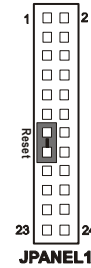
PIN	ASSIGNMENT
1	L KDAT
2	NC
3	GND
4	VCC
5	L KCLK
6	NC
7	L MDAT
8	NC
9	GND
10	VCC
11	L MCLK
12	NC



## 2-9. RESET CONNECTOR

**JPANEL1 (13-15)** : Reset Connector.  
 The pin assignments are as follows :

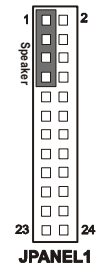
PIN	ASSIGNMENT
13	GND
15	RST BUTT



## 2-10. EXTERNAL SPEAKER CONNECTOR

**JPANEL1 (1,3,5,7) :** External Speaker Connector  
 The pin assignments are as follows :

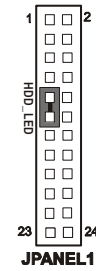
PIN	ASSIGNMENT
1	SPK3
3	NC
5	NC
7	SPK VCC



## 2-11. HARD DISK DRIVE LED CONNECTOR

**JPANEL1 (9-11) :** Hard Disk Drive LED Connector  
 The pin assignments are as follows :

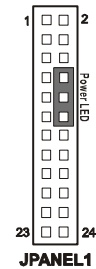
PIN	ASSIGNMENT
9	VCC3_3
11	HD_LED



## 2-12. POWER LED CONNECTOR

**JPANEL1 (8,10,12) :** Power LED Connector  
 The pin assignments are as follows :

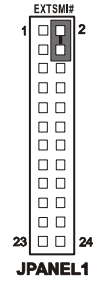
PIN	ASSIGNMENT
8	SEL_LED_PWR
10	SEL_LED_PWR
12	SEL_LED_PWR



### 2-13. EXTSMI# CONNECTOR

**JPANEL1 (2,4) : EXTSMI# Connector**  
 The pin assignments are as follows :

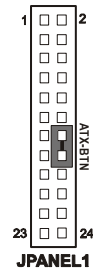
PIN	ASSIGNMENT
2	GPIO7
4	GND



### 2-14. ATX POWER BUTTON

**JPANEL1 (14,16) : ATX Power Button**  
 The pin assignments are as follows :

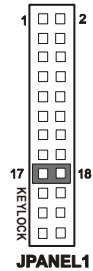
PIN	ASSIGNMENT
14	PWRBTN#
16	GND



### 2-15. KEYLOCK CONNNECTOR

**JPANEL1 (17,18,21,22,23,24) : KEYLOCK Connector**  
 The pin assignments are as follows :

PIN	ASSIGNMENT
17	KEYLOCK
18	GND



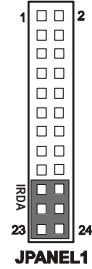
## 2-16. IRDA CONNECTOR

**JPANEL1 (19,20,21,22,23,24)** : Infrared Connector.

Share with IRDA1.

The pin assignments are as follows :

PIN	ASSIGNMENT
19	NC
20	NC
21	VCC5
22	GND
23	IRTX2
24	IRRX2



## 2-17. PRINTER CONNECTOR

**JPRNT1** : Printer Connector

The pin assignments are as follows :



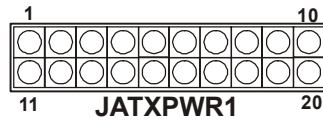
JPRNT1

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	STB	14	AFD
2	P0	15	ERR
3	P1	16	INIT
4	P2	17	SLIN
5	P3	18	GND
6	P4	19	GND
7	P5	20	GND
8	P6	21	GND
9	P7	22	GND
10	ACK	23	GND
11	BUSY	24	GND
12	PE	25	GND
13	SLCT		

## 2-18. ATX POWER CONNECTOR

**JATXPWR1** : ATX Power Connector

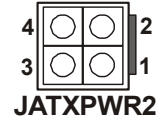
The pin assignments are as follows :



PIN	ASSIGNMENT
1	+3.3V
2	+3.3V
3	GND
4	+5.0V
5	GND
6	+5.0V
7	GND
8	Power Good
9	+5.0V Standby
10	+12.0V
11	+3.3V
12	-12.0V
13	GND
14	Power On
15	GND
16	GND
17	GND
18	-5.0V
19	+5.0V
20	+5.0V

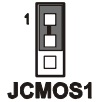

**JATXPWR2** : ATX Power Connector  
The pin assignments are as follows :

PIN	ASSIGNMENT
1	VCC12
2	VCC12
3	GND
4	GND



## 2-19. CLEAR CMOS DATA SELECTION

**JCMOS1** : Clear CMOS Data Selection  
The selection are as follows :

FUNCTION	JUMPER SETTING (pin closed)	JUMPER ILLUSTRATION
Normal	1-2	
Clear CMOS	2-3	

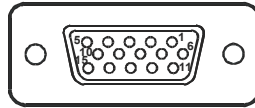
\*\*\* Manufacturing Default is set as Normal.

Note: To clear CMOS data, user must power-off the computer and set the jumper to “Clear CMOS” as illustrated above. After five to six seconds, set the jumper back to “Normal” and power-on the computer.

## 2-20. VGA CONNECTOR

**JVGA1** : VGA Connector

The pin assignments are as follows:



**JVGA1**

PIN	ASSIGNMENT
1	RED
2	GREEN
3	BLUE
4	VCC
5	GND
6	GND
7	GND
8	GND
9	VCC
10	GND
11	VCC
12	DDCDA
13	HSYNC
14	VSYNC
15	DDCCL



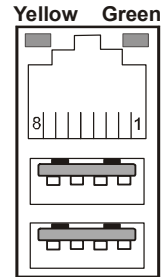
## 2-21. USB & LAN CONNECTOR

### JRJ45USB1: USB & LAN Connector

This connector contains two USB ports and one LAN.  
The pin assignments are as follows:

#### LAN

PIN	ASSIGNMENT
1	TX+
2	TX-
3	RX+
4	ISOLATED GND
5	ISOLATED GND
6	RX-
7	ISOLATED GND
8	ISOLATED GND
11	Pull Hi
12	LED - Yellow
13	Pull Hi
14	LED - Green



JRJ45USB1

#### USB

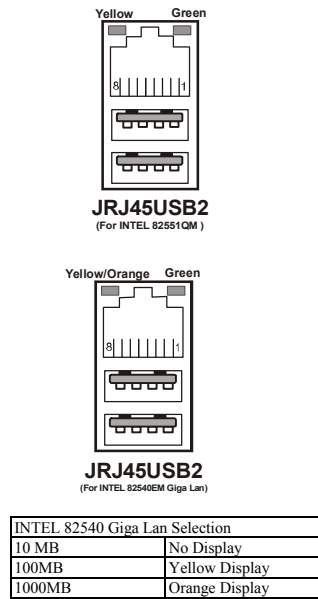
PIN	ASSIGNMENT
A1	+5.0V or +5.0V Standby
A2	USBP0-
A3	USBP0+
A4	GND
B1	+5.0V or +5.0V Standby
B2	USBP1-
B3	USBP1+
B4	GND

**JRJ45USB2: USB & LAN Connector**

This connector contains two USB ports and one LAN. The pin assignments are as follows:

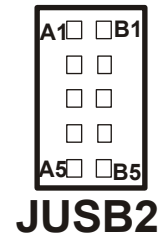
LAN

PIN	ASSIGNMENT	
	INTEL82551QM	INTEL 82540
1	TX+	MDIO+
2	TX-	MDIO-
3	RX+	MDI1+
4	ISOLATED GND	MDI1-
5	ISOLATED GND	MDI2+
6	RX-	MDI2-
7	ISOLATED GND	MDI3+
8	ISOLATED GND	MDI3-
11	Pull Hi	Orange-
12	LED – Yellow	Yellow-
13	Pull Hi	Pull Hi
14	LED - Green	LED - Green



USB

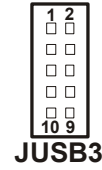
PIN	ASSIGNMENT
A1	+5.0V or +5.0V STAND BY
A2	USBP0-
A3	USBP0+
A4	GND
B1	+5.0V or +5.0V STAND BY
B2	USBP1-
B3	USBP1+
B4	GND



**Note:** JUSB2 share the JRJ45USB2 for cable with rear USB bracket.

**JUSB3:** Front USB Connector  
 The pin assignments are as follows:

PIN	ASSIGNMENT
1	+5.0V or +5.0V STAND BY
2	+5.0V or +5.0V STAND BY
3	USB D4--
4	USB D5--
5	USB D4++
6	USB D5++
7	GND
8	GND
9	NC
10	NC



## 2-22. USB POWER SETTING

**JUSBV2:** JRJ45USB1 POWER SELECTION

PIN	ASSIGNMENT
1	+5.0V
2	PIN#A1 PIN#B1
3	+5.0V STAND BY



**JUSBV1:** JRJ45USB2 POWER SELECTION

PIN	ASSIGNMENT
1	+5.0V
2	PIN#A1 PIN#B1
3	+5.0V STAND BY



**JUSBV3: JRJ45USB2 POWER SELECTION**

PIN	ASSIGNMENT
1	+5.0V
2	PIN#A1 PIN#B1
3	+5.0V STAND BY



**2-23. LAN2 ENABLE / DISABLE SELECTION**

**JP6:** LAN2 Enable / Disable Selection (for JRJ45USB2)

The selections are as follows:

SELECTION	JUMPER SETTINGS (pins closed)	JUMPER ILLUSTRATION
Enabled	1-2	 JP6
Disabled	2-3	 JP6

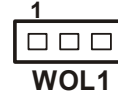
\*\*\* ) Manufactory default --- Enabled.

( LAN1 Enable/Disable Selection (for JRJ45USB1) by BIOS Setting.

## 2-24. WAKE-ON-MODEM CONNECTOR

**WOL1** : Wake-On-Modem Connector  
The pin assignments are as follows:

PIN	ASSIGNMENT
1	5V STANDBY
2	GND
3	ACTIVE SIGNAL



## 2-25. CPU FAN CONNECTOR

**JCFAN1** : CPU Fan connector  
The pin assignments are as follows:

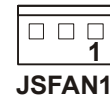
PIN	ASSIGNMENT
1	GND
2	+12V
3	FAN1



## 2-26. SYSTEM FAN CONNECTOR

**JSFAN1** : System Fan connector  
The pin assignments are as follows:

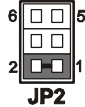
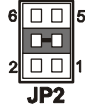
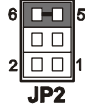
PIN	ASSIGNMENT
1	GND
2	+12V
3	FAN0




## 2-27. RESET/NMI/CLEAR WATCHDOG SELECTION

**JP2** : Reset/NMI/Clear Watchdog Selection

The selections are as follows:

FUNCTION	JUMPER SETTING (pin closed)	JUMPER ILLUSTRATION
RESET	1-2	
NMI	3-4	
CLEAR WATCHDOG	5-6	

\*\*\*Manufacturing Default is set as NMI.

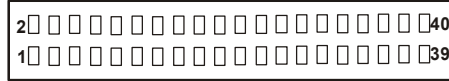
 User may select to use the Reset or NMI watchdog. NMI, also known as Non-Maskable Interrupt, is used for serious conditions that demand the processor's immediate attention; it cannot be ignored by the system unless it is shut off specifically. To clear NMI command, user should short the "Clear Watchdog" pin via push button.

## 2-28. HARD DISK DRIVE CONNECTOR

The Prox-1750 possesses two HDD connectors, IDE1 and IDE2.

### IDE1: Hard Disk Drive Connector

The pin assignments are as follows:



### IDE1

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	IDERST	21	IDEREQ
2	GND	22	GND
3	IDED7	23	IDEIOW
4	IDED8	24	GND
5	IDED6	25	IDEIOR
6	IDED9	26	GND
7	IDED5	27	IDE ORDY
8	IDED10	28	PULL LOW
9	IDED4	29	IDEACK
10	IDED11	30	GND
11	IDED3	31	IRQ14
12	IDED12	32	NC
13	IDED2	33	IDEA1
14	IDED13	34	P66 DETECT
15	IDED1	35	IDEA0
16	IDED14	36	IDEA2
17	IDED0	37	IDECS1P
18	IDED15	38	IDECS3P
19	GND	39	IDEACTP
20	NC	40	GND



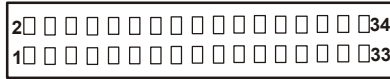


## 2-29. FLOPPY DISK DRIVE CONNECTOR

**FDD1** : Floppy Disk Drive Connector

You can use a 34-pin daisy-chain cable to connect two FDD. On one end of this cable there is a 34-pin flat cable to attach the FDD on the board; the other side is attaches to two FDD.

The pin assignments are as follows :



**FDD1**

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	2	DENSEL
3	GND	4	NC
5	GND	6	NC
7	GND	8	INDEX
9	GND	10	MOTEA
11	GND	12	DRVB
13	GND	14	DRVA
15	GND	16	MOTEB
17	GND	18	DIR
19	GND	20	STEP
21	GND	22	WDATA
23	GND	24	WGATE
25	GND	26	TK00
27	GND	28	WPT
29	NC	30	RDATA
31	GND	32	SIDE1
33	NC	34	DSKCHG

## 2-30. SOUND CONNECTOR

**J1** : Sound Connector, including Line-In, Line-Out & Mic. Also can support only MIC connector.

The pin assignments are as follows :

### Line-In

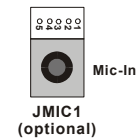
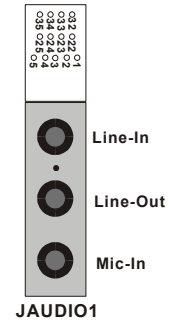
PIN	ASSIGNMENT
32	LINE L
33	AUD GND
34	AUD GND
35	LINE R

### Line-Out

PIN	ASSIGNMENT
22	SPK-L
23	NC
24	NC
25	SPK-R

### Mic-In

PIN	ASSIGNMENT
1	CGND
2	MIC_IN
3	CGND
4	NC
5	+5VAUDIO



## 2-31. CD AUDIO-IN CONNECTOR

**JCDIN1** : CD Audio-in Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	CDL
2	CDR
3	GND
4	CDR

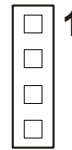


**JCDIN1**

**JCDIN2** : CD Audio-in Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	CDL
2	CDR
3	CDR
4	GND



**JCDIN2**

## 2-32. MEMORY INSTALLATION

The Prox-1750 embedded board supports two DDR memory sockets for a maximum total memory of 2GB in DDR memory type. The memory module capacities supported are 64MB, 128MB, 256MB, 512MB and 1GB. The following table lists the supported DDR DIMM configurations. Intel 845G supports configurations defined in the JEDEC DDR DIMM specification only (A,B,C). Non-JEDEC standard DIMMs such as double-sided x 16 DDR SDRAM DIMMs are not supported.

### Supported DDR DIMM Configurations.

Density	64Mbit		128Mbit		256Mbit		512Mbit	
Device Width	X8	X16	X8	X16	X8	X16	X8	X16
Single/Double	SS/DS	SS/DS	SS/DS	SS/DS	SS/DS	SS/DS	SS/DS	SS/DS
184-pin DDR	64/128 MB	32MB/NA	128/256 MB	64MB/NA	256/512 MB	128M B/NA	512/1024M	256M B/NA

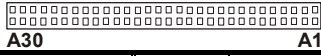
### Installing and Removing Memory Modules

To install the DDR modules, locate the memory slot on the embedded board and perform the following steps:

1. Hold the DDR module so that the key of the DDR module align with those on the memory slot.
2. Gently push the DDR module in an upright position until the clips of the slot close to hold the DDR module in place when the DDR module touches the bottom of the slot.
3. To remove the DDR module, press the clips with both hands.

### 2-33. PPCI CONNECTOR

You will find a PPCI connector in our Prox-1750. This connector is used to connect our SCSI daughter boards. The pin assignments are as follows:

<div style="display: flex; justify-content: space-between; align-items: center;"> <span>B30</span> <span>PPCI</span> <span>B1</span> </div> 			
PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	GND	B1	GND
A2	AD0	B2	AD1
A3	AD2	B3	AD3
A4	AD4	B4	AD5
A5	AD6	B5	AD7
A6	AD8	B6	AD9
A7	AD10	B7	AD11
A8	VCC	B8	VCC
A9	AD12	B9	AD13
A10	AD14	B10	AD15
A11	AD16	B11	AD17
A12	AD18	B12	AD19
A13	AD20	B13	AD21
A14	AD22	B14	AD23
A15	VCC	B15	VCC
A16	AD24	B16	AD25
A17	AD26	B17	AD27
A18	AD28	B18	AD29
A19	AD30	B19	AD31
A20	PIRQ#B	B20	PAR
A21	PP CLK	B21	IRDY#
A22	ID SEL	B22	TRDY#
A23	CBE#0	B23	CBE#1
A24	CBE#2	B24	CBE#3
A25	PGNT#4	B25	PREQ#4
A26	SERR#	B26	PERR#
A27	PIRQ#A	B27	PCI RST#
A28	STOP#	B28	PLOCK#
A29	DEVSEL#	B29	FRAME#
A30	GND	B30	GND

- The PPCI expansion connector of this Card is designed based on PCI Bus Master. That means when the PPCI expansion connector is used, the 4<sup>th</sup> PCI slot on the backplane is occupied.

## 2-34. AGP CONNECTOR

**AGP1** : AGP Connector (Note: only for AGP 4X/1.5V)

The pin assignments are as follows :

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	+12V	A34	VDDQ
A2	TYPEDET	A35	AD22
A3	RESERVED	A36	AD20
A4	USB-	A37	GND
A5	GND	A38	AD18
A6	INTA	A39	AD16
A7	RST	A40	VDDQ
A8	GNT	A41	FRAME
A9	VCC3.3	A42	RESERVED
A10	ST1	A43	GND
A11	RESERVED	A44	RESERVED
A12	PIPE	A45	VCC3.3
A13	GND	A46	TRDY
A14	WBF	A47	STOP
A15	SBA1	A48	PME
A16	VCC3.3	A49	GND
A17	SBA3	A50	PAR
A18	SB_STB	A51	AD15
A19	GND	A52	VDDQ
A20	SBA5	A53	AD13
A21	SBA7	A54	AD11
A22	RESERVED	A55	GND
A23	GND	A56	AD9
A24	RESERVED	A57	C/BE0
A25	VCC3.3	A58	VDDQ
A26	AD30	A59	AD_STB0
A27	AD28	A60	AD6
A28	VCC3.3	A61	GND
A29	AD26	A62	AD4
A30	AD24	A63	AD2
A31	GND	A64	VDDQ
A32	AD_STB1	A65	AD0
A33	C/BE3	A66	RESERVED

PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	OVRCNT	B34	VDDQ
B2	+5V	B35	AD21
B3	+5V	B36	AD19
B4	USB+	B37	GND
B5	GND	B38	AD17
B6	INTB	B39	C/BE
B7	CLK	B40	VDDQ
B8	REQ	B41	IRDY
B9	VCC3.3	B42	RESERVED
B10	ST0	B43	GND
B11	ST2	B44	RESERVED
B12	RBF	B45	VCC3.3
B13	GND	B46	DEVEL
B14	RESERVED	B47	VDDQ
B15	SBA0	B48	PERR
B16	VCC3.3	B49	GND
B17	SBA2	B50	SERR
B18	SB_STB	B51	C/BE1
B19	GND	B52	VDDQ
B20	SBA4	B53	AD14
B21	SBA6	B54	AD12
B22	RESERVED	B55	GND
B23	GND	B56	AD10
B24	RESERVED	B57	AD8
B25	VCC3.3	B58	VDDQ
B26	AD31	B59	AD_STB0
B27	AD29	B60	AD7
B28	VCC3.3	B61	GND
B29	AD27	B62	AD5
B30	AD25	B63	AD3
B31	GND	B64	VDDQ
B32	AD_STB1	B65	AD1
B33	AD23	B66	RESERVED

# ***SOFTWARE UTILITIES***

## **CHAPTER 3**

This chapter comprises the detailed information of VGA driver, LAN driver, Sound driver, and flash BIOS update. It also describes on how to install the watchdog timer.

Section includes:

- VGA Driver Utility
- Flash BIOS Update
- LAN Driver Utility
- SOUND Driver Utility
- Watchdog Timer Configuration



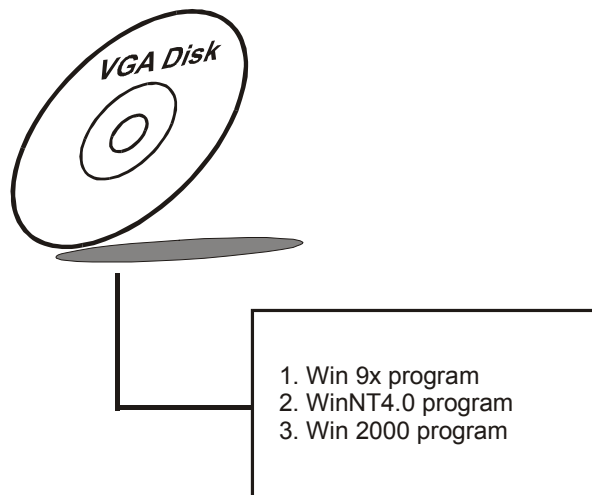
### 3-1. INTRODUCTION

Enclosed with our Prox-1750 package, you will find a CD ROM disk containing all types of drivers we have. As a Prox-1750 user, you will only need the some of files contained in the CD ROM disk, please take note of the following chart :

File name (Assume that CD ROM drive is D:)	Purpose
D:\VGA\	For VGA driver installation
D:\Flash\Awdflash.exe	For BIOS update
D:\LAN\	For LAN Driver installation
D:\Sound\	For Sound driver installation
D:\Utility\Infinst_autol.exe	For Intel® Chipset Software Installation Utility
***Install this software first!	Software Update 95/98/98SE/2K.

### 3-2. VGA DRIVER UTILITY

The VGA interface is embedded with our Prox-1750 system to support CRT display, Panel Link and TV-Out Encoder. The following illustration briefly shows you the content of VGA driver in D:\VGA\:



**3-2-1. Installation of VGA Driver**

- (1) Start the computer (Win 9x/NT/2K).
  - (2) Insert the Utility Disk into the CD ROM drive or drive A/B.
  - (3) Double-click "D:\VGA\WIN9X\SETUP.EXE" (if D is not your CD ROM drive and substitute D with the right drive) in the text entry area and press OK.
  - (4) Click "Next" on the Welcome screen.
  - (5) Read the license agreement and click "Yes" to continue.
  - (6) The driver files will now be installed. When finished, choose the "Yes" to reboot option, and click "Finished" to restart your computer. The driver should now be loaded.
- ⌚ For more information on VGA driver installation, please refer to the readme.txt found on the sub-directory of the VGA driver utility.

**3-3. FLASH BIOS UPDATE**

**3-3-1. System BIOS Update:**

Users of Prox-1750 can use the program "Awdflash.exe" contained in CD ROM for BIOS update. This is found in D:\flash\Awdflash.exe.

**3-3-2. To update BIOS :**

- (1) Install "Awdflash.exe" from the CD ROM Disk into your system.
- (2) Insert the new BIOS file you have obtained from Prox-1750 vendor.
- (3) Type the pathname to Awdflash.exe and execute the BIOS update with file b55xxxxx.bin  
C:\UTIL\AWDFLASH\AWARDFLASH H50xxxxx.bin
- (4) The screen will display the table below:

FLASH MEMORY WRITER v7.xx ( C ) Award Software 2000 All Rights Reserved
For i845-ITE8712-6A69RP69C-0      DATE : 05/06/01 Flash Type - SST 49LF004A /3.3V
File Name to Program : B55xxxxx.bin Checksum : XXXXX
Error Message : Do You Want To Save BIOS (Y/N)

If you want to save up the original BIOS, enter “Y ”and press < Enter > .  
If you choose “N”, the following table will appear on screen.

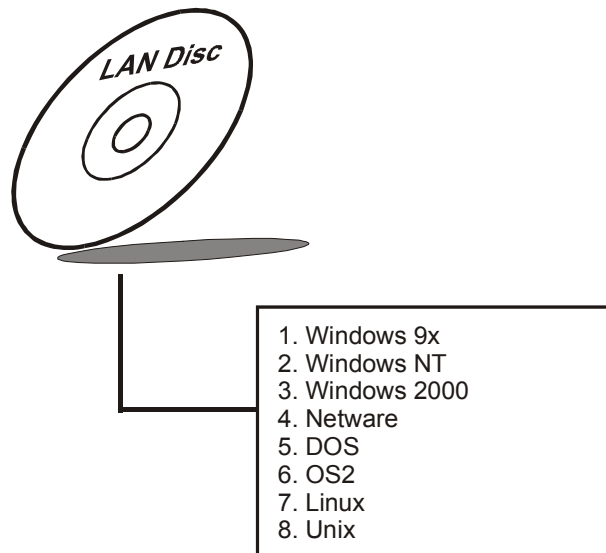
FLASH MEMORY WRITER v7.xx ( C ) Award Software 2000 All Rights Reserved
For i845-ITE8712-6A69RP69C-0      DATE : 05/06/01 Flash Type - SSL 49LF004A /3.3V
File Name to Program : H50xxxxx.bin Checksum : XXXXX
Error Message : Are You Sure To Program (Y/N)

Select “Y”, and the BIOS will be renewed. When you are refreshing your BIOS, do not turn off or reset the system, or you will damage the BIOS. After you have completed all the programming, you will see the line: “Reset System or power off to accomplish update process!”. Please turn off or reset the system. Then the Flash BIOS is fully implemented.

### 3-4. LAN DRIVER UTILITY

#### 3-4-1. Introduction

The Prox-1750 Mainboard is enhanced with LAN function that can support various network adapters. The content of the LAN driver is found as follows :



#### 3-4-2. Installation Procedure for Windows 9x

- (1) Insert the LAN Driver disk into Drive A or CD ROM drive.
- (2) In the "My computer", select the "Control Panel" icon.
- (3) In the "Control Panel" windows, select the "System" icon.
- (4) The "System Properties" window will appear, select the "Device Manager" and look for the "Network Adapter – PCI Fast Ethernet Adapter".
- (5) Click "Properties" and then "PCI Fast Ethernet Adapter Properties", choose the "Driver" icon.
- (6) Click "Update Driver" icon, and follow the remaining instruction.

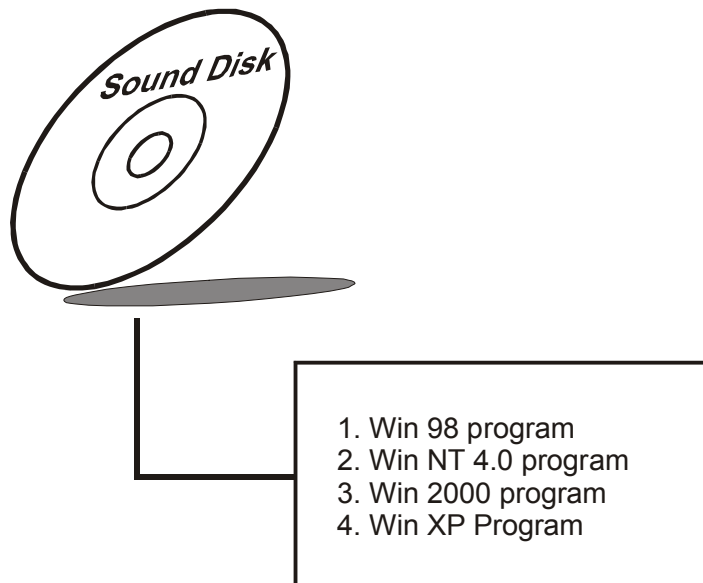
**3-4-3. Installation Procedure for Windows NT**

1. In the Main group to NT, select the “Control Panel” icon.
2. In the Control Panel window, choose the “Network” icon.
3. In the Network Settings dialog box, choose the “Add adapter” button.  
The Add Network Adapter dialog box appears.
4. In the list of network cards, select “<Other> requires disk from manufacturer”, and then press <Enter> button.
5. Insert the LAN Driver disk in Drive A or CDROM drive, and type D:\LAN, and then choose OK button.
6. Follow the remaining instruction, and re-boot your system to complete the installation process.

## 3-5. SOUND DRIVER UTILITY

### 3-5-1. Introduction

The ALC201A sound function enhanced in this system is fully compatible with Windows 98, Windows NT 4.0, Windows 2000 and Windows XP. Below, you will find the content of the Sound driver :



### 3-5-2. Installation Procedure for Windows 9x/NT/2000

- (1) From the task bar, click on Start, and then Run.
- (2) In the Run dialog box, type D:\Sound\path\setup, where "D:\Sound\path" refers to the full path to the source files.
- (3) Click on the OK button or press the ENTER key.
- (4) Click on the "Next" and OK prompts as they appear.
- (5) Reboot the system to complete the driver installation.

### 3-6. WATCHDOG TIMER CONFIGURATION

This board has watchdog timer function for monitoring whether the system is still work or not after a period of time. The user can select watchdog timer to system reset or NMI (Non Maskable interrupt) depending on the jumper set in "Reset/NMI/Clear Watchdog Selection" as described in chapter 2. This is defined at I/O port **443H**. When you want to enable the watchdog timer, please write I/O port **443H**, then the system will either reset itself or perform the NMI function. Likewise, when you want to disable the function, write I/O port **441H**, the system will run the command to stop the Watchdog function.

In Prox-1750 watchdog function, You must write your program so when it writes I/O port address 443 for enable watchdog and write I/O port address 441 for disable watchdog. The timer's intervals have a tolerance of 25% so you should program an instruction that will refresh the timer about every second.

The following program shows you how to program the watch timer in your program.

**Watchdog enable program:**

```
MOV    AX, 000FH      (choose the values you need; start from 0)
MOV    DX, 0443H
OUT    DX, AX
```

**Watchdog disable program:**

```
MOV    AX, 000FH      (this value can be ignored)
MOV    DX, 0441H
OUT    DX, AX
```

The Watchdog Timer control table is as follows:

Level	Value	Time/sec	Level	Value	Time/sec
1	F	0	9	7	64
2	E	8	10	6	72
3	D	16	11	5	80
4	C	24	12	4	88
5	B	32	13	3	96
6	A	40	14	2	104
7	9	48	15	1	112
8	8	56	16	0	120

# ***AWARD BIOS SETUP***

CHAPTER
<b>4</b>

This chapter shows how to set up the Award BIOS.

Section includes:

- Introduction
- Entering Setup
- The Standard CMOS Features
- The Advanced BIOS Features
- The Advanced Chipset Features
- Integrated Peripherals
- Power Management Setup
- PnP/PCI Configurations
- PC Health Status
- Frequency/Voltage Control
- Load Fail-Safe Defaults
- Password Setting
- Save and Exit Setup
- Exit Without Saving



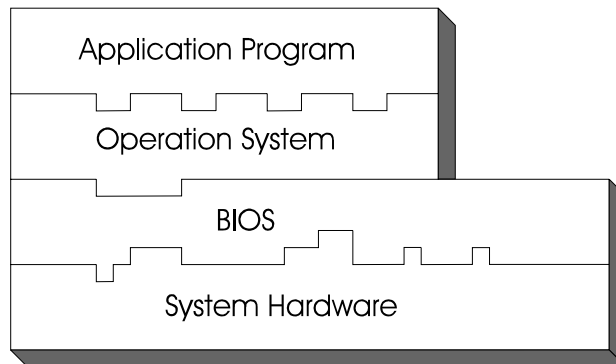
## 4-1. INTRODUCTION

This chapter will show you the function of the BIOS in managing the features of your system. The Prox-1750 Socket 370 Mainboard is equipped with the BIOS for system chipset from Award Software Inc. This page briefly explains the function of the BIOS in managing the special features of your system. The following pages describe how to use the BIOS for system chipset Setup menu.

Your application programs (such as word processing, spreadsheets, and games) rely on an operating system such as DOS or OS/2 to manage such things as keyboard, monitor, disk drives, and memory.

The operating system relies on the BIOS (Basic Input and Output system), a program stored on a ROM (Read-only Memory) chip, to initialize and configure your computer's hardware. As the interface between the hardware and the operating system, the BIOS enables you to make basic changes to your system's hardware without having to write a new operating system.

The following diagram illustrates the interlocking relationships between the system hardware, BIOS, operating system, and application program:



## 4-2. ENTERING SETUP

When the system is powered on, the BIOS will enter the Power-On Self Test (POST) routines and the following message will appear on the lower screen:

**PRESS <DEL> TO ENTER SETUP, ESC TO SKIP MEMORY TEST**

As long as this message is present on the screen you may press the <Del> key (the one that shares the decimal point at the bottom of the number keypad) to access the Setup program. In a moment, the main menu of the Award Setup Program will appear on the screen:

Phoenix - AwardBIOS CMOS Setup Utility

<ul style="list-style-type: none"> <li>▶ Standard CMOS Features</li> <li>▶ Advanced BIOS Features</li> <li>▶ Advanced Chipset Features</li> <li>▶ Integrated Peripherals</li> <li>▶ Power Management Setup</li> <li>▶ PnP/PCI Configurations</li> <li>▶ PC Health Status</li> </ul>	<ul style="list-style-type: none"> <li>▶ Frequency/Voltage Control</li> <li>Load Fail-Safe Defaults</li> <li>Load Optimized Defaults</li> <li>Set Supervisor Password</li> <li>Set User Password</li> <li>Save &amp; Exit Setup</li> <li>Exit Without Saving</li> </ul>
Esc : Quit	↑↓→← : Select Item
F10 : Save & Exit Setup	
Time, Date, Hard Disk Type . . .	

### Setup program initial screen

You may use the cursor the up/down keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear at the bottom of the screen.

### 4-3. THE STANDARD CMOS FEATURES

Highlight the "STANDARD CMOS FEATURES" and press the <ENTER> key and the screen will display the following table:

Phoenix - AwardBIOS CMOS Setup Utility  
Standard CMOS Features

Date (mm:dd:yy)	Wed, Aug 7 2002	Item Help
Time (hh:mm:ss)	10 : 50 : 17	
▶ IDE Primary Master	[None]	Menu Level ▶  Change the day, month, year and century
▶ IDE Primary Slave	[IBM-DHEA-36481]	
▶ IDE Secondary Master	[None]	
▶ IDE Secondary Slave	[CD-540E]	
Drive A	[1.44M, 3.5 in.]	
Drive B	[None]	
Video	[EGA/VGA]	
Halt On	[All, But Keyboard]	
Base Memory	640K	
Extended Memory	129024K	
Total Memory	130048K	
↑↓→←:Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

**Standard CMOS Setup Screen**

In the above Setup Menu, use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item. Note that as you highlight each item, the description of the selected item is found on the right side of the screen.

**Date:**

< Month >, < Date > and < Year >. Ranges for each value are in the CMOS Setup Screen, and the week-day will skip automatically.

**Time:**

< Hour >, < Minute >, and < Second >. Use 24 hour clock format, i.e., for PM numbers, add 12 to the hour. For example: 4: 30 P.M. You should enter the time as 16:30:00.

**IDE Primary Master / Slave:****IDE Secondary Master / Slave:**

The options for these items are found in its sub menu. By pressing the <ENTER> key, you are prompt to enter the sub menu of the detailed options as shown below:

Phoenix - AwardBIOS CMOS Setup Utility		Item Help
IDE Primary Master		
IDE HDD Auto-Detection	[Press Enter]	
IDE Primary Master Access Mode	[Auto]	Menu Level ►►
Capacity	37511 MB	To auto-detect the HDD's size, head...on this channel
Cylinder	17956	
Head	16	
Precomp	65535	
Landing Zone	17955	
Sector	255	
↑↓→←:Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

**Table 1 – IDE Primary Master sub menu**

Descriptions on each item above are as follows:

1. IDE HDD Auto-detection  
Press the enter key to auto-detect the HDD on this channel. If detection is successful, it fills the remaining fields on this menu.
2. IDE Primary Master  
There are three available option on this category, namely: None, Auto, and Manual. By selecting “Manual”, you can set the remaining fields on this screen, such as -
  - a. Cylinder – Set the number of cylinders for this hard disk.
  - b. Head – Set the number of read/write heads.
  - c. Precomp - \*\*\***Warning!** Setting a value of 65535 means no HDD.
  - d. Landing Zone
  - e. Sector – Set the number of sector per track

3. Access Mode

There are four available options for this item, namely: Normal, LBA, Large and Auto. Choose the access mode for this hard disk.

4. Capacity

Disk Drive capacity (Approximated). Note that this size is usually slightly greater than the size of a formatted disk given by a disk-checking program.

**DRIVE A AND DRIVE B:**

Select the type of floppy disk drive installed in your system. The available options are 360KB 5.25in, 1.2KB 5.25in, 720KB 3.5in, 1.44MB 3.5in, 2.88MB 3.5in and None.

**VIDEO:**

This category selects the type of video adapter used for the primary system monitor. Although secondary monitors are supported, you do not have to select the type in Setup. Available Options are Mono, Color 40, VGA/EGA, and Color 80.

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA, SVGA or PGA monitor adapters.
CGA 40	Color Graphics Adapter, power up in 40 column mode.
CGA 80	Color Graphics Adapter, power up in 80 column mode.
MONO	Monochrome adapter, includes high resolution monochrome adapters.

**HALT ON:**

This category allows user to choose whether the computer will stop if an error is detected during power up. Available options are "All errors", "No errors", "All, But keyboard", "All, But Diskette", and "All But Disk/Key".

**BASE MEMORY:**

Displays the amount of conventional memory detected during boot up.

**EXTENDED MEMORY:**

Displays the amount of extended memory detected during boot up.

**TOTAL MEMORY:**

Displays the total memory available in the system.

**HARD DISK ATTRIBUTES:**

Type	Cylinders	Heads	V-P comp	LZone	Sect	Capacity
1	306	4	128	305	17	10
2	615	4	300	615	17	20
3	615	6	300	615	17	30
4	940	8	512	940	17	62
5	940	6	512	940	17	46
6	615	4	65535	615	17	20
7	642	8	256	511	17	30
8	733	5	65535	733	17	30
9	900	15	65535	901	17	112
10	820	3	65535	820	17	20
11	855	5	65535	855	17	35
12	855	7	65535	855	17	49
13	306	8	128	319	17	20
14	733	7	65535	733	17	42
15	000	0	0000	000	00	00
16	612	4	0000	663	17	20
17	977	5	300	977	17	40
18	977	7	65535	977	17	56
19	1024	7	512	1023	17	59
20	733	5	300	732	17	30
21	733	7	300	732	17	42
22	733	5	300	733	17	30
23	306	4	0000	336	17	10
24	977	5	65535	976	17	40
25	1024	9	65535	1023	17	76
26	1224	7	65535	1223	17	71
27	1224	11	65535	1223	17	111
28	1224	15	65535	1223	17	152
29	1024	8	65535	1023	17	68
30	1024	11	65535	1023	17	93
31	918	11	65535	1023	17	83
32	925	9	65535	926	17	69
33	1024	10	65535	1023	17	85
34	1024	12	65535	1023	17	102
35	1024	13	65535	1023	17	110
36	1024	14	65535	1023	17	119
37	1024	2	65535	1023	17	17
38	1024	16	65535	1023	17	136
39	918	15	65535	1023	17	114
40	820	6	65535	820	17	40
41	1024	5	65535	1023	17	42
42	1024	5	65535	1023	26	65
43	809	6	65535	852	17	40
44	809	6	65535	852	26	61
45	776	8	65335	775	33	100
47			AUTO			

Award Hard Disk Type Table

### 4-4. THE ADVANCED BIOS FEATURES

Choose the "ADVANCED BIOS FEATURES" in the main menu, the screen shown as below.

Phoenix - AwardBIOS CMOS Setup Utility  
Advanced BIOS Features

Virus Warning	[Disabled]	Item Help
CPU Internal Cache	[Enabled]	
External Cache	[Enabled]	Menu Level ▶
CPU L2 Cache ECC Checking	[Enabled]	
Processor Number Feature	[Enabled]	Allows you to choose the VIRUS warning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempt to write data into this area, BIOS will show a warning message on screen and alarm beep
Quick Power On Self Test	[Enabled]	
First Boot Device	[Floppy]	
Second Boot Device	[HDD-0]	
Third Boot Device	[LS120]	
Boot Other Device	[Enabled]	
Swap Floppy Drive	[Disabled]	
Boot Up Floppy Seek	[Enabled]	
Boot Up NumLock Status	[On]	
Gate A20 Option	[Fast]	
Typematic Rate Setting	[Disabled]	
x Typematic Rate (Chars/Sec)	6	
x Typematic Delay (Msec)	250	
Security Option	[Setup]	
OS Select For DRAM > 64MB	[Non-OS2]	
HDD S.M.A.R.T. Capability	[Disabled]	
Report No FDD for WIN 95	[No]	
Small Logo (EPA) Show	[Disabled]	
↑↓→←:Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

Advanced BIOS Features Setup Screen

The "ADVANCED BIOS FEATURES SETUP" allow you to configure your system for basic operation. The user can select the system's default speed, boot-up sequence, keyboard operation, shadowing and security.

A brief introduction of each setting in the BIOS FEATURES SETUP program is given below.

**VIRUS WARNING :**

This item allows you to choose the Virus Warning feature for IDE Hard Disk boot sector protection. When this function is enabled, the BIOS will supervise the boot sector and partition table of the hard disk drive for any attempt for modification.

**CPU INTERNAL CACHE/EXTERNAL CACHE :**

These two categories speed up memory access. However, it depends on CPU/chipset design.

**CPU L2 CACHE ECC CHECKING :**

This item allows you to enable or disable CPU L2 Cache ECC checking.

**PROCESSOR NUMBER FEATURE :**

This option is for Pentium III processor only. During Enabled, this will check the CPU Serial number. Disabled this option if you don't want the system to know the Serial number.

**QUICK POWER ON SELF TEST:**

This item allows you to speed up Power On Self Test (POST) after power-up the computer. When enabled, the BIOS will shorten or skip some check items during POST.

**FIRST/SECOND/THIRD/OTHER BOOT DEVICE:**

The BIOS load the operating system in a sequence selected by these items.

**SWAP FLOOPY DRIVE:**

This field is effective only in systems with two floppy drives. Selecting Enabled assigns physical drive B to logical drive A, and physical drive A to logical drive B.

**BOOT UP FLOOPY SEEK:**

You may enable / disable this item to define whether the system will look for a floppy disk drive to boot at power-on, or proceed directly to the hard disk drive.



**BOOT UP NUMLOCK STATUS:**

Select power on state for NumLock.

**GATE 20A OPTION:**

This entry allows you to select how the gate A20 is handled. When Normal was set, a pin in the keyboard controller controls Gate A20. And when Fast was set, the chipset controls Gate A20.

**TYPEMATIC RATE SETTING:**

Enable this item if you wish to be able to configure the characteristics of your keyboard. Typematic refers to the way in which characters are entered repeatedly if a key is held down. For example, if you press and hold down the "A" key, the letter "a" will repeatedly appear on your screen on your screen until you release the key. When enabled, the typematic rate and typematic delay can be selected.

**TYPEMATIC RATE (CHARS/SEC):**

This item sets the number of times a second to repeat a key stroke when you hold the key down. The choices are 6, 8, 10, 12, 15, 20, 24, and 30.


**TYPEMATIC DELAY (MSEC):**

The item sets the delay time after the key is held down before it begins to repeat the keystroke. The choices are 250, 500, 750, and 1000.

**SECURITY OPTION:**

This category allows you to limit access to the system and Setup, or just to Setup.

System	The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt.
Setup	The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt.

 To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.

**OS SELECT FOR DRAM >64MB :**

Select the operating system that is running with greater than 64MB or RAM on the system. You may choose OS2 or Non-OS2.

**REPORT NO FDD FOR WIN 95 :**

Select Yes to release IRQ6 when the system contains no floppy drive, for compatibility with Windows 95 logo certification. In the Integrated Peripherals screen, select Disabled for the Onboard FDC Controller field.

**4-5. ADVANCED CHIPSET FEATURES**

Choose the "ADVANCED CHIPSET FEATURES" from the main menu, the screen shown as below.

Phoenix - AwardBIOS CMOS Setup Utility  
Advanced Chipset Features

		Item Help
DRAM Timing Selectable	[By SPD]	
CAS Latency Time	[2.5]	
Active to Precharge Delay	[6]	
DRAM RAS-to-CAS Delay	[3]	
DRAM RAS Precharge	[3]	Menu Level ▶
Turbo Mode	[Disabled]	
Memory Frequency For	[Auto]	
System BIOS Cacheable	[Enabled]	
Video BIOS Cacheable	[Disabled]	
Memory Hole At 15M-16M	[Disabled]	
Delayed Transaction	[Enabled]	
AGP Aperture Size	[64MB]	
** On-Chip VGA Setting **		
On-Chip VGA	[Enabled]	
On-Chip Frame Buffer Size	[8MB]	
I/O Channel Check NMI	[Disabled]	

↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help  
 F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults

**Advanced Chipset Features Setup**

The parameters in this setup screen are for system designers, service personnel, and technically competent users only. Do not reset these values unless you understand the consequences of your changes.

**DRAM TIMEING SELECTABLE:**

The value in this field depends on performance parameters of the installed memory chips (DRAM). Do not change the value from the factory setting unless you install new memory that has a different performance rating than the original DRAMs.

**CAS LATENCY TIME:**

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing.

**DRAM RAS-TO-CAS DELAY:**

This item let you insert a timing delay between the CAS and RAS strobe signals, used when DRAM is written to, read from, or refreshed. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system. The choices are 2 and 3.

**DRAM RAS PRECHARGE TIME:**

If an insufficient number of cycles is allowed for the RAS to accumulate its charge before DRAM refresh, the refresh may be incomplete and the DRAM may fail to retain data. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system. The choices are 2 & 3.

**SYSTEM BIOS CACHEABLE:**

This item allows you to enable caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

**VIDEO BIOS CACHEABLE:**

This item allows you to enable caching of the video BIOS, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

**MEMORY HOLE AT 15M-16M:**

You may reserve this area of system memory for ISA adapter ROM. When this area is reserved, it cannot be cached. The user information of peripherals that need to use this area of system memory usually discusses their memory requirements.

**DELAY TRANSACTION:**

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select Enabled to support compliance with PCI specification version 2.1.

**AGP APERTURE SIZE:**

The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation.

**ON-CHIP Frame Buffer SIZE:**

Select the on-chip frame buffer size for VGA drive use. The available choices are 8MB, and Disabled.

**IO CHANNEL CHECK NMI:**

This field enable or disable IO channel check NMI. Before selecting this function, the user should check first that NMI function is enabled as described in chapter 2 (Reset/NMI/Clear Watchdog Selection)

### 4-6. INTEGRATED PERIPHERALS

Choose "INTEGRATED PERIPHERALS" from the main setup menu, a display will be shown on screen as below:

Phoenix - AwardBIOS CMOS Setup Utility  
Integrated Peripherals

		Item Help
On-Chip Primary PCI IDE	[Enabled]	
IDE Primary Master PIO	[Auto]	
IDE Primary Slave PIO	[Auto]	
IDE Primary Master UDMA	[Auto]	
IDE Primary Slave UDMA	[Auto]	
On-Chip Secondary PCI IDE	[Enabled]	
IDE Secondary Master PIO	[Auto]	Menu Level ▶
IDE Secondary Slave PIO	[Auto]	
IDE Secondary Master UDMA	[Auto]	
IDE Secondary Slave UDMA	[Auto]	
USB Controller	[Enabled]	
USB 2.0 Controller	[Enabled]	
USB Keyboard Support	[Disabled]	
USB Mouse Support	[Disabled]	
AC97 Audio	[Auto]	
Onboard/CNR LAN selection	[Auto]	
Init Display First	[Onboard/AGP]	
BIOS Flash Function	[Enabled]	
IDE HDD Block Mode	[Enabled]	
POWER ON Function	[BUTTON ONLY]	
x KB Power ON Password	[Enter]	
x Hot Key Power ON	[Ctrl-F1]	
Onboard FDC Controller	[Enabled]	
Onboard Serial Port 1	[3F8/IRQ4]	
Onboard Serial Port 2	[2F8/IRQ3]	
UART Mode Select	[Normal]	
x RxD, TxD Active	[Hi, Lo]	
x IR Transmission Delay	[Enabled]	
x UR2 Duplex Mode	Half	
x Use IR Pins	IR-Rx2Tx2	
Onboard Parallel Port	[378/IRQ7]	
Parallel Port Mode	[SPP]	
x EPP Mode Select	EPP1.7	
x ECP Mode Use DMA	3	
PWRON After PWR-Fail	[Off]	
Onboard Serial Port 3	[3E8]	
Serial Port 3 Use IRQ	[IRQ10]	
Onboard Serial Port 4	[2E8]	
Serial Port 4 Use IRQ	[IRQ11]	
↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

**INTEGRATED PERIPHERALS**

By moving the cursor to the desired selection and pressing <F1> key, the all options for the desired selection will be displayed for choice.

**ON-CHIP PRIMARY PCI IDE  
ON-CHIP SECONDARY PCI IDE:**

The Integrated peripheral controller contains an IDE interface with support for two IDE channels. Select Enabled to activate each channel separately.

**IDE PRIMARY MASTER/SLAVE PIO:  
IDE SECONDARY MASTER/SLAVE PIO:**

The four IDE PIO fields allow you to set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

**IDE PRIMARY MASTER/SLAVE UDMA:  
IDE SECONDARY MASTER/SLAVE UDMA:**

UDMA (Ultra DMA) is a DMA data transfer protocol, that utilizes ATA commands and the ATA bus to allow DMA commands to transfer data at a maximum burst rate of 33 MB/s. The implementation of UDMA is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If you hard drive and your system software both support Ultra DMA/33, select Auto to enable BIOS support.

**USB CONTROLLER:**

Select enabled if the system contains a Universal Serial Bus (USB) controller and you have USB peripherals.

**USB KEYBOARD SUPPORT:**

Select enabled if the system contains a Universal Serial Bus (USB) controller and you have a USB Keyboard.

**USB MOUSE SUPPORT:**

Select enabled if the system contains a Universal Serial Bus (USB) controller and you have a USB Mouse.

**INIT DISPLAY FIRST:**

This item allows you to decide to active whether PCI Slot or on-chip VGA first.

**AC97 AUDIO:**

This item allows you to decide to enable or disable the 810 chipset family to support AC97 Audio.

**IDE HDD BLOCK MODE:**

Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

**ONBOARD FDC CONTROLLER:**

Select Enabled if the system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install and-in FDC or the system has no floppy drive, select Disabled.

**ONBOARD SERIAL PORT 1**

**ONBOARD SERIAL PORT 2:**

Select an address and corresponding interrupt for the first and second serial ports.

**UART MODE SELECT:**

Select an operating mode for the second serial port. The choices are IrDA, Normal and ASK IR.

**RxD, TxD ACTIVE:**

Consult your IR peripheral documentation to select the correct setting of the TxD and RxD signals.

**UR2 DUPLEX MODE:**

This item allows you to select the IR half/full duplex function.

**ONBOARD PARALLEL PORT:**

This field determine access onboard parallel port controller with which I/O address.

**PARALLEL PORT MODE:**

Select an operating mode for the onboard parallel port. The available choices are *SPP*, *EPP*, *ECP*, or *ECP+EPA* modes.

**EPP MODE SELECT:**

This item allows you to configure the parallel port for the selected EPP mode. The Selection of EPP port type is 1.7 or 1.9.

**ECP MODE USE DMA:**

This item allows you to configure the parallel port DMA channel for the selected ECP mode. This selection is only available when ECP or ECP+EPP is selected in *Parallel Port mode*.

**PWRON AFTER PWR-FAIL:**

This item allows you to select if you want to power on the system after power failure.

**ONBOARD SERIAL PORT 3:**

**ONBOARD SERIAL PORT 4:**

Select a logical COM port name and matching address for the third and forth serial ports. Select an address and corresponding interrupt for third and forth serial port.

**SERIAL PORT 3 USE IRQ:**

**SERIAL PORT 4 USE IRQ:**

The items set the IRQ address of the serial ports.



### 4-7. POWER MANAGEMENT SETUP

Choose "POWER MANAGEMENT SETUP" option on the main menu, a display will be shown on screen as below :

Phoenix - AwardBIOS CMOS Setup Utility  
Power Management Setup

ACPI Function	[Enabled]	Item Help
Power Management	[User Define]	
MODEM Use IRQ	[3]	Menu Level ►
Suspend Mode	[Disabled]	
Soft-Off by PWR-BTTN	[Instant-Off]	
Wake-Up by PCI card	[Enabled]	
Wake up On LAN	[Enabled]	
Resume by Alarm	[Disabled]	
x Date (of Month) Alarm	[0]	
x Time (hh:mm:ss) Alarm	[0 : 0 : 0]	
** Reload Global Timer Events **		
Primary IDE 0	[Disabled]	
Primary IDE 1	[Disabled]	
Secondary IDE 0	[Disabled]	
Secondary IDE 1	[Disabled]	
FDD, COM, LPT Port	[Disabled]	
↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

**Power Management Setup Screen**

The "Power Management Setup" allows the user to configure the system to the most effectively save energy while operating in a manner consistent with your own style of computer use.

**ACPI FUNCTION:**

Users are allowed to enable or disable the Advanced Configuration and Power Management (ACPI).

**POWER MANAGEMENT:**

This item allows the user to select the type or degree of power saving and is directly related to the following modes:

- a. HDD Power Down
- b. Suspend Mode

**MODEM USE IRQ:**

This item enable you to name the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system.

**SUSPEND MODE:**

When enabled and after the set time of system inactivity, all devices except the CPU will be shut off.

**SOFT-OFF BY PWR-BTTN:**

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state when the system has “hung”. The choices are Delay 4 Sec and Instant-Off.

**WAKE-UP BY PCI CARD:**

An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) awakens the system from the soft-off state.

**RESUME BY ALARM:**

When enabled, you can set the date and time at which the RTC alarm awakens the system from Suspend mode.

**PM EVENTS:**

PM Events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything, which occurs to a device, which is configured as Enabled, even when the system is in a power down mode.

- |                      |                   |
|----------------------|-------------------|
| ❶ Primary IDE 0      | ❷ Primary IDE 1   |
| ❸ Secondary IDE 0    | ❹ Secondary IDE 1 |
| ❺ FDD, COM, LPT Port | ❻ PCI PIRQ[A-D]#  |

### 4-8. PNP/PCI CONFIGURATION

Choose "PNP/PCI CONFIGURATION" from the main menu, a display will be shown on screen as below:

Phoenix - AwardBIOS CMOS Setup Utility  
PnP/PCI Configurations

PNP OS Installed	[Yes]	Item Help
Reset Configuration Data	[Disabled]	
Resources Controlled By	[Auto(ESCD)]	Menu Level ▶  Select Yes if you are using a Plug and Play capable operating system Select No if you need the BIOS to configure non-boot devices
x IRQ Resources	Press Enter	
x DMA Resources	Press Enter	
PCI/VGA Palette Snoop	[Disabled]	
↑↓→←:Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

#### PNP/PCI CONFIGURATION

This section describes how to configure PCI bus system. PCI, also known as Personal Computer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when communicating with its own special components. This section covers technical items, which is strongly recommended for experienced users only.

**PNP OS INSTALLED:**

This item allows you to determine install PnP OS or not.

**RESET CONFIGURATION DATA:**

Normally, you leave this field Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on and the system configuration has caused such a serious conflict that the operating system can not boot.

**RESOURCE CONTROLLED BY:**

The Award Plug and Play Bios can automatically configure all of the booth and Plug and Play-compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows 95. By choosing "manual", you are allowed to configure the *IRQ Resources*, *DMA Resources* and *Memory Resources*. The choices are Auto(ESCD) and Manual.

**IRQ RESOURCES:**

You may assign each system interrupt a type, depending on the type of device using the interrupt.

**IRQ3/4/5/7/9/10/11/12/14/15 ASSIGNED TO:**

This item allows you to determine the IRQ assigned to ISA bus and is not available to any PCI slot. Legacy ISA for devices compliant with the original PC AT bus specification, PCI/ISA PnP for devices compliant with the Plug and Play standard whether designed for PCI or ISA bus architecture.

**DMA RESOURCES:**

When resources are controlled manually, assign each system DMA channel a type, depending on the type of device using the DMA channel.

**DMA 0/1/3/5/6/7 ASSIGNED TO:**

Legacy ISA for devices compliant with the original PC AT bus specification, PCI/ISA PnP for devices compliant with the Plug and Play standard whether designed for PCI or ISA bus architecture.

**PCI/VGA PALETTE SNOOP:**

Leave this field at disabled.

### 4-9. PC HEALTH STATUS

Choose "PC HEALTH STATUS" from the main menu, a display will be shown on screen as below:

Phoenix - AwardBIOS CMOS Setup Utility  
PC Health Status

CPU Warning Temperature	[50½C/122½F]	Item Help	
Current CPU Temperature	[49½C/120½F]		
Current SYSTEM Fan Speed	0 RPM	Menu Level ▶	
Current CPU Fan Speed	2481 RPM		
Vcore	1.42V		
3.3 V	3.34V		
+5 V	5.05V		
+12 V	11.79V		
-12 V	(-)12.52V		
-5 V	(-) 4.99V		
VBAT (V)	3.18V		
5VSB (V)	4.96V		
Shutdown Temperature	[60½C/140½F]		
↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults			

**PC Health Status Setup Screen**

**CPU WARMING TEMPERATURE:**

Select the combination of lower and upper limits for the CPU temperature. If the CPU temperature extends beyond either limit, any warning mechanism programmed into your system will be activated.

**CURRENT CPU TEMPERATURE:**

This field displays the current CPU temperature, if your computer contains a monitoring system.

**SHUTDOWN TEMPERATURE:**

User is allowed to set the temperature on which the system automatically shutdown when reaches or exceeds the temperature set.

**4-10. FREQUENCY/VOLTAGE CONTROL**

Choose "FREQUENCY/VOLTAGE CONTROL" from the main menu, a display will be shown on screen as below:

Phoenix - AwardBIOS CMOS Setup Utility  
Frequency/Voltage Control

Spread Spectrum	[Disabled]	Item Help
		Menu Level ►
↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults		

**Frequency / Voltage Control Setup Screen**

This setup menu allows you to specify your settings for frequency/voltage control.

**SPREAD SPECTRUM:**

This item allows you to enable or disable the spread spectrum modulate.

#### **4-11. LOAD FAIL-SAFE DEFAULTS**

By pressing the <ENTER> key on this item, you get a confirmation dialog box with a message similar to the following:

Load Fail-Safe Defaults ( Y/N ) ? N

To use the BIOS default values, change the prompt to "Y" and press the <Enter > key. CMOS is loaded automatically when you power up the system.

#### **4-12. LOAD OPTIMIZED DEFAULTS**

When you press <Enter> on this category, you get a confirmation dialog box with a message similar to the following:

Load Optimized Defaults ( Y/N ) ? N

Pressing "Y" loads the default values that are factory setting for optimal performance system operations.

### 4-13. PASSWORD SETTING


User is allowed to set either supervisor or user password, or both of them. The difference is that the supervisor password can enter and change the options of the setup menus while the user password can enter only but do not have the authority to change the options of the setup menus.

#### TO SET A PASSWORD

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

Enter Password:

Type the password up to eight characters in length, and press < Enter >. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press the < Enter > key. You may also press < Esc > to abort the selection and not enter a password.

 User should bear in mind that when a password is set, you will be asked to enter the password everything you enter CMOS setup Menu.

#### TO DISABLE THE PASSWORD

To disable the password, select this function (do not enter any key when you are prompt to enter a password), and press the <Enter> key and a message will appear at the center of the screen:

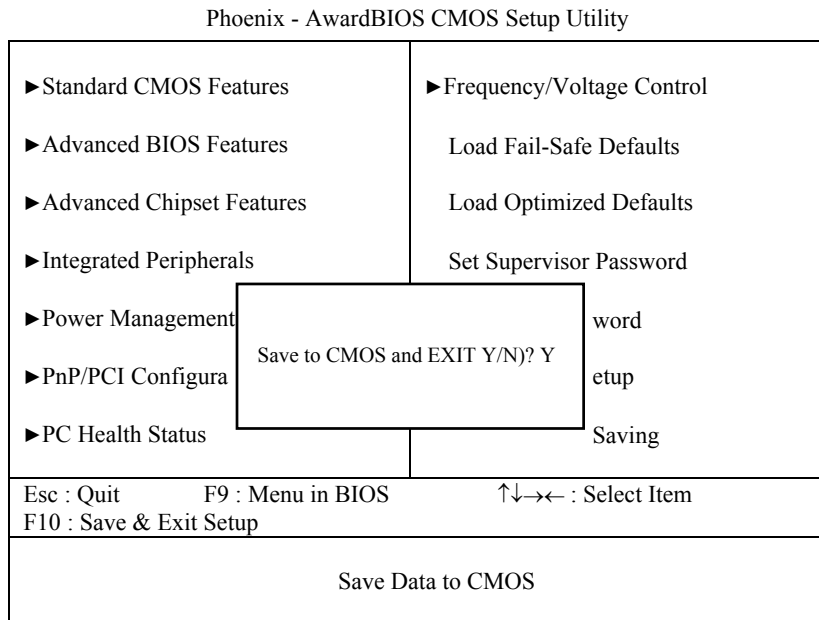
PASSWORD DISABLED!!!  
Press any key to continue...

Press the < Enter > key again and the password will be disabled. Once the password is disabled, you can enter Setup freely.



### 4-14. SAVE & EXIT SETUP

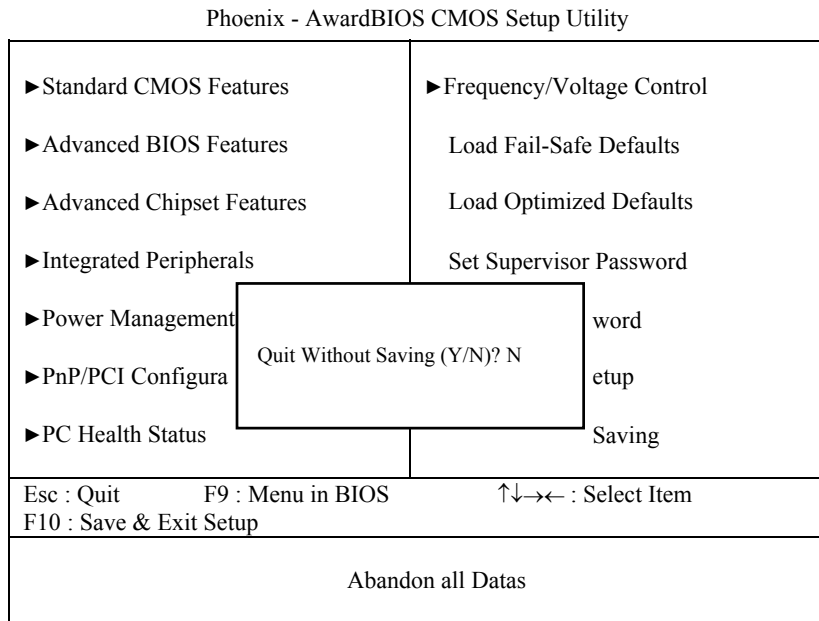
After you have completed adjusting all the settings as required, you must remember to save these setting into the CMOS RAM. To save the settings, select "SAVE & EXIT SETUP" and press <Enter>, a display will be shown as follows:



When you confirm that you wish to save the settings, your system will be automatically restarted and the changes you have made will be implemented. You may always call up the setup program at any time to adjust any of the individual items by pressing the <Del> key during boot up.

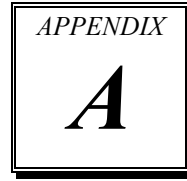
### 4-15. EXIT WITHOUT SAVING

If you wish to cancel any changes you have made, you may select the “EXIT WITHOUT SAVING” and the original setting stored in the CMOS will be retained. The screen will be shown as below:





# ***EXPANSION BUS***



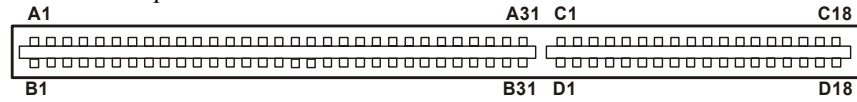
This appendix indicates the pin assignments.

Section includes:

- ISA BUS Pin Assignment
- PCI BUS Pin Assignment

## ISA BUS PIN ASSIGNMENT

The ISA-bus connector is divided into two sets : one consists of 62 pins; the other consists of 36 pins.

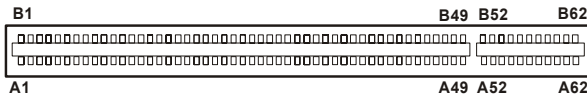


The pin assignments as follows :

B		A		D		C	
PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	GND	A1	-I/O CH CHK	D1	-MEMCS16	C1	SBHE
B2	RESET	A2	SD07	D2	-I/OCS16	C2	LA23
B3	+5V	A3	SD06	D3	IRQ10	C3	LA22
B4	IRQ9	A4	SD05	D4	IRQ11	C4	LA21
B5	-5V	A5	SD04	D5	IRQ12	C5	LA20
B6	DRQ2	A6	SD03	D6	IRQ15	C6	LA19
B7	-12V	A7	SD02	D7	IRQ14	C7	LA18
B8	OWS	A8	SD01	D8	-DACK0	C8	LA17
B9	+12V	A9	SD00	D9	DRQ0	C9	-MEMR
B10	GND	A10	-I/O CH RDY	D10	-DACK5	C10	-MEMW
B11	-SMEMW	A11	AEN	D11	DRQ5	C11	SD08
B12	-SMEMR	A12	SA19	D12	-DACK6	C12	SD09
B13	-IOW	A13	SA18	D13	DRQ6	C13	SD10
B14	-IOR	A14	SA17	D14	-DACK7	C14	SD11
B15	-DACK3	A15	SA16	D15	DRQ7	C15	SD12
B16	-DRQ3	A16	SA15	D16	+5V	C16	SD13
B17	-DACK1	A17	SA14	D17	-MASTER	C17	SD14
B18	-DRQ1	A18	SA13	D18	GND	C18	SD15
B19	-REFRESH	A19	SA12				
B20	BCLK	A20	SA11				
B21	IRQ7	A21	SA10				
B22	IRQ6	A22	SA09				
B23	IRQ5	A23	SA08				
B24	IRQ4	A24	SA07				
B25	IRQ3	A25	SA06				
B26	-DACK2	A26	SA05				
B27	T/C	A27	SA04				
B28	BALE	A28	SA03				
B29	+5V	A29	SA02				
B30	OSC	A30	SA01				
B31	GND	A31	SA00				

## PCI BUS PIN ASSIGNMENT

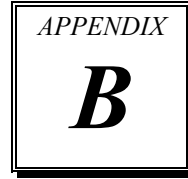
Like ISA-BUS connector, the PCI-BUS edge connector is also divided into two sets: one consists of 98-pin; the other consists of 22-pin. The pin assignments are as follows :



B		A		B		A	
PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	-12V	A1	TRST#	B31	+3.3V	A31	AD18
B2	TCK	A2	+12V	B32	AD17	A32	AD16
B3	GND	A3	TMS	B33	C/BE2#	A33	+3.3V
B4	TDO	A4	TDI	B34	GND	A34	FRAME#
B5	+5V	A5	+5V	B35	IRDY#	A35	GND
B6	+5V	A6	INTA#	B36	+3.3V	A36	TRDY#
B7	INTB#	A7	INTC#	B37	DEVSEL#	A37	GND
B8	INTD#	A8	+5V	B38	GND	A38	STOP#
B9	REQ3#	A9	CLKC	B39	LOCK#	A39	+3.3V
B10	REQ1#	A10	+5V(I/O)	B40	PERR#	A40	SDONE
B11	GNT3#	A11	CLKD	B41	+3.3V	A41	SB0#
B12	GND	A12	GND	B42	SERR#	A42	GND
B13	GND	A13	GND	B43	+3.3V	A43	PAR
B14	CLKA	A14	GNT1#	B44	C/BE1#	A44	AD15
B15	GND	A15	RST#	B45	AD14	A45	+3.3V
B16	CLKB	A16	+5V(I/O)	B46	GND	A46	AD13
B17	GND	A17	GNT0#	B47	AD12	A47	AD11
B18	REQ0#	A18	GND	B48	AD10	A48	GND
B19	+5V(I/O)	A19	REQ2#	B49	GND	A49	AD09
B20	AD31	A20	AD30	B52	AD08	A52	C/BE0#
B21	AD29	A21	+3.3V	B53	AD07	A53	+3.3V
B22	GND	A22	AD28	B54	+3.3V	A54	AD06
B23	AD27	A23	AD26	B55	AD05	A55	AD04
B24	AD25	A24	GND	B56	AD03	A56	GND
B25	+3.3V	A25	AD24	B57	GND	A57	AD02
B26	C/BE3#	A26	GNT2#	B58	AD01	A58	AD00
B27	AD23	A27	+3.3V	B59	+5V(I/O)	A59	+5V(I/O)
B28	GND	A28	AD22	B60	ACK64#	A60	REQ64#
B29	AD21	A29	AD20	B61	+5V	A61	+5V
B30	AD19	A30	GND	B62	+5V	A62	+5V



# ***TECHNICAL SUMMARY***



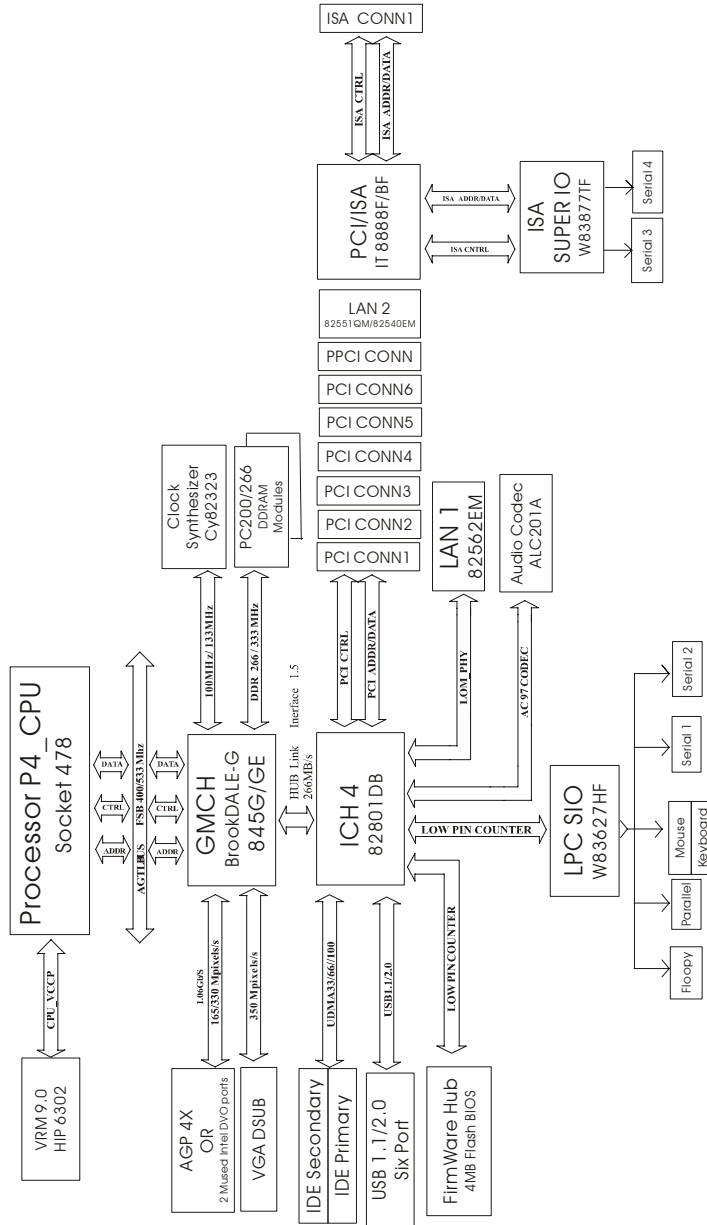
This section introduce you the maps concisely.

Sections include:

- Block Diagram
- Interrupt Map
- RTC (Standard) RAM Bank
- Timer & DMA Channels Map
- I / O & Memory Map



BLOCK DIAGRAM



**INTERRUPT MAP**

<b>IRQ</b>	<b>ASSIGNMENT</b>
0	System TIMER interrupt from TIMER-0
1	Keyboard output buffer full
2	Cascade for IRQ 8-15
3	Serial port 2
4	Serial port 1
5	Parallel Port 2
6	Floppy Disk adapter
7	Parallel port 1
8	RTC clock
9	Available
10	Serial port 3
11	Serial port 4
12	Available
13	Math coprocessor
14	Hard Disk adapter
15	Available

## RTC (STANDARD) RAM BANK

CODE	ASSIGNMENT
00h	Seconds
01h	Second alarm
02h	Minutes
03h	Minutes alarm
04h	Hours
05h	Hours alarm
06h	Day of week
07h	Day of month
08h	Month
09h	Year
0Ah	Status register A
0Bh	Status register B
0Ch	Status register C
0Dh	Status register D
0Eh-7Fh	114 Bytes of User RAM

## **TIMER & DMA CHANNELS MAP**

### **Timer Channel Map :**

<b>Timer Channel</b>	<b>Assignment</b>
0	System timer interrupt
1	DRAM Refresh request
2	Speaker tone generator

### **DMA Channel Map :**

<b>DMA Channel</b>	<b>Assignment</b>
0	Available
1	Available
2	Floppy Disk adapter
3	Available
4	Cascade
5	Available
6	Available
7	Available

**I/O & MEMORY MAP**

**Fixed I/O Ranges Decoded by ICH2 :**

<b>I/O Address</b>	<b>Read Target</b>	<b>Write Target</b>	<b>Internal Unit</b>
00h-08h	DMA Controller	DMA Controller	DMA
09h-0Eh	Reserved	DMA Controller	DMA
0Fh	DMA Controller	DMA Controller	DMA
10h-18h	DMA Controller	DMA Controller	DMA
19h-1Eh	Reserved	DMA Controller	DMA
1Fh	DMA Controller	DMA Controller	DMA
20h-21h	Interrupt Controller	Interrupt Controller	Interrupt
24h-25h	Interrupt Controller	Interrupt Controller	Interrupt
28h-29h	Interrupt Controller	Interrupt Controller	Interrupt
2Ch-2Dh	Interrupt Controller	Interrupt Controller	Interrupt
2Eh-2Fh	LPC SIO	LPC SIO	Forwarder to LPC
30h-31h	Interrupt Controller	Interrupt Controller	Interrupt
34h-35h	Interrupt Controller	Interrupt Controller	Interrupt
38h-39h	Interrupt Controller	Interrupt Controller	Interrupt
3Ch-3Dh	Interrupt Controller	Interrupt Controller	Interrupt
40h-42h	Timer/Counter	Timer/Counter	PIT (8254)
43h	Reserved	Timer/Counter	PIT
4E-4F	LPC SIO	LPC SIO	Forwarder to LPC
50h-52h	Timer/Counter	Timer/Counter	PIT
53h	Reserved	Timer/Counter	PIT
60h	Microcontroller	Microcontroller	Forwarder to LPC
61h	NMI Controller	NMI Controller	Processor I/F
62h	Microcontroller	Microcontroller	Forwarder to LPC
63h	NMI Controller	NMI Controller	Processor I/F
64h	Microcontroller	Microcontroller	Forwarder to LPC
65h	NMI Controller	NMI Controller	Processor I/F
66h	Microcontroller	Microcontroller	Forwarder to LPC
67h	NMI Controller	NMI Controller	Processor I/F
70h	Reserved <sup>5</sup>	NMI & RTC controller	RTC
71h	RTC Controller	RTC Controller	RTC
72h	RTC Controller	NMI & RTC controller	RTC
73h	RTC Controller	RTC Controller	RTC
74h	RTC Controller	NMI & RTC controller	RTC
75h	RTC Controller	RTC Controller	RTC
76h	RTC Controller	NMI & RTC controller	RTC
77h	RTC Controller	RTC Controller	RTC

<b>I/O Address</b>	<b>Read Target</b>	<b>Write Target</b>	<b>Internal Unit</b>
80h	DMA Controller	DMA controller & LPC/PCI	DMA
81h-83h	DMA Controller	DMA Controller	DMA
84h-86h	DMA Controller	DMA Controller & LPC or PCI	DMA
87h	DMA Controller	DMA Controller	DMA
88h	DMA Controller	DMA Controller & LPC or PCI	DMA
89h-8Bh	DMA Controller	DMA Controller	DMA
8Ch-8Eh	DMA Controller	DMA Controller & LPC or PCI	DMA
08Fh	DMA Controller	DMA Controller	DMA
90h-91h	DMA Controller	DMA Controller	DMA
92h	Reset Generator	Reset Generator	Processor I/F
93h-9Fh	DMA Controller	DMA Controller	DMA
A0h-A1h	Interrupt Controller	Interrupt Controller	Interrupt
A4h-A5h	Interrupt Controller	Interrupt Controller	Interrupt
A8h-A9h	Interrupt Controller	Interrupt Controller	Interrupt
ACh-ADh	Interrupt Controller	Interrupt Controller	Interrupt
B0h-B1h	Interrupt Controller	Interrupt Controller	Interrupt
B2h-B3h	Power Management	Power Management	Power Management
B4h-B5h	Interrupt Controller	Interrupt Controller	Interrupt
B8h-B9h	Interrupt Controller	Interrupt Controller	Interrupt
BCh-BDh	Interrupt Controller	Interrupt Controller	Interrupt
C0h-D1h	DMA Controller	DMA Controller	DMA
D2h-DDh	Reserved	DMA Controller	DMA
DEh-DFh	DMA Controller	DMA Controller	DMA
F0h	See Note 3	FERR# /IGNNE#/ Interrupt Controller	Processor interface
170h-177h	IDE Controller <sup>1</sup>	IDE Controller <sup>1</sup>	Forwarded to IDE
1F0h-1F7h	IDE Controller <sup>2</sup>	IDE Controller <sup>2</sup>	Forwarded to IDE
376h	IDE Controller <sup>1</sup>	IDE Controller <sup>1</sup>	Forwarded to IDE
3F6h	IDE Controller <sup>2</sup>	IDE Controller <sup>2</sup>	Forwarded to IDE
4D0h-4D1h	Interrupt Controller	Interrupt Controller	Interrupt
CF9h	Reset Generator	Reset Generator	Processor interface

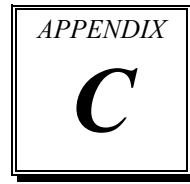
Notes:

1. Only if IDE Standard I/O space is enabled for Primary Drive. Otherwise, the target is PCI.
2. Only if IDE Standard I/O space is enabled for Secondary Drive. Otherwise, the target is PCI.
3. If POS\_DEC\_EN bit is enabled, reads from F0h will not be decoded by the ICH2. If POS\_DEC\_EN is not enabled, reads from F0h will forward to LPC.

**Memory Decode Ranges From Processor Perspective :**

<b>Memory Range</b>	<b>Target</b>	<b>Dependency/Comments</b>
0000 0000h-000D FFFFh 0010 0000-TOM (Top of Memory)	Main Memory	TOM registers in Host Controller
000E 0000h-000F FFFFh	FWH	Bit 7 in FWH Decode Enable Register is set
FEC0 0000h-FEC0 0100h	I/O APIC inside ICH2	
FFC0 0000h-FFC7 FFFFh FF80 0000h-FF87 FFFFh	FWH	Bit 0 in FWH Decode Enable Register
FFC8 0000h-FFCF FFFFh FF88 0000h-FF8F FFFFh	FWH	Bit 1 in FWH Decode Enable Register
FFD0 0000h-FFD7 FFFFh FF90 0000h-FF97 FFFFh	FWH	Bit 2 in FWH Decode Enable Register is set
FFD8 0000h-FFDF FFFFh FF98 0000h-FF9F FFFFh	FWH	Bit 3 in FWH Decode Enable Register is set
FFE0 0000h-FFE7 FFFFh FFA0 0000h-FFA7 FFFFh	FWH	Bit 4 in FWH Decode Enable Register is set
FFE8 0000h-FEF7 FFFFh FFA8 0000h-FFAF FFFFh	FWH	Bit 5 in FWH Decode Enable Register is set
FFF0 0000h-FFF7 FFFFh FFB0 0000h-FFB7 FFFFh	FWH	Bit 6 in FWH Decode Enable Register is set
FFF8 0000h-FFFF FFFFh FFB8 0000h-FFBF FFFFh	FWH	Always Enabled. The top two 64K blocks of this range can be swapped as described in Section 6.4.1.
FF70 0000h-FF7F FFFFh FF30 0000h-FF3F FFFFh	FWH	Bit 3 in FWH Decode Enable 2 Register is set
FF60 0000h-FF6F FFFFh FF20 0000h-FF2F FFFFh	FWH	Bit 2 in FWH Decode Enable 2 Register is set
FF50 0000h-FF5F FFFFh FF10 0000h-FF1F FFFFh	FWH	Bit 1 in FWH Decode Enable 2 Register is set
FF40 0000h-FF4F FFFFh FF00 0000h-FF0F FFFFh	FWH	Bit 0 in FWH Decode Enable 2 Register is set
Anywhere in 4GB range	D110 LAN Controller	Enable via BAR in Device 29:Function 0 (D110 LAN Controller)
All Other	PCI	None

# ***TROUBLE SHOOTING***



This section outlines the error messages that may occur when you operate the system. It also gives you the suggestions on solving the problems.

Section includes:

- Trouble Shooting for Error Messages
- Trouble Shooting for POST Code



## **TROUBLE SHOOTING FOR ERROR MESSAGES**

The following information gives you the error messages and the troubleshooting. Please adjust your systems according to the messages below. And make sure all the components and connectors are in proper position and firmly attached. If the errors still encountered, please contact with your distributor for maintenance.

### **POST BEEP :**

Currently there are two kinds of beep codes in BIOS. One code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps. The other code indicates that your DRAM error has occurred. This beep code consists of a single long beep repeatedly.

### **CMOS BATTERY HAS FAILED :**

This message informs you that the CMOS battery is no longer functional. The user should replace it.

### **CMOS CHECKSUM ERROR :**

This message informs you that the CMOS is incorrect. This error may have caused by a weak battery. Check the battery and replace if necessary.

### **DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER :**

No boot device was found. This could mean that either a boot drive was not detected or the drive does not contain proper system boot files. Insert a system disk into Drive A: and press < Enter >. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also make sure the disk is formatted as a boot device. Then reboot the system.

### **DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP :**

Type of diskette drive installed in the system is different from the CMOS definition. Run Setup to reconfigure the drive type correctly.

**DISPLAY SWITCH IS SET INCORRECTLY :**

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

**DISPLAY TYPE HAS CHANGED SINCE LAST BOOT :**

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

**EISA CONFIGURATION CHECKSUM ERROR  
PLEASE RUN EISA CONFIGURATION UTILITY :**

The EISA non-volatile RAM checksum is incorrect or cannot correctly read the EISA slot. This can indicate either the EISA non-volatile memory has become corrupt or the slot has been configured incorrectly. Also be sure the card is installed firmly in the slot.

**EISA CONFIGURATION IS NOT COMPLETE  
PLEASE RUN EISA CONFIGURATION UTILITY :**

The slot configure information stored in the EISA non-volatile memory is incomplete.

Note: When either of these errors appear, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

**ERROR ENCOUNTERED INITIALIZING HARD DRIVE :**

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

**ERROR INITIALIZING HARD DISK CONTROLLER :**

Cannot initialize controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive is selected in Setup. Also check to see if any jumper needs to be set correctly on the hard drive.

**FLOPPY DISK CNTRLR ERROR OR NO CNTRLR PRESENT :**

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

**INVALID EISA CONFIGURATION**

**PLEASE RUN EISA CONFIGURATION UTILITY :**

The non-volatile memory containing EISA configuration information was programmed incorrectly or has become corrupt. Re-run EISA configuration utility to correctly program the memory.

Note: When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

**KEYBOARD ERROR OR NO KEYBOARD PRESENT :**

Cannot initialize the keyboard. Make sure that the keyboard is properly attached and no keys being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

**MEMORY ADDRESS ERROR AT ... :**

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

**MEMORY PARITY ERROR AT ... :**

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

**MEMORY SIZE HAS CHANGED SINCE LAST BOOT :**

Memory has been added or removed since the last boot. In EISA mode use Configuration Utility to reconfigure the memory configuration. In ISA mode enter Setup and enter the new memory size in the memory fields.

**MEMORY VERIFY ERROR AT ... :**

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

**OFFENDING ADDRESS NOT FOUND :**

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

**OFFENDING SEGMENT :**

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

**PRESS A KEY TO REBOOT :**

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

**PRESS F1 TO DISABLE NMI, F2 TO REBOOT :**

When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot, or you can reboot the system with the NMI enabled.

**RAM PARITY ERROR – CHECKING FOR SEGMENT :**

Indicates a parity error in Random Access Memory.

**Should be Empty But EISA Board Found**

**PLEASE RUN EISA CONFIGURATION UTILITY :**

A valid board ID was found in a slot that was configured as having no board ID.

Note: When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

**Should Have EISA Board But Not Found**

**PLEASE RUN EISA CONFIGURATION UTILITY :**

The board installed is not responding to the ID request, or no board ID has been found in the indicated slot.

Note: When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

**Slot Not Empty :**

Indicates that a slot designated as empty by the EISA Configuration Utility actually contains a board.

**SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT :**

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

**Wrong Board in Slot**

**PLEASE RUN EISA CONFIGURATION UTILITY :**

The board ID does not match the ID stored in the EISA non-volatile memory.

Note: When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

**FLOPPY DISK(S) FAIL (80) :**

Unable to reset floppy subsystem.

**FLOPPY DISK(S) FAIL (40) :**

Floppy type mismatch.

**Hard Disk(S) Fail (80) :**

Hard Disk Drive reset failed.

**Hard Disk(S) Fail (40) :**

Hard Disk Drive controller diagnostics failed.

**Hard Disk(S) Fail (20) :**

Hard Disk Drive initialization error.

**Hard Disk(S) Fail (10) :**

Unable to recalibrate fixed disk.

**Hard Disk(S) Fail (08) :**

Sector Verify failed.

**Keyboard is locked out – Unlock the key :**

BIOS detect the keyboard is locked. P17 of keyboard controller is pulled low.

**Keyboard error or no keyboard present :**

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

**Manufacturing POST loop :**

System will repeat POST procedure infinitely while the P15 of keyboard controller is pull low. This is also used for M/B burn in test.

**BIOS ROM checksum error – System halted :**

The checksum of ROM address F0000H-FFFFFFH is bad.

**Memory test fail :**

BIOS reports the memory test fail if the onboard memory is tested error.

## **TROUBLE SHOOTING FOR POST CODES**

The lists below indicate you the post codes. Please follow the instruction to adjust your system. If the error still occurred, please contact with your distributor for maintenance.

- CFh** : Test CMOS R/W functionality.
- C0h** : Early chipset initialization
  - Disable shadow RAM
  - Disable L2 cache (socket 7 or below)
  - Program basic chipset registers
- C1h** : Detect memory
  - Auto-detection of DRAM size, type and ECC
  - Auto-detection of L2 cache (socket 7 or below)
- C3h** : Expand compressed BIOS code to DRAM.
- C5h** : Call chipset hook to copy BIOS block to E000 & F000 shadow RAM.
- 0h1** : Expand the Xgroup codes locating in physical address 1000:0
- 02h** : Reserved.
- 03h** : Initial Superio\_Early\_Init switch.
- 04h** : Reserved.
- 05h** :
  1. Blank out screen
  2. Clear CMOS error flag
- 06h** : Reserved
- 07h** :
  1. Clear 8042 interface
  2. Initialize 8042 self-test

- 08h** :
  - 1. Test special keyboard controller for Winbond 977 series Super I/O Chips.
  - 2. Enable keyboard interface.
- 09h** : Reserved
- 0Ah** :
  - 1. Disable PS/2 mouse interface (optional)
  - 2. Auto detect ports for keyboard & mouse followed by a port & interface swap (optional).
  - 3. Reset keyboard for Winbond 977 series Super I/O chips.
- 0Bh** : Reserved
- 0Ch** : Reserved
- 0Dh** : Reserved
- 0Eh** : Test F000h segment shadow to see whether it is R/W-able or not. If test fails, keep beeping the speaker.
- 0Fh** : Reserved
- 10h** : Auto detect flash type to load appropriate flash R/W codes into the run time area in F000 for ESCD and DMI support.
- 11h** : Reserved
- 12h** : Use walking 1's algorithm to check out interface in CMOS circuitry. Also set real-time clock power status, and then check for override.
- 13h** : Reserved
- 14h** : Program chipset default values into chipset. Chipset default values are MODBINable by OEM customers.
- 15h** : Reserved
- 16h** : Initial Early\_Init\_Onboard\_Generator switch.



- 17h** : Reserved
- 18h** : Detect CPU information including brand, SMI type (Cyrrix or Intel) and CPU level (586 or 686).
- 19h** : Reserved
- 1Ah** : Reserved
- 1Bh** : Initial interrupts vector table. If no special specified, all H/W interrupts are directed to SPURIOUS\_INIT\_HDLR & S/W interrupts to SPURIOUS\_soft\_HDLR.
- 1Ch** : Reserved
- 1Dh** : Initial EARLY\_PM\_INIT switch
- 1Eh** : Reserved
- 1Fh** : Load keyboard matrix (notebook platform)
- 20h** : Reserved
- 21h** : HPM initialization (notebook platform)
- 22h** : Reserved
- 23h** :
  1. Check validity of RTC value:  
e.g. a value of 5Ah is an invalid value for RTC minute.
  2. Load CMOS settings into BIOS stack. If CMOS checksum fails, use default value instead.
  3. Prepare BIOS resource map for PCI & PnP use. If ESCD is valid, take into consideration of the ESCD's legacy information.
  4. Onboard clock generator initialization. Disable respective clock resource to empty PCI & DIMM slots.
  5. Early PCI initialization:
    - Enumerate PCI bus number
    - Assign memory & I/O resource
    - Search for a valid VGA device & VGA BIOS, and put it into C000:0.

- 24h** : Reserved
- 25h** : Reserved
- 26h** : Reserved
- 27h** : Initialize INT 09 buffer
- 28h** : Reserved
- 29h** :
  1. Program CPU internal mtrr (P6 & PII) for 0-640K memory address.
  2. Initialize the APIC for Pentium class CPU.
  3. Program early chipset according to CMOS setup.  
Example: onboard IDE controller.
  4. Measure CPU speed.
  5. Invoke video BIOS.
- 2Ah** : Reserved
- 2Bh** : Reserved
- 2Ch** : Reserved
- 2Dh** :
  1. Initialize multi-language
  2. Put information on screen display, including Award title, CPU type, CPU speed ...
- 2Eh** : Reserved
- 2Fh** : Reserved
- 30h** : Reserved
- 31h** : Reserved
- 32h** : Reserved
- 33h** : Reset keyboard except Winbond 977 series Super I/O chips.

- 34h** : Reserved
- 35h** : Reserved
- 36h** : Reserved
- 37h** : Reserved
- 38h** : Reserved
- 39h** : Reserved
- 3Ah** : Reserved
- 3Bh** : Reserved
- 3Ch** : Test 8254
- 3Dh** : Reserved
- 3Eh** : Test 8259 interrupt mask bits for channel 1.
- 3Fh** : Reserved
- 40h** : Test 8259 interrupt mask bits for channel 2.
- 41h** : Reserved
- 42h** : Reserved
- 43h** : Test 8259 functionality.
- 44h** : Reserved
- 45h** : Reserved
- 46h** : Reserved
- 47h** : Initialize EISA slot

- 48h** : Reserved
- 49h** :
  1. Calculate total memory by testing the last double word of each 64K page.
  2. Program writes allocation for AMD K5 CPU.
- 4Ah** : Reserved
- 4Bh** : Reserved
- 4Ch** : Reserved
- 4Dh** : Reserved
- 4Eh** :
  1. Program MTRR of M1 CPU
  2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable range.
  3. Initialize the APIC for P6 class CPU.
  4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical.
- 4Fh** : Reserved
- 50h** : Initialize USB
- 51h** : Reserved
- 52h** : Test all memory (clear all extended memory to 0)
- 53h** : Reserved
- 54h** : Reserved
- 55h** : Display number of processors (multi-processor platform)
- 56h** : Reserved
- 57h** :
  1. Display PnP logo
  2. Early ISA PnP initialization
    - Assign CSN to every PnP device.

- 58h** : Reserved
- 59h** : Initialize the combined Trend Anti-Virus code.
- 5Ah** : Reserved
- 5Bh** : (Optional Feature)  
Show message for entering AWDFLASH.EXE from FDD  
(optional)
- 5Ch** : Reserved
- 5Dh** : 1. Initialize Init\_Onboard\_Super\_IO switch.  
2. Initialize Init\_Onboard\_AUDIO switch.
- 5Eh** : Reserved
- 5Fh** : Reserved
- 60h** : Okay to enter Setup utility; i.e. not until this POST stage can users  
enter the CMOS setup utility
- 61h** : Reserved
- 62h** : Reserved
- 63h** : Reserved
- 64h** : Reserved
- 65h** : Initialize PS/2 Mouse
- 66h** : Reserved
- 67h** : Prepare memory size information for function call:  
INT 15h ax=E820h
- 68h** : Reserved
- 69h** : Turn on L2 cache.

- 6Ah** : Reserved
- 6Bh** : Program chipset registers according to items described in Setup and Auto-configuration table.
- 6Ch** : Reserved
- 6Dh** :
  1. Assign resources to all ISA PnP devices.
  2. Auto assign ports to onboard COM ports if the corresponding item in Setup is set to "AUTO".
- 6Eh** : Reserved
- 6Fh** :
  1. Initialize floppy controller.
  2. Set up floppy related fields in 40:hardware.
- 70h** : Reserved
- 71h** : Reserved
- 72h** : Reserved
- 73h** : (Optional Feature)  
Enter AWDFLASH.EXE if:
  - AWDFLASH is found in floppy drive.
  - ALT+F2 is pressed
- 74h** : Reserved
- 75h** : Detect & install all IDE devices: HDD, LS120, ZIP, CDROM.....
- 76h** : Reserved
- 77h** : Detect serial ports and parallel ports
- 78h** : Reserved
- 79h** : Reserved
- 7Ah** : Detect and install co-processor

- 7Bh** : Reserved
- 7Ch** : Reserved
- 7Dh** : Reserved
- 7Eh** : Reserved
- 7Fh** :
  - 1. Switch back to text mode if full screen logo is supported.
  - If errors occur, report errors and wait for keys
  - If no errors occur or F1 key is pressed to continue:
    - \*Clear EPA or customization logo.
- 80h** : Reserved
- 81h** : Reserved
- 82h** :
  - 1. Call chipset power management hook.
  - 2. Recover the text font used by EPA logo (not for full screen logo).
  - 3. If password is set, ask for password.
- 83h** : Save all data in stack back to CMOS.
- 84h** : Initialize ISA PnP boot devices.
- 85h** :
  - 1. USB final initialization.
  - 2. NET PC: Build SYSID structure.
  - 3. Switch screen back to text mode.
  - 4. Setup ACPI table at top of memory.
  - 5. Invoke ISA adapter ROMs.
  - 6. Assign IRQs to PCI devices.
  - 7. Initialize APM.
  - 8. Clear noise of IRQs.
- 86h** : Reserved
- 87h** : Reserved

- 88h** : Reserved
- 89h** : Reserved
- 90h** : Reserved
- 91h** : Reserved
- 92h** : Reserved
- 93h** : Read HDD boot sector information for Trend Anti-Virus code.
- 94h** :
  - 1. Enable l2 cache.
  - 2. Program boot up speed.
  - 3. Chipset final initialization.
  - 4. Power management final initialization.
  - 5. Clear screen and display summary table
  - 6. Program K6 write allocation.
  - 7. Program P6 class write combining.
- 95h** :
  - 1. Program daylight saving.
  - 2. Update keyboard LED and typematic rate.
- 96h** :
  - 1. Build MP table.
  - 2. Build and update ESCD.
  - 3. Set CMOS century to 20h or 19h.
  - 4. Load CMOS time into DOS timer tick.
  - 5. Build MSIRQ routing table.
- FFh** : Boot attempt (INT 19h)



