

CHAPTER 2 INSTALLATION

2-1 SIM MODULES INSTALLATION

The OPTi-895 486 Green motherboard can be expanded from 2MB to 128 MB by using 256K,1M,4M,16M types of SIM Modules.

The following table provides various configurations of SIM Modules installed in Memory Bank 0-1.

JP34: 1-2 (SIM 9, 10 use single density SIM, please refer figure 2-1.)

BANK 0 30pin SIMMs SIMMs1-4	BANK 0 72pin SIMM SIMM 9	BANK 1 72pin SIMM SIMM10	TOTAL MEMORY
256KBX4	NONE	1MB	2MB
NONE	1MB	1MB	2MB
1MBX4	NONE	NONE	4MB
NONE	4MB	NONE	4MB
256KBX4	NONE	4MB	5MB
NONE	1MB	4MB	5MB
1MBX4	NONE	4MB	8MB
NONE	4MB	4MB	8MB
4MBX4	NONE	NONE	16MB
NONE	16MB	NONE	16MB
NONE	1MB	16MB	17MB
256KBX4	NONE	16MB	17MB
1MBX4	NONE	16MB	20MB
NONE	4MB	16MB	20MB
4MBX4	NONE	16MB	32MB
NONE	16MB	16MB	32MB
16MBX4	NONE	NONE	64MB
NONE	64MB	NONE	64MB
16MBX4	NONE	64MB	128MB
NONE	64MB	64MB	128MB

JP34: 2-3 (SIM 9, 10 Use double density SIM, please refer figure 2-2)

BANK 0 SIM 1-4	BANK 0,1 72pin SIMM SIM 9	BANK 2,3 72pin SIMM SIM 10	TOTAL MEMORY
NONE	2MB	2MB	4MB
NONE	8MB	NONE	8MB
NONE	8MB	8MB	16MB

2-2 SIM MODULES LOCATION

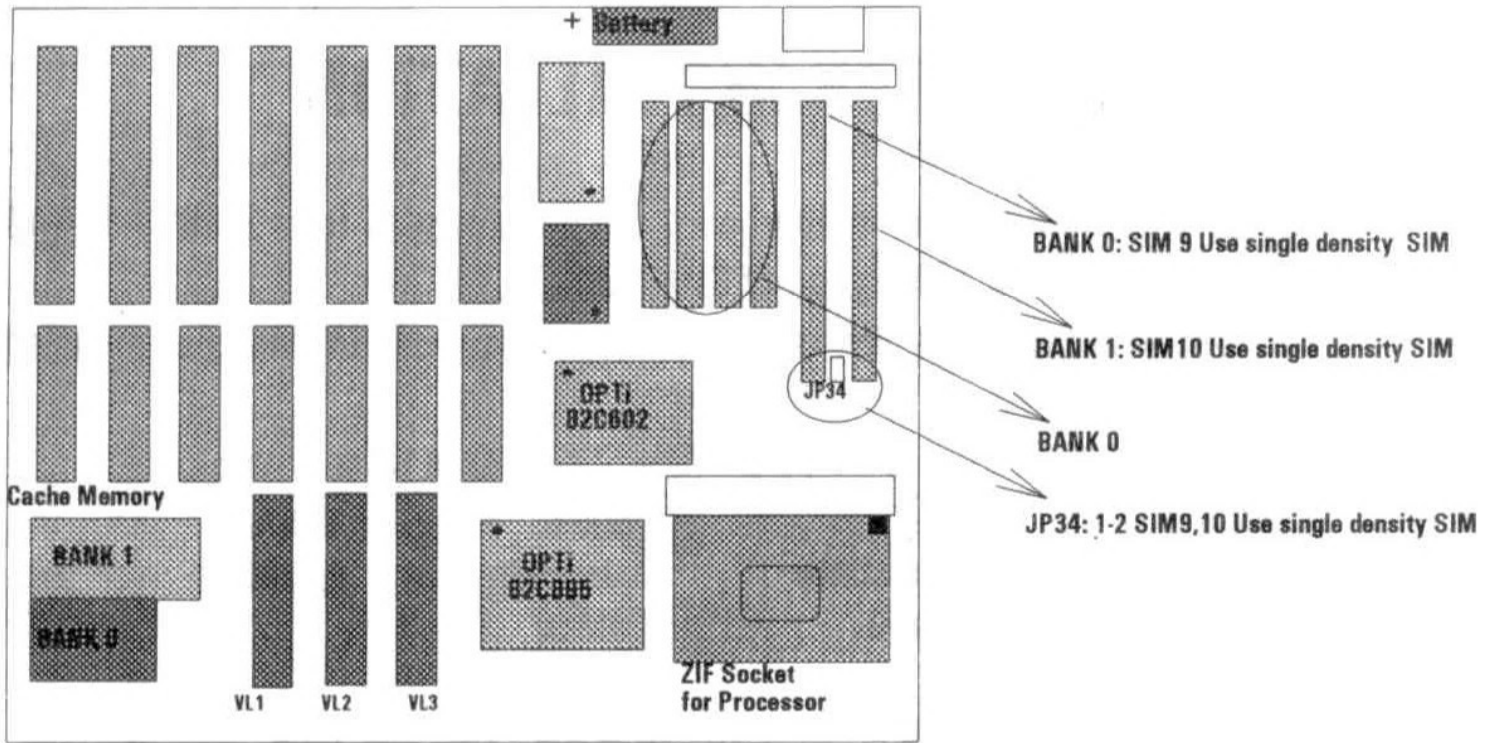


Figure 2-1

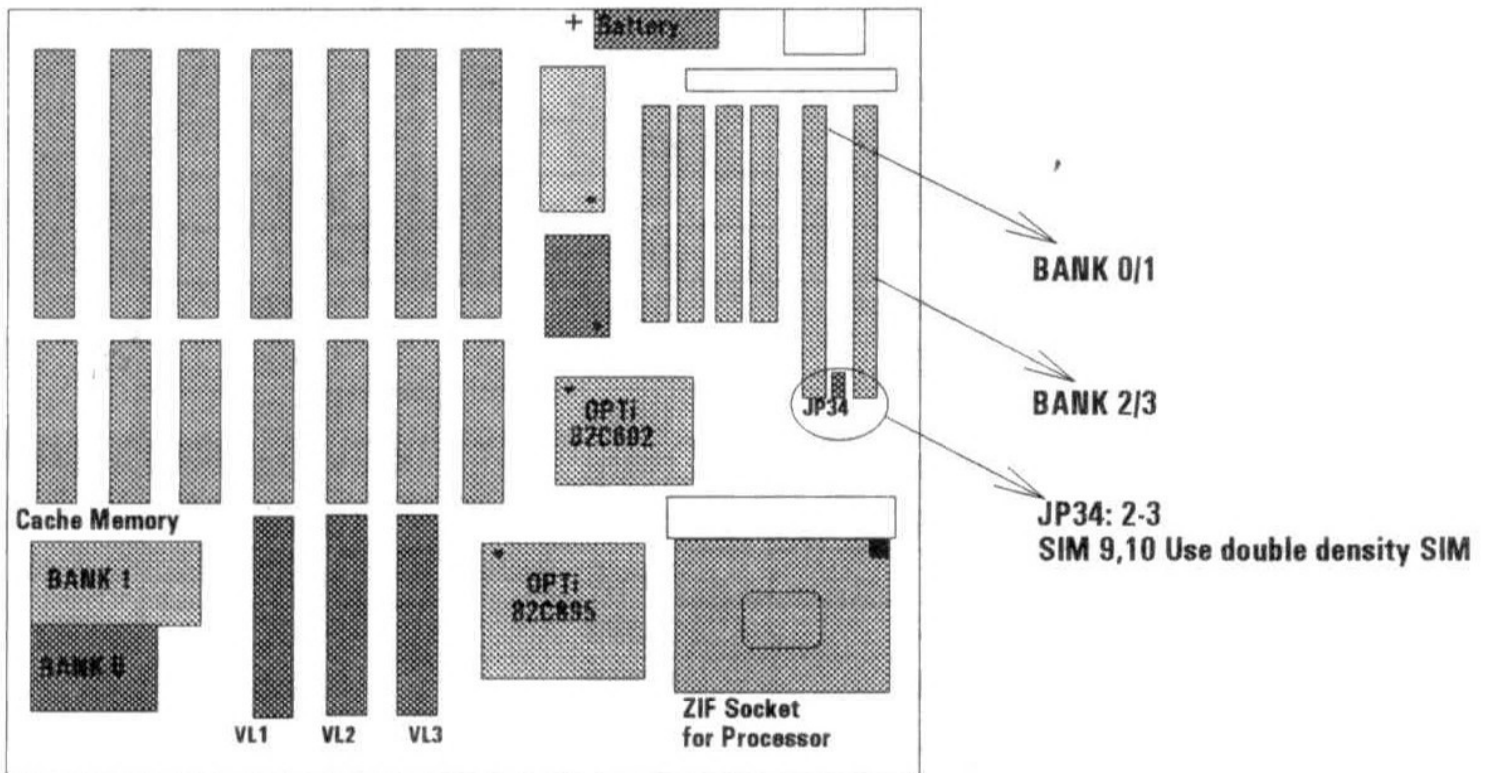


Figure 2-2

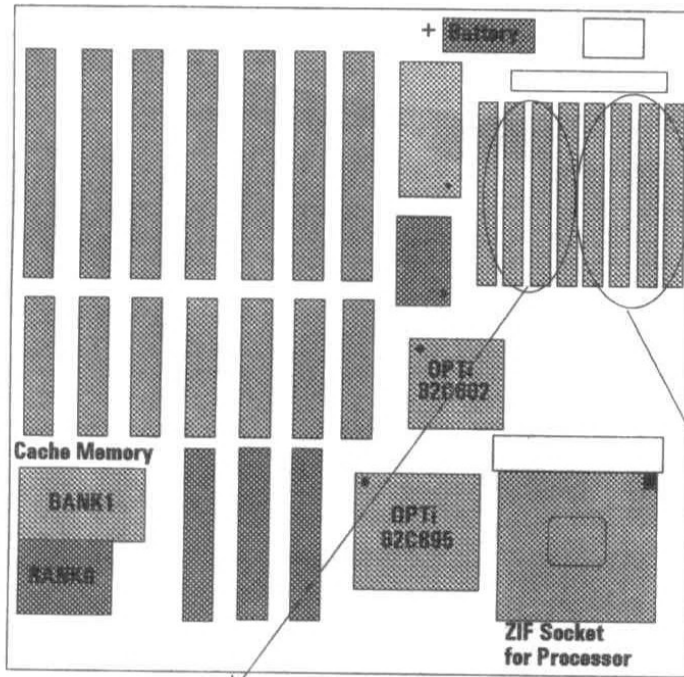


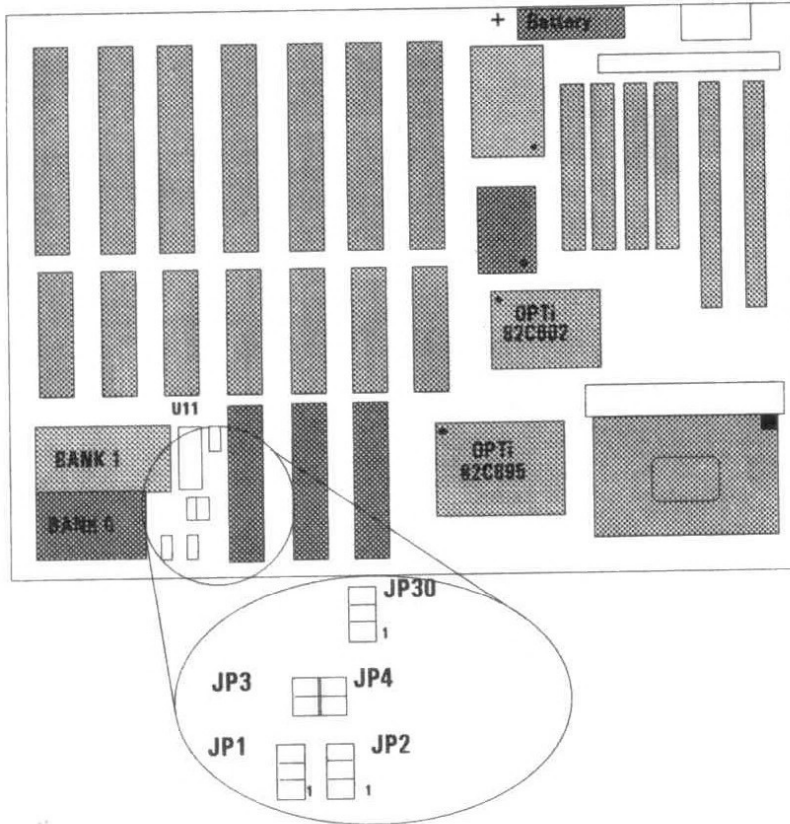
Figure 2-3

BANK 0,1: Use 30pin SIM Module, please refer figure 2-3.

BANK0 30pin SIMMs SIM 1-4	BANK 1 30pin SIMMs SIM 5-8	TOTAL MEMORY
256KBX4	256KBX4	2MB
1MBX4	NONE	4MB
256KBX4	1MBX4	5MB
1MBX4	1MBX4	8MB
4MBX4	NONE	16MB
256KBX4	4MBX4	17MB
1MBX4	4MBX4	20MB
4MBX4	4MBX4	32MB
16MBX4	NONE	64MB
16MBX4	16MBX4	128MB

2-3 SRAM INSTALLATION

The motherboard can support cache memory from 64K to 256K bytes. Any of 8KX8 or 32KX8 can use on the motherborad.



Cache Configuration Size

Cache Size	TAG RAM	Data RAM	JP1	JP2	JP3	JP4	JP30
64K	8KX8 (U11)	BANK 0,1 8KX8 (U1-U4) (U7-U10)					
128K	8KX8 (U11)	BANK 0 32KX8 (U1-U4)					
256K	32KX8 (U11)	BANK 0,1 32KX8 (U1-U4) (U7-U10)					

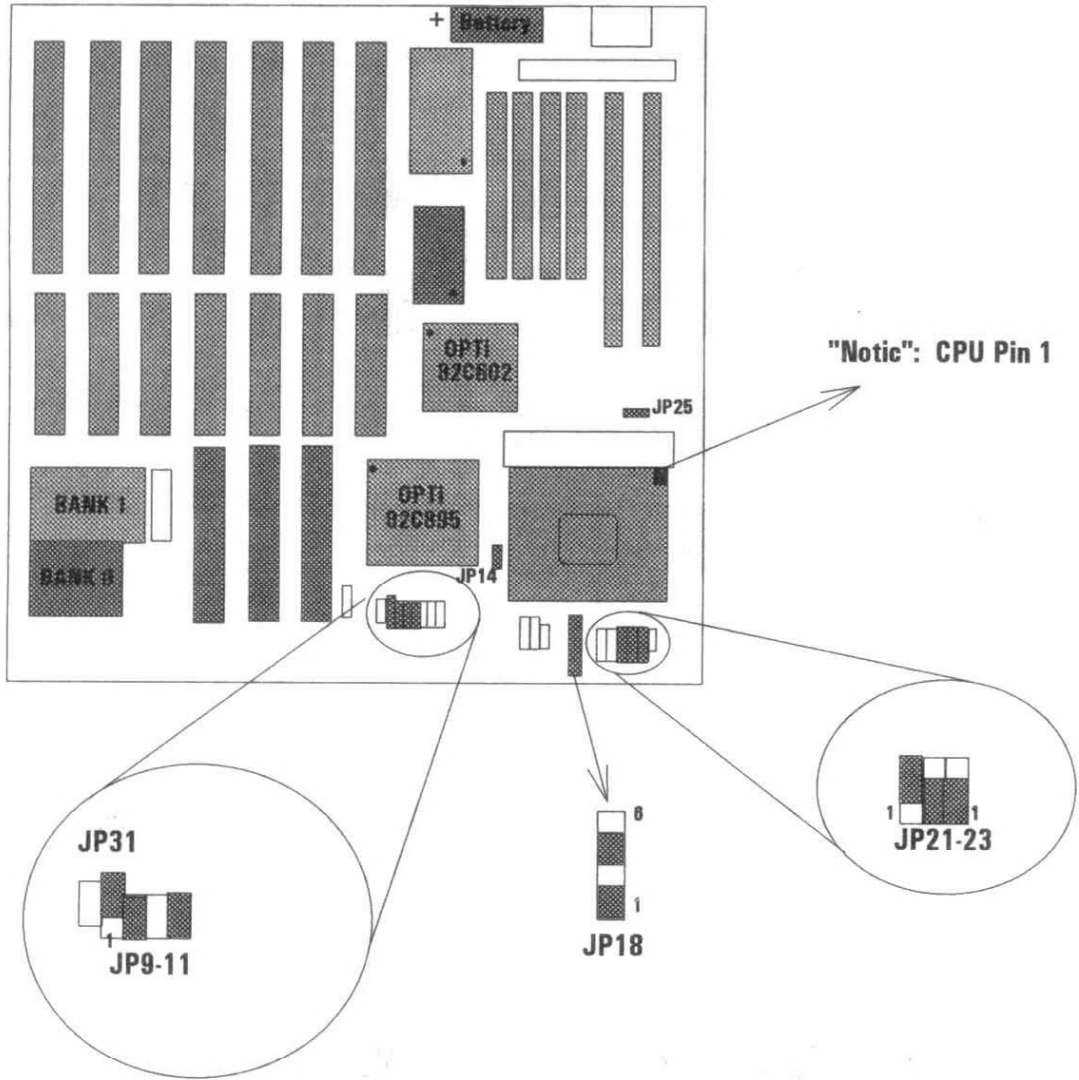
**** NOTE ****

BANK 0: U1,U2,U3,U4

BANK 1: U7,U8,U9,U10

2-4 CPU INSTALLATION
















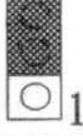








Please notice CPU to be installed on the ZIF socket on correct Pin 1 location, or it will be destroyed.



CPU TYPE SELECTION:

CPU TYPE	JP21	JP22	JP23
INTEL 486DX/DX2/DX4,P24D AMD 486DX/DX2 CYRIX M7			
INTEL 486SX/SX2 CYRIX M6			
P24D			

CPU SPEED SELECTION:

CLOCK RATE	JP9	JP10	JP11	JP31
486DX-25/ DX2-50				
486DX-33/ DX2-66				
486DX-40/ 486DX2-80				
486DX-50				
486DX4-75 *				
486DX4-100 *				

**** NOTE ****

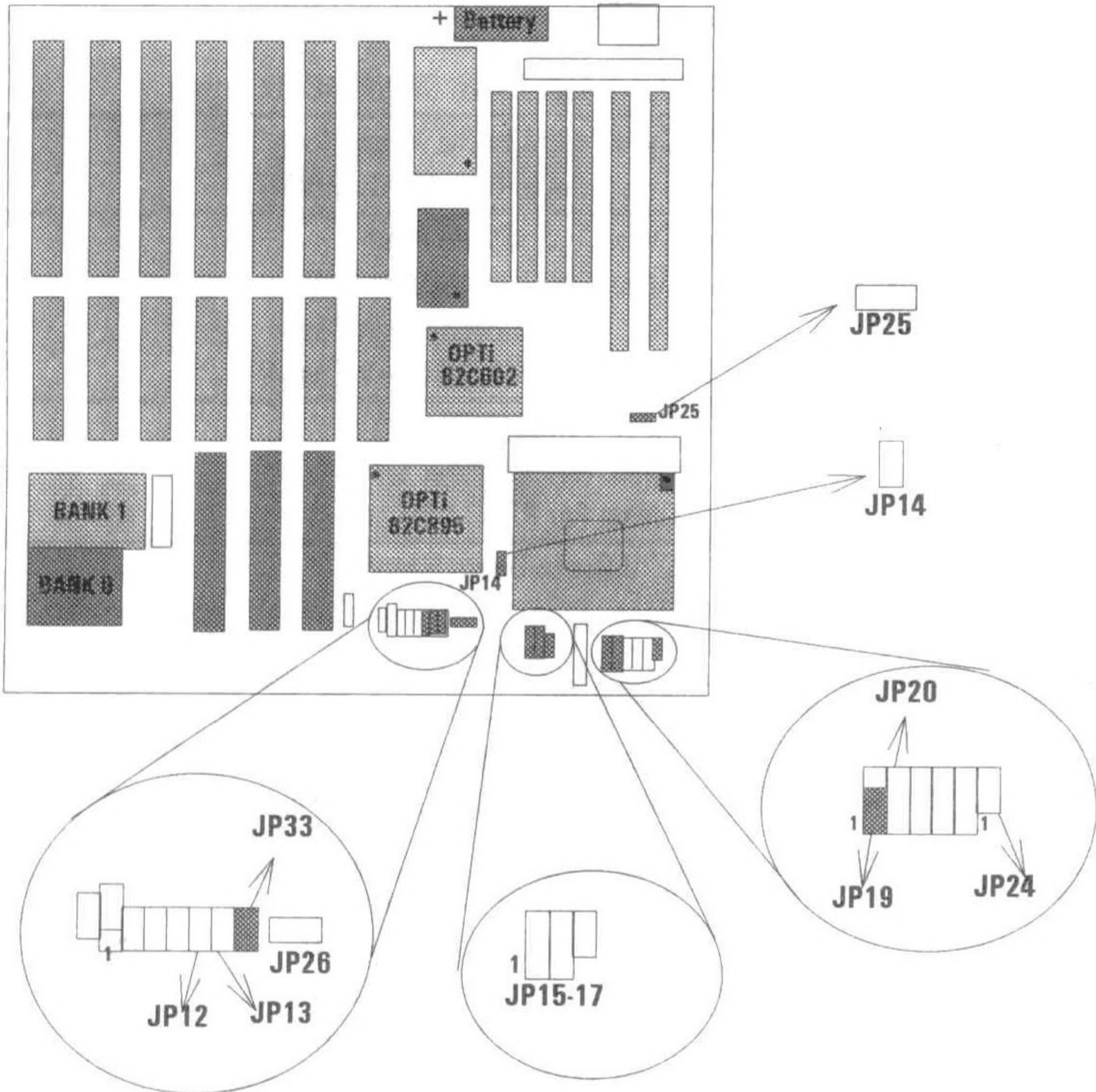
- ✓ * JP18 :1-2 , 4-5 Use 5V CPU Normal CPU 486DX2, DX,SX
2-3 , 5-6 Use 3.3V CPU (Example: 486DX4)

* 486DX4 IS P24C

JP80 ON for AMD 3.3V DX2-66,80

OFF for AMD 3.3V DX4-75-100

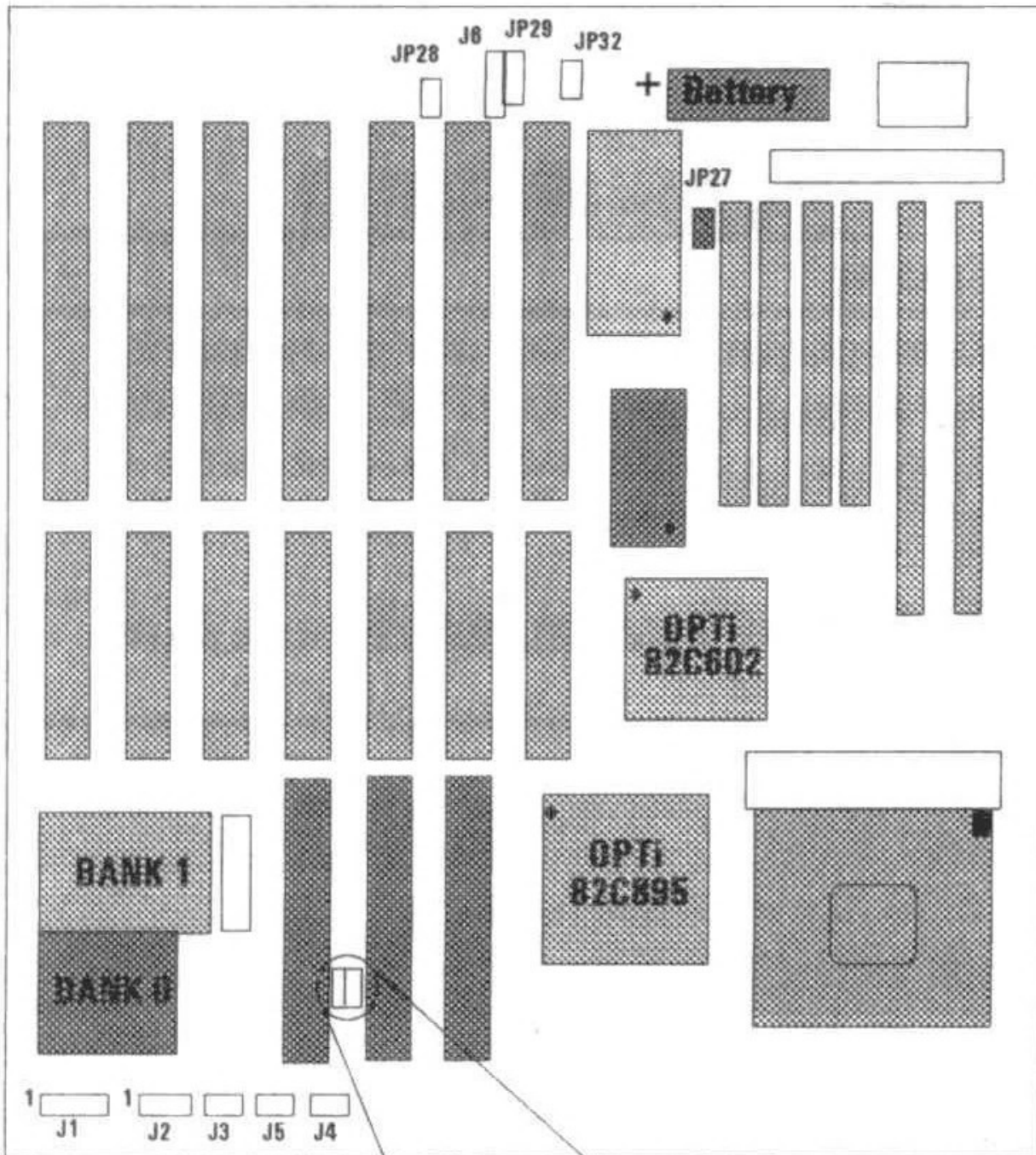
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OTHER JUMPER SETTINGS

CONNECTOR	DESCRIPTION
JP16	P24C Clock Speed Rate Open: For Normal CPU/P24C 3X Speed
JP19	Cyrix M6/M7 CPU Jumper 1-2: Normal/Other CPU, M6/M7
JP20	Cyrix Clock Speed Rate Open: For Normal/Other CPU 1-2 : M7/M6 1X Clock Speed 2-3 : M7/M6 2X Clock Speed
JP24	P24D Jumper Open: Normal/Other CPU Close: For P24D CPU only
JP12, JP13	CPU Green Down Speed Open, open (Green speed 8MHz/16MHz)
JP14, JP25, JP26, JP33	SMI CPU Jumper Open, Open, Open, Open: For Normal CPU Close, Close, Close, Close: For Intel SMI CPU, Intel S Serial CPU
JP15, JP17	Write Back CPU Function Jumper Open, Open: For Normal CPU 1-2, Open: For Cyrix Write Back CPU 2-3, Open: For Intel P24T Write Back Open, Close: Intel P24D Write Back
JP7	VESA Bus Cycle Wait State Fix on 2-3

2-5 FUNCTION OF CONNECTORS



CHAPTER 2 INSTALLATION

CONNECTOR	DESCRIPTION
J3	Reset Switch
J5	Turbo Led
J4	Turbo Switch Closed: Turbo Mode Opened: Non-Turbo Mode
J2	Speaker
J1	Key Lock Connector Pin 1: Power Led Pin 2: Unused Pin 3: Ground Pin 4: Key Lock Pin 5: Ground
JP5	VESA ID3 Wait State Default: Closed
JP6	VESA ID2 VESA Bus Speed Default: Closed
J6	External Battery Pin 1: Battery(+) Pin 2: Unused Pin 3: Ground Pin 4: Ground
JP32	Opened: Discharge Battery Closed: Charge Battery
JP29	Battery Type A. Charge: 1-2 Normal 2-3 Clear CMOS Data
JP27	Monitor Type Closed: Color Monitor Opened: Mono Monitor
JP28	Green Power Signal Input Connector