# SIIG, INC.
# I/O EXTENSION-X.2

| | |
|---|---|
| **Card Type** | Multi-I/O |
| **Chip Set** | Unidentified |
| **I/O Options** | Game port, parallel port, serial ports (2), clock/calendar |
| **Data Bus** | 8-bit ISA |



| CONNECTIONS | | | | |
|---|---|---|---|---|
| **Function** | **Label** | **Function** | | **Label** |
| Serial port 1 | J1 | Parallel port | | J3 |
| Serial port 2 | J2 | Game port | | J4 |

| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| **Function** | **Label** | **Position** |
| í  Parallel port IRQ is 7 | JP3 | Pins 3 & 4 closed |
| Parallel port IRQ is 5 | JP3 | Pins 1 & 2 closed |
| í  Parallel port address is 378h | SW1/3 | Off |
| Parallel port address is 278h | SW1/3 | On |
| í  Clock/calendar enabled | SW1/4 | On |
| Clock/calendar disabled | SW1/4 | Off |

| SERIAL PORT ADDRESSES | | | |
|---|---|---|---|
| **Port 1 Setting** | **Port 2 Setting** | **SW1/1** | **SW1/2** |
| COM1: (3F8h) | COM2: (2F8h) | Off | On |
| COM2: (2F8h) | COM3: (3E8h) | On | Off |
| COM3: (3E8h) | COM4: (2E8h) | On | On |
| Disabled | Disabled | Off | Off |

| SERIAL PORT 1 INTERRUPT | | | | |
|---|---|---|---|---|
| **Setting** | **JP1/A** | **JP1/B** | **JP1/C** | **JP1/D** |
| IRQ2 | Closed | Open | Open | Open |
| IRQ3 | Open | Closed | Open | Open |
| í  IRQ4 | Open | Open | Closed | Open |
| IRQ5 | Open | Open | Open | Closed |
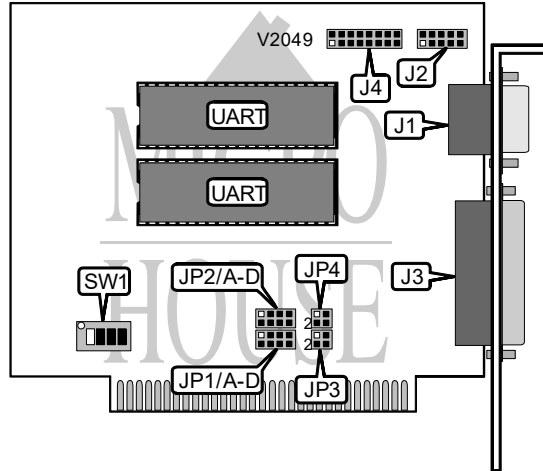
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| SERIAL PORT 2 INTERRUPT | | | | |
|---|---|---|---|---|
| **Setting** | **JP2/A** | **JP2/B** | **JP2/C** | **JP2/D** |
| IRQ2 | Closed | Open | Open | Open |
| IRQ3 | Open | Closed | Open | Open |
| í IRQ4 | Open | Open | Closed | Open |
| IRQ5 | Open | Open | Open | Closed |

| CLOCK/CALENDAR INTERRUPT | |
|---|---|
| **Setting** | **JP4** |
| í Disabled | Open |
| IRQ2 | Pins 3 & 4 closed |
| IRQ5 | Pins 1 & 2 closed |