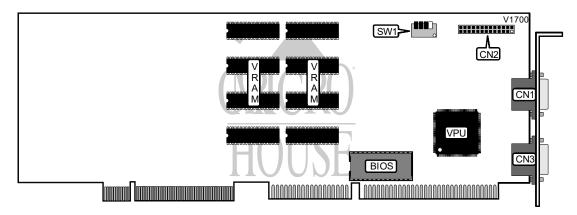
MIRO COMPUTER PRODUCTS, INC. MIRO 40SV TWIN/VL

Category Video
Video Types Supported XVGA
Video Processor S3 Vision964
Highest Resolution Supported Data Bus Type 32-bit VESA Local Bus

Memory Type VRAM

Maximum Onboard Memory 4MB (6MB when daughter board is installed)



CONNECTIONS						
Purpos ;	Location	Purpose	Location			
15-pin analog video port	CN1	15-pin analog video port (Daughter board)	CN3			
VESA feature connector	CN2					

SCREEN CONFIGURATION							
Setting	Monitor 1	Monitor 2	l aughter card	System	SW1/1	SW1/2	
Single or dual	VGA or SVGA	VGA or SVGA	Disabled	1	Off	On	
Multi-screen	(2) SVGA	VGA	Enabled	1	Off	Off	
Multi-screen	(2) SVGA	VGA	Enabled	2	On	N/A	
Multi-screen	SVGA	SVGA	Disabled	1	Off	On	
Multi-screen	SVGA	SVGA	Disabled	2	On	N/A	

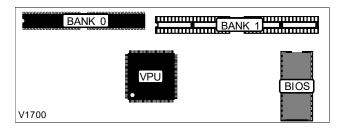
Note: If you have two Miro Twins in your system switch SW1/1 determines one system as system 1 (boot system) & the other as system 2. SW1/2 determines whether the computer boots on the Miro Twin board VPU or the daughter board VPU.

FACTORY CONFIGURED - DO NOT ALTER				
Switch	Position			
SW1/3	Unidentified			
SW1/4	Unidentified			

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VRAM CONFIGURATION (DAUGHTER BOARD)					
Size	Bank (Bank '				
1MB	(2) 256K x 16	NONE			
2MB	(2) 256K x 16	(2) 256K x 16			

MISCELLANEOUS TECHNICAL NOTE	
The daughter has a S3 Vision 864 video processor & a maximum resolution of 1408 x 1024.	