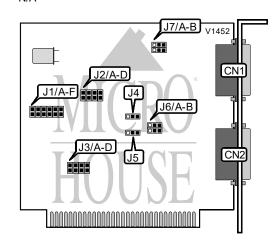
BOCA RESEARCH, INC. 10650

Card Type Chipset/Controller I/O Options Maximum DRAM

Serial Card Unidentified Serial ports (2) N/A



CONNECTIONS					
Purpos : Location Purpos : Locati					
Serial port 1	CN1	Serial port 2	CN2		

SERIAL PORT 1 CONFIGURATION					
C(M	7/A	J7/B	J4		
í COM3	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed		
COM1	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed		
COM2	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed		
COM4	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed		
Disabled	N/A	N/A	Pins 1 & 2 closed		

SERIAL PORT 2 CONFIGURATION					
C(M	6/A	J6/B	J5		
í COM4	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed		
COM3	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed		
COM1	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed		
COM2	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed		
Disabled	N/A	N/A	Pins 1 & 2 closed		

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SERIAL PORT 1 INTERRUPT SELECTION						
IRQ	J2/A	J2/B	J2/C	J2/D		
í IRQ5	Open	Open	Closed	Open		
IRQ3	Closed	Open	Open	Open		
IRQ4	Open	Closed	Open	Open		
IRQ9	Open	Open	Open	Closed		

	SERIAL PORT 2 INTERRUPT SELECTION					
IR	Q J3/A	J3/B	J3/C	J3/D		
í IRC	9 Oper	n Open	Open	Closed		
IRO	Q3 Close	d Open	Open	Open		
IRO	Q4 Oper	n Closed	Open	Open		
IRO	Q5 Oper	Open	Closed	Open		

SERIAL PORT I/O ADDRESS SELECTION/CLOCK SET						
A Idress Pair	J1/A	J1/B	J1/C	J1/D	J1/E	J1/F
í 2C0h, 2C8h	Closed	Closed	Open	Open	Open	Open
2D0h, 2D8h	Open	Open	Closed	Closed	Open	Open
3C0h, 3C8h	Open	Open	Open	Open	Closed	Closed