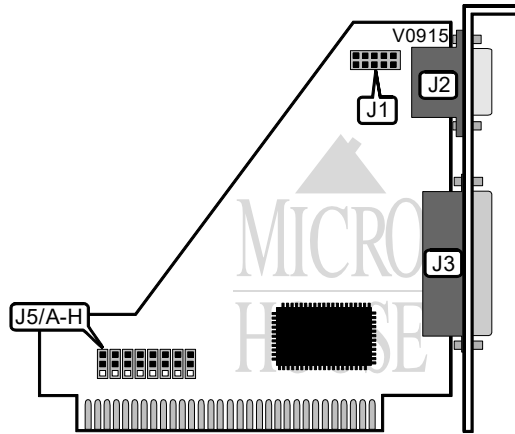# BOCA RESEARCH, INC.
I O A T 4 3

Card Type   MULTI I/O
Chipset Controller National Semiconductor
I/O Options   Parallel port, Serial port (2)
Maximum DRAM N/A

CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	J1	Serial port 1	J2
Parallel port	J3		

PARALLEL PORT BASE I/O ADDRESS SELECTION	
Address	J5/C
LPT1	Pins 1 & 2 closed
LPT2	Pins 2 & 3 closed

SERIAL PORT 1 BASE I/O ADDRESS SELECTION	
Address	J5/A
COM1	Pins 1 & 2 closed
COM3	Pins 2 & 3 closed

SERIAL PORT 2 BASE I/O ADDRESS SELECTION	
Address	J5/B
COM2	Pins 1 & 2 closed
COM4	Pins 2 & 3 closed

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PARALLEL PORT INTERRUPT SELECTION	
IRQ	J5/D
í 7	Pins 2 & 3 closed
5	Pins 1 & 2 closed

SERIAL PORT 1 INTERRUPT SELECTION				
IRQ	J5/E pins 1 & 2	J5/F pins 1 & 2	J5/G pins 1 & 2	J5/H pins 1 & 2
í 4	Open	Closed	Open	Open
3	Closed	Open	Open	Open
5	Open	Open	Closed	Open
9	Open	Open	Open	Closed

SERIAL PORT 2 INTERRUPT SELECTION				
IRQ	J5/E pins 2 & 3	J5/F pins 2 & 3	J5/G pins 2 & 3	J5/H pins 2 & 3
í 3	Closed	Open	Open	Open
4	Open	Closed	Open	Open
5	Open	Open	Closed	Open
9	Open	Open	Open	Closed