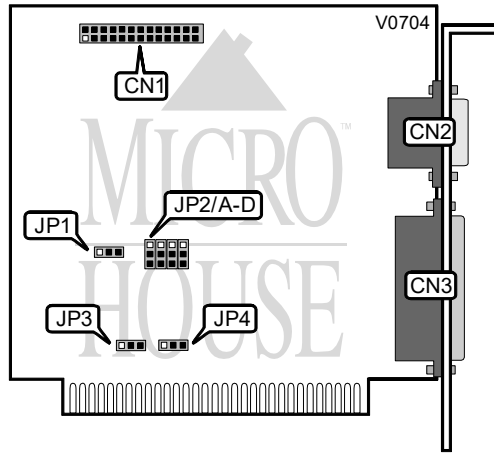


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Card Type
Chipset/Controller
I/O Options
Maximum DRAM

Multi-I/O
Unidentified
Parallel port, Serial ports (2)
N/A



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Parallel port	CN3
Serial port 1	CN2		

PARALLEL PORT CONFIGURATION	
LPT/Address	JP1
LPT1/378h	Pins 1 & 2 closed
LPT2/278h	Pins 2 & 3 closed
Disabled	All pins open

SERIAL PORT 1 CONFIGURATION - JP2				
COM/Address	umper A	umper B	umper C	umper D
COM1/3F8h	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open
COM2/2F8h	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open	Pins 1 & 2 open
COM3/3E8h	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed	Pins 1 & 2 open
COM4/2E8h	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 closed
Disabled	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open	Pins 1 & 2 open

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SERIAL PORT 2 CONFIGURATION - JP2				
COM/Address	umper A	umper B	umper C	umper D
COM2/2F8h	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open
COM1/3F8h	Pins 2 & 3 closed	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open
COM3/3E8h	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed	Pins 2 & 3 open
COM4/2E8h	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 closed
Disabled	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open	Pins 2 & 3 open

SERIAL PORTS 1 & 2 INTERRUPT SELECT			
Port 1	Port 2	P3	P4
IRQ4	IRQ3	Pins 2 & 3 closed	Pins 2 & 3 closed
IRQ3	IRQ4	Pins 1 & 2 closed	Pins 1 & 2 closed
IRQ3	Disabled	Pins 1 & 2 closed	All pins open
IRQ4	Disabled	All pins open	Pins 2 & 3 closed
Disabled	IRQ3	Pins 2 & 3 closed	All pins open
Disabled	IRQ4	All pins open	Pins 1 & 2 closed
Disabled	Disabled	All pins open	All pins open