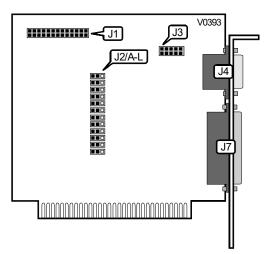
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IOAT45

Card Type Chipset/Controller I/O Options Maximum DRAM

Multi I/O card GM82C455

Parallel ports (2), serial ports (2) N/A



CONNECTIONS				
Purpos :	Location	Purpos :	Location	
Parallel port 2	J1	Serial port 1	J4	
Serial port 2	J3	Parallel port 1	J7	

SERIAL PORT CONFIGURATION					
Port 1 (J4)	rt 1 (J4)				
í COM1	Pins 1 & 2	Pins 1 & 2	COM2	Pins 1 & 2	Pins 1 & 2
COM3	Pins 1 & 2	Pins 2 & 3	COM4	Pins 1 & 2	Pins 2 & 3
Disabled	Pins 2 & 3	N/A	Disabled	Pins 2 & 3	N/A
Note: All Pins designated should be placed in the closed position.					

PRIMARY SERIAL PORT INTERRUPT SELECT				
IRQ	J2/I	J2/J	J2/K	J2/L
í IRQ4	N/A	Pins 1 & 2 closed	N/A	N/A
IRQ3	Pins 1 & 2 closed	N/A	N/A	N/A
IRQ5	N/A	N/A	Pins 1 & 2 closed	N/A
IRQ9	N/A	N/A	N/A	Pins 1 & 2 closed

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SECONDARY SERIAL PORT INTERRUPT SELECT				
IRQ	J2/I	J2/J	J2/K	J2/L
í IRQ3	Pins 2 & 3 closed	N/A	N/A	N/A
IRQ4	N/A	Pins 2 & 3 closed	N/A	N/A
IRQ5	N/A	N/A	Pins 2 & 3 closed	N/A
IRQ9	N/A	N/A	N/A	Pins 2 & 3 closed

PARALLEL PORT CONFIGURATION					
P(t 1 (J7)	, 2/C	P(† 2 (J1)	2/D		
í Enabled	Pins 1 & 2 closed	í Enabled	Pins 1 & 2 closed		
Disabled	Pins 2 & 3 closed	Disabled	Pins 2 & 3 closed		

PARALLEL PORT INTERRUPT SELECT					
P(t 1 (J7)	√2/G	P(t 2 (J1)	2/H		
í IRQ7	Pins 2 & 3 closed	í IRQ5	Pins 1 & 2 closed		
IRQ5	Pins 1 & 2 closed	IRQ7	Pins 2 & 3 closed		