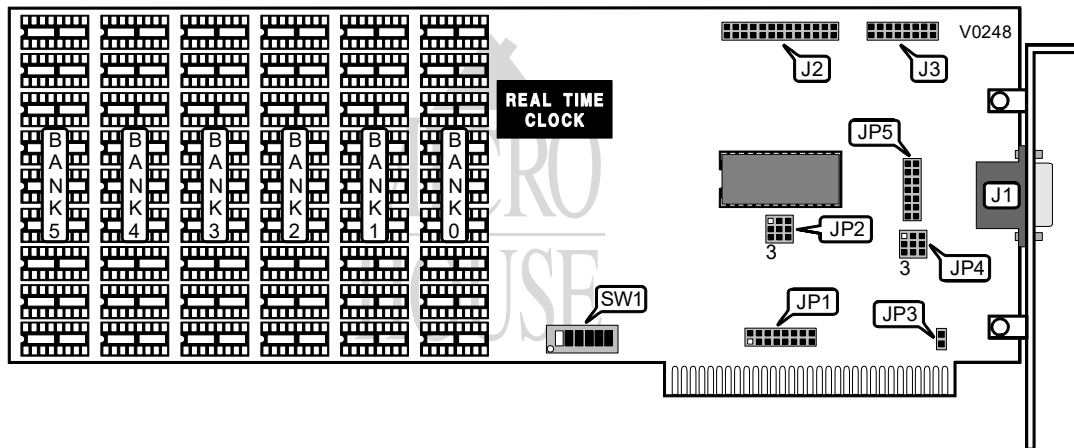


JAMECO ELECTRONIC COMPONENTS

JE1078

Card Type Multi I/O card
Chipset/Controller Unidentified
I/O Options Game port, memory, parallel port, serial port
Maximum DRAM 384KB



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port	J1	Game port	J3
Parallel port	J2		

SERIAL PORT CONFIGURATION	
Port 1 (J1)	JP2
COM1	Pins 2 & 3 closed
COM2	Pins 1 & 2 closed

SERIAL PORT NORMAL/FORCED TRUE CONFIGURATION	
Port 1	JP4
Normal	Pins 1 & 4, 2 & 5, 3 & 6 closed
Forced True	Pins 4 & 7, 5 & 8, 6 & 9 closed

SERIAL PORT INTERRUPT SELECTION	
Port 1	JP1
IRQ4	Pins 3 & 4 closed
IRQ3	Pins 2 & 4 closed

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JAMECO ELECTRONIC COMPONENTS

JE1078

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PARALLEL PORT CONFIGURATION			
LPT	RQ7	JP1/Pins 13 & 14	P2
if 1	if Enabled	Closed	Pins 5 & 6 closed
2	Enabled	Open	Pins 4 & 5 closed

CLOCK/CALENDAR CONFIGURATION		
Select	I/O Port Address	JP2
if Clock 2	if 2C0h - 2DFh	Pins 7 & 8 closed
Clock 1	340h - 35Fh	Pins 8 & 9 closed
Disabled	N/A	Open

CLOCK/CALENDAR INTERRUPT SELECTION	
IRQ	JP1
4	Pins 5 & 6 closed
5	Pins 7 & 8 closed
7	Pins 9 & 10 closed
2	Pins 11 & 12 closed

DRAM CONFIGURATION									
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5	SW1/4	SW1/5	SW1/6
64KB	(9) 4164	None	None	None	None	None	Off	Off	Off
128KB	(9) 4164	(9) 4164	None	None	None	None	Off	Off	On
192KB	(9) 4164	(9) 4164	(9) 4164	None	None	None	Off	On	Off
256KB	(9) 4164	(9) 4164	(9) 4164	(9) 4164	None	None	On	Off	Off
320KB	(9) 4164	(9) 4164	(9) 4164	(9) 4164	(9) 4164	None	On	Off	On
384KB	(9) 4164	(9) 4164	(9) 4164	(9) 4164	(9) 4164	(9) 4164	On	On	Off

STARTING MEMORY ADDRESS CONFIGURATION				
Starting Address	Max. RAM on JE1078	SW1/1	SW1/2	SW1/3
0KE (:00000)	3 4KB	Off	Off	Off
64KI (:10000)	3 4KB	Off	Off	On
128K (:20000)	3 4KB	Off	On	Off
192K (:30000)	3 4KB	Off	On	On
256K (:40000)	3 4KB	On	Off	Off
320K (:50000)	3 0KB	On	Off	On
384K (:60000)	2 6KB	On	On	Off
448K (:70000)	1 2KB	On	On	On

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PARITY CONFIGURATION	
Setting	JP3
Enabled	Closed
Disabled	Open

FACTORY CONFIGURED - DO NOT ALTER	
Jumper	Position
JP5	N/A