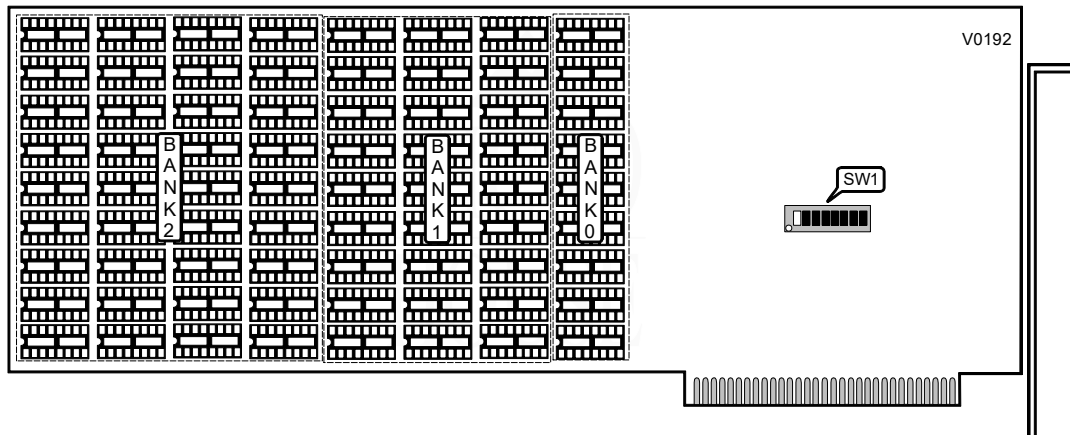


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Card Type Memory card
Chipset/Controller N/A
I/O Options N/A
Maximum DRAM 2048KB



I/O PORT ADDRESS SELECTION				
Address	SW1/1	SW1/2	SW1/3	SW1/4
í 268h - 269h	On	Off	Off	Off
208h - 209h	Off	On	Off	Off
218h - 219h	Off	Off	On	Off
258h - 259h	Off	Off	Off	On

FRAME SEGMENT ADDRESS SELECTION		
Address	SW1/5	SW1/6
í D000h	Off	On
E000h	On	Off

ADDITIONAL WAIT STATE SELECTION	
Setting	SW1/7
í No wait states added	Off
One wait state added	On

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PARITY CHECKING CONFIGURATION	
Setting	SW1/8
í Enabled	On
Disabled	Off

DRAM CONFIGURATION			
ize	B nk 0	B nk 1	B nk 2
256KB	(8) 41256	None	None
1024KB	(8) 41256	(24) 41256	None
2048KB	(8) 41256	(24) 41256	(32) 41256