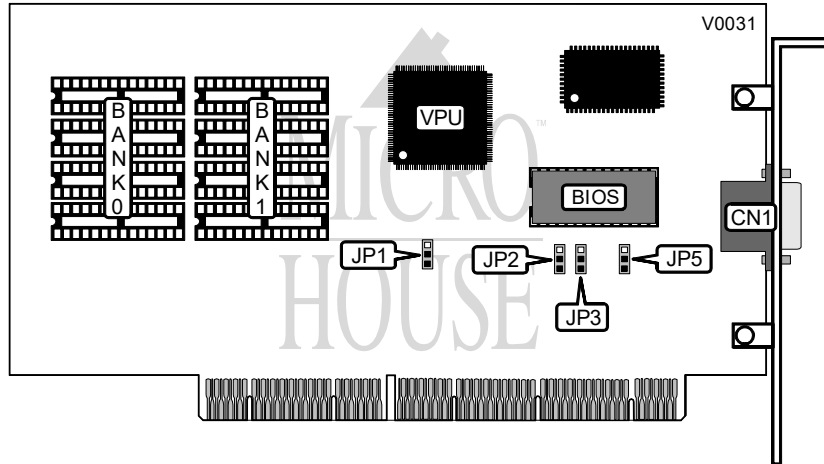


# ADVANCED INTEGRATION RESEARCH, INC. TSVGA EISA LOCAL BUS

<b>Category</b>	Video
<b>Video Types Supported</b>	XVGA
<b>Video Processor</b>	ET4000
<b>Highest Resolution Supported</b>	1280 x 1024
<b>Data Bus Type</b>	16-bit EISA
<b>Memory Type</b>	DRAM or VRAM
<b>Maximum Onboard Memory</b>	1024KB



CONNECTIONS	
Purpose	Location
15-pin analog video port	CN1

INTERRUPT SELECTION	
IRQ 2/9	JP1
Disabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed

WAIT STATE SELECTION			
Memory wait state	I/O wait state	P2	P3
1	3	Pins 1 & 2 closed	Pins 1 & 2 closed
2	4	Pins 2 & 3 closed	Pins 1 & 2 closed
3	3	Pins 1 & 2 closed	Pins 2 & 3 closed
4	4	Pins 2 & 3 closed	Pins 2 & 3 closed

Continued on next page . . .

ADVANCED INTEGRATION RESEARCH, INC.  
 TSVGA EISA LOCAL BUS

MAIN BOARD PROCESSOR SELECTION	
Type	JP5
80386DX	Pins 1 & 2 closed
80486SX	Pins 2 & 3 closed
80486DX	Pins 2 & 3 closed

DRAM/VRAM CONFIGURATION		
Size	Bank (	Bank )
512KB	(4) 256K x 4	NONE
1024KB	(4) 256K x 4	(4) 256K x 4
Note: TSVGA card may be configured from the factory with either DRAM or VRAM.		