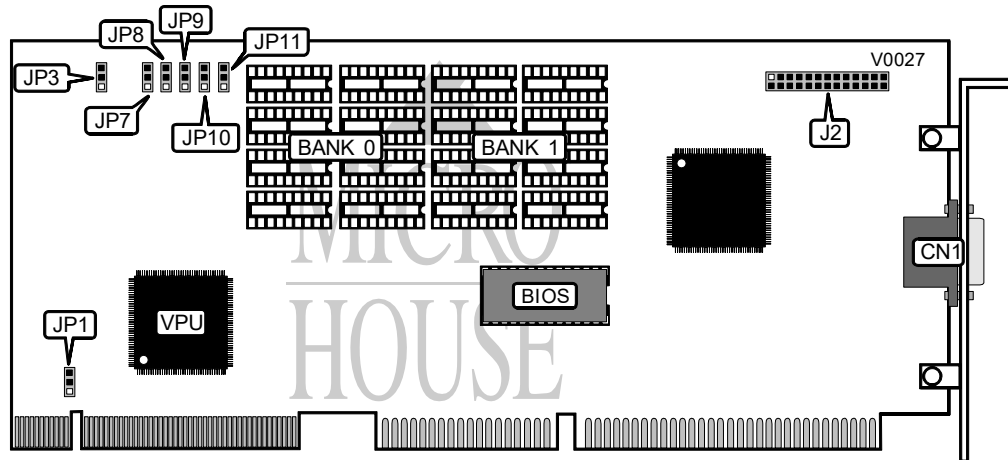


ADVANCED INTEGRATION RESEARCH, INC. OPTIMA VL-BUS SVGA

Category Video
Video Types Supported XVGA
Video Processor 86C805
Highest Resolution Supported 1280 x 1024
Data Bus Type 32-bit VESA Local Bus
Memory Type DRAM
Maximum Onboard Memory 2048KB



CONNECTIONS			
Purpose	Location	Purpose	Location
15-pin analog video port	CN1	VESA Feature connector	J2

FACTORY CONFIGURED - DO NOT ALTER	
Switch	Position
JP8	Pins 1 & 2 closed

INTERRUPT SELECTION	
IRQ 2/9	JP1
Disabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed

MULTIMEDIA CONFIGURATION	
Setting	JP7
if Multimedia card is not installed	Pins 2 & 3 closed
Multimedia card is installed	Pins 1 & 2 closed

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OPTIMA VL-BUS SVGA

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REFRESH RATE CONFIGURATION						
640 x 480	800 x 600	1024 x 768	1280 x 1024	JP9	JP10	JP11
60Hz	56Hz	43Hz	46Hz	Pins 1 & 2	Pins 1 & 2	Pins 1 & 2
60Hz	56Hz	43Hz	46Hz	Pins 1 & 2	Pins 1 & 2	Pins 2 & 3
60Hz	60Hz	60Hz	60Hz	Pins 1 & 2	Pins 2 & 3	Pins 1 & 2
72Hz	72Hz	72Hz	60Hz	Pins 1 & 2	Pins 2 & 3	Pins 2 & 3
60Hz	56Hz	43Hz	46Hz	Pins 2 & 3	Pins 1 & 2	Pins 1 & 2
60Hz	56Hz	43Hz	46Hz	Pins 2 & 3	Pins 1 & 2	Pins 2 & 3
60Hz	60Hz	60Hz	60Hz	Pins 2 & 3	Pins 2 & 3	Pins 1 & 2
72Hz	72Hz	72Hz	60Hz	Pins 2 & 3	Pins 2 & 3	Pins 2 & 3

Note: Pins designated should be in the closed position.

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	P3
1024KB	(8) 44256	None	Pins 2 & 3 closed
2048KB	(8) 44256	(8) 44256	Pins 1 & 2 closed