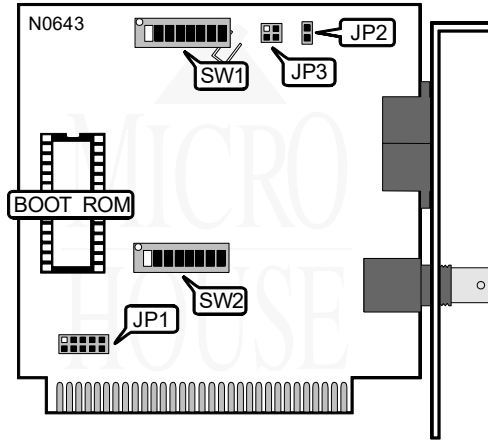


LONGSHINE MICROSYSTEM, INC.
LCS-8830 (REVISION A1)

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 8-bit ISA
Topology Linear/Star
Wiring Type RG62A/U 93ohm coaxial
 Unshielded twisted pair
Boot ROM Available



BASE I/O ADDRESS			
Address range	SW2/switch 6	SW2/switch 7	SW2/switch 8
260h - 26Fh	Closed	Closed	Closed
290h - 29Fh	Open	Closed	Closed
2E0h - 2EFh	Closed	Open	Closed
2F0h - 2FFh	Open	Open	Closed
300h - 30Fh	Closed	Closed	Open
350h - 35Fh	Open	Closed	Open
380h - 38Fh	Closed	Open	Open
3E0h - 3EFh	Open	Open	Open

BOOT ROM	
Setting	JP2
Disabled	Open
Enabled	Closed

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THE NETWORK INTERFACE CARD TECHNICAL GUIDE

LONGSHINE MICROSYSTEM, INC. LCS-8830 (REVISION A1)

... continued from previous page

BASE MEMORY ADDRESS						
RAM Address range	PROM address range	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5
C:0000h - C:07FFh	C:2000h - C:3FFFh	On	On	On	On	On
C:0800h - C:0FFFh	C:2000h - C:3FFFh	Off	On	On	On	On
C:1000h - C:17FFh	C:2000h - C:3FFFh	On	Off	On	On	On
C:1800h - C:1FFFh	C:2000h - C:3FFFh	Off	Off	On	On	On
C:4000h - C:47FFh	C:6000h - C:7FFFh	On	On	Off	On	On
C:4800h - C:4FFFh	C:6000h - C:7FFFh	Off	On	Off	On	On
C:5000h - C:57FFh	C:6000h - C:7FFFh	On	Off	Off	On	On
C:5800h - C:5FFFh	C:6000h - C:7FFFh	Off	Off	Off	On	On
C:C000h - C:C7FFh	C:E000h - C:FFFFh	On	On	On	Off	On
C:C800h - C:CFFFh	C:E000h - C:FFFFh	Off	On	On	Off	On
C:D000h - C:D7FFh	C:E000h - C:FFFFh	On	Off	On	Off	On
C:D000h - C:DFFFh	C:E000h - C:FFFFh	Off	Off	On	Off	On
D: 0000h - D:07FFh	D:2000h - D:3FFFh	On	On	Off	Off	On
D: 0800h - D:0FFFh	D:2000h - D:3FFFh	Off	On	Off	Off	On
D:1000h - D:17FFh	D:2000h - D:3FFFh	On	Off	Off	Off	On
D:1800h - D:1FFFh	D:2000h - D:3FFFh	Off	Off	Off	Off	On
D:4000h - D:47FFh	D:6000h - D:7FFFh	On	On	On	On	Off
D:4800h - D:4FFFh	D:6000h - D:7FFFh	Off	On	On	On	Off
D:5000h - D:57FFh	D:6000h - D:7FFFh	On	Off	On	On	Off
D:5800h - D:5FFFh	D:6000h - D:7FFFh	On	On	Off	Off	On
D:8000h - D:87FFh	D:A000h - D:BFFFh	Off	Off	On	Off	On
D:8800h - D:8FFFh	D:A000h - D:BFFFh	On	Off	On	Off	On
D:9000h - D:97FFh	D:A000h - D:BFFFh	Off	On	On	Off	On
D:9800h - D:9FFFh	D:A000h - D:BFFFh	On	On	On	Off	On
D:C000h - D:C7FFh	D:E000h - D:FFFFh	Off	Off	Off	On	On
D:C800h - D:CFFFh	D:E000h - D:FFFFh	On	Off	Off	On	On
D:D000h - D:D7FFh	D:E000h - D:FFFFh	Off	On	Off	On	On
D:D800h - D:DFFFh	D:E000h - D:FFFFh	On	On	Off	On	On
E:0000h - E:07FFh	E:2000h - E:3FFFh	Off	Off	On	On	On
E:0800h - E:0FFFh	E:2000h - E:3FFFh	On	Off	On	On	On
E:1000h - E:17FFh	E:2000h - E:3FFFh	Off	On	On	On	On
E:1800h - E:1FFFh	E:2000h - E:3FFFh	On	On	On	On	On

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LONGSHINE MICROSYSTEM, INC.
LCS-8830 (REVISION A1)

... continued from previous page

INTERRUPT REQUEST					
IRQ	JP1/1	JP1/2	JP1/3	JP1/4	JP1/5
IRQ2	Closed	Open	Open	Open	Open
IRQ3	Open	Closed	Open	Open	Open
IRQ4	Open	Open	Closed	Open	Open
IRQ5	Open	Open	Open	Closed	Open
IRQ7	Open	Open	Open	Open	Closed

RESPONSE AND RECONFIGURATION SETTINGS			
Response Time	Idle Time	Reconfiguration Time	JP3
78μ	86μ	840ms	Open
285μ	316μ	1680ms	Pins 1 & 2 closed
563μ	624μ	1680ms	Pins 3 & 4 closed
1130μ	1237μ	1680ms	Closed

NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1	On	On	On	On	On	On	On	Off
2	On	On	On	On	On	On	Off	On
3	On	On	On	On	On	On	Off	Off
4	On	On	On	On	On	Off	On	On
251	Off	Off	Off	Off	Off	On	Off	Off
252	Off	Off	Off	Off	Off	Off	On	On
253	Off	Off	Off	Off	Off	Off	On	Off
254	Off	Off	Off	Off	Off	Off	Off	On
255	Off	Off	Off	Off	Off	Off	Off	Off

Note: Node address 0 is used for messaging between nodes and must not be used.
 A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 1=128, 2=64, 3=32, 4=16, 5=8, 6=4, 7=2, 8=1. Turn Off the switches and add the values of the Off switches to obtain the correct node address. (On=0, Off=1)