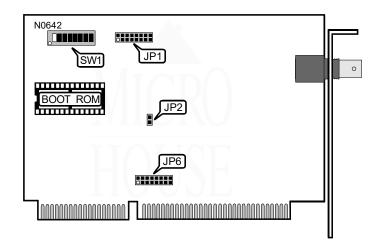
LONGSHINE MICROSYSTEM, INC. LCS-8630 (REVISION B1)

NIC Type Transfer Rate ARCnet 2.5Mbps Data Bus 8-bit ISA Linear/Star Topology

Wiring Type Boot ROM RG62A/U 93ohm coaxial

Available



BASE I/O ADDRESS								
Address range	JP1/6	JP1/7	JP1/8					
260h - 26Fh	Closed	Closed	Closed					
290h - 29Fh	Open	Closed	Closed					
2E0h - 2EFh	Closed	Open	Closed					
2F0h - 2FFh	Open	Open	Closed					
300h - 30Fh	Closed	Closed	Open					
350h - 35Fh	Open	Closed	Open					
380h - 38Fh	Closed	Open	Open					
3E0h - 3EFh	Open	Open	Open					

BOOT ROM					
Setting	JP2				
í Disabled	Open				
Enabled	Closed				

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BASE MEMORY ADDRESS									
RAM Address range	PROM address range	JP1/1	JP1/2	JP1/3	JP1/4	JP1/5			
C:0000h - C:07FFh	C:2000h - C:2FFFh	Closed	Closed	Closed	Closed	Closed			
C:0800h - C:0FFFh	C:2000h - C:2FFFh	Open	Closed	Closed	Closed	Closed			
C:1000h - C:17FFh	C:2000h - C:2FFFh	Closed	Open	Closed	Closed	Closed			
C:1800h - C:1FFFh	C:2000h - C:2FFFh	Open	Open	Closed	Closed	Closed			
C:4000h - C:47FFh	C:6000h - C:7FFFh	Closed	Closed	Open	Closed	Closed			
C:4800h - C:4FFFh	C:6000h - C:7FFFh	Open	Closed	Open	Closed	Closed			
C:5000h - C:57FFh	C:6000h - C:7FFFh	Closed	Open	Open	Closed	Closed			
C:5800h - C:5FFFh	C:6000h - C:7FFFh	Open	Open	Open	Closed	Closed			
C:C000h - C:C7FFh	C:E000h - C:FFFFh	Closed	Closed	Closed	Open	Closed			
C:C800h - C:CFFFh	C:E000h - C:FFFFh	Open	Closed	Closed	Open	Closed			
C:D000h - C:D7FFh	C:E000h - C:FFFFh	Closed	Open	Closed	Open	Closed			
C:0000h - C:DFFFh	C:E000h - C:FFFFh	Open	Open	Closed	Open	Closed			
D: 0000h - D:07FFh	D:2000h - D:3FFFh	Closed	Closed	Open	Open	Closed			
D: 0800h - D:0FFFh	D:2000h - D:3FFFh	Open	Closed	Open	Open	Closed			
D:1000h - D:17FFh	D:2000h - D:3FFFh	Closed	Open	Open	Open	Closed			
D:1800h - D:1FFFh	D:2000h - D:3FFFh	Open	Open	Open	Open	Closed			
D:4000h - D:47FFh	D:6000h - D:7FFFh	Closed	Closed	Closed	Closed	Open			
D:4800h - D:4FFFh	D:6000h - D:7FFFh	Open	Closed	Closed	Closed	Open			
D:5000h - D:57FFh	D:6000h - D:7FFFh	Closed	Open	Closed	Closed	Open			
D:5800h - D:5FFFh	D:6000h - D:7FFFh	Open	Open	Closed	Closed	Open			
D:8000h - D:87FFh	D:A000h - D:BFFFh	Closed	Closed	Open	Closed	Open			
D:8800h - D:8FFFh	D:A000h - D:BFFFh	Open	Closed	Open	Closed	Open			
D:9000h - D:97FFh	D:A000h - D:BFFFh	Closed	Open	Open	Closed	Open			
D:9800h - D:9FFFh	D:A000h - D:BFFFh	Open	Open	Open	Closed	Open			
D:C000h - D:C7FFh	D:E000h - D:FFFFh	Closed	Closed	Closed	Open	Open			
D:C800h - D:CFFFh	D:E000h - D:FFFFh	Open	Closed	Closed	Open	Open			
D:D000h - D:D7FFh	D:E000h - D:FFFFh	Closed	Open	Closed	Open	Open			
D:D800h - D:DFFFh	D:E000h - D:FFFFh	Open	Open	Closed	Open	Open			
E:0000h - E:07FFh	E:2000h - E:3FFFh	Closed	Closed	Open	Open	Open			
E:0800h - E:0FFFh	E:2000h - E:3FFFh	Open	Closed	Open	Open	Open			
E:1000h - E:17FFh	E:2000h - E:3FFFh	Closed	Open	Open	Open	Open			
E:1800h - E:1FFFh	E:2000h - E:3FFFh	Open	Open	Open	Open	Open			

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INTERRUPT REQUEST								
IRQ	JP6/1	JP6/2	JP6/3	JP6/4	JP6/5	JP6/6	JP6/7	JP6/8
í IRQ2	Open	Closed						
IRQ3	Open	Open	Open	Open	Open	Open	Closed	Open
IRQ4	Open	Open	Open	Open	Open	Closed	Open	Open
IRQ5	Open	Open	Open	Open	Closed	Open	Open	Open
IRQ10	Open	Open	Open	Closed	Open	Open	Open	Open
IRQ11	Open	Open	Closed	Open	Open	Open	Open	Open
IRQ12	Open	Closed	Open	Open	Open	Open	Open	Open
IRQ15	Closed	Open						

NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
1	Off	On						
2	On	Off	On	On	On	On	On	On
3	Off	Off	On	On	On	On	On	On
4	On	On	Off	On	On	On	On	On
251	Off	Off	On	Off	Off	Off	Off	Off
252	On	On	Off	Off	Off	Off	Off	Off
253	Off	On	Off	Off	Off	Off	Off	Off
254	On	Off						
255	Off							

Note: Node address 0 is used for messaging between nodes and must not be used.

A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 1=128, 2=64, 3=32, 4=16, 5=8, =4, 7=2, 8=1. Turn Off the switches and add the values of the Off switches to obtain the correct node at Iress. (On=0, Off=1)