# XINETRON, INC.
# R E 3 2 1

| | |
|---|---|
| **NIC Type** | Ethernet |
| **Transfer Rate** | 10Mbps |
| **Data Bus** | 16-bit ISA |
| **Topology** | Linear bus |
| **Wiring Type** | AUI transceiver via DB-15 port |
| | RG-58A/U 50ohm coaxial |
| **Boot ROM** | Available |
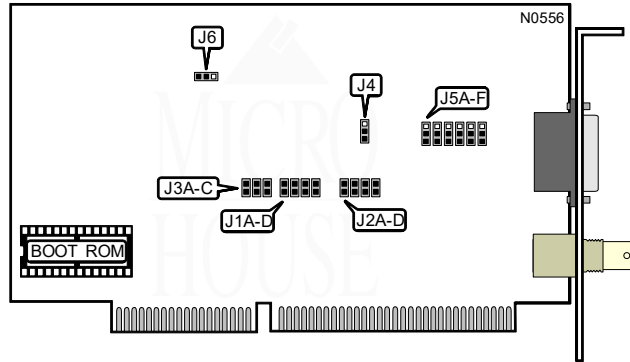


### INTERRUPT REQUEST

| IRQ | J1A | J1B | J1C | J1D |
|---|---|---|---|---|
| 2/9 | Open | Open | Open | Closed |
| í3 | Open | Open | Closed | Open |
| 4 | Open | Closed | Open | Open |
| 5 | Closed | Open | Open | Open |

### I/O BASE ADDRESS

| Address | J2A | J2B | J2C | J2D |
|---|---|---|---|---|
| í300 - 31Fh | Open | Closed | Closed | Closed |
| 320 - 33Fh | Open | Closed | Closed | Open |
| 340 - 35Fh | Open | Closed | Open | Closed |
| 360 - 37Fh | Open | Closed | Open | Open |

### BOOT ROM ADDRESS

| Address | J3A | J3B | J3C |
|---|---|---|---|
| C000h | Open | Open | Closed |
| CC00h | Open | Closed | Open |
| D000h | Closed | Open | Closed |

*Continued on next page . . .*

**THE NETWORK INTERFACE CARD TECHNICAL GUIDE**

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| CABLE TYPE | | |
|---|---|---|
| Type | J4 | J5A - J5F |
| íRG-58A/U 50ohm coaxial | Pins 1 & 2 closed | Pins 2 & 3 closed |
| AUI transceiver via DB-15 port | Pins 2 & 3 closed | Pins 1 & 2 closed |

| COMPATIBILITY MODE | |
|---|---|
| Setting | JP1 |
| íDisabled | Pins 2 & 3 closed |
| Enabled | Pins 1 & 2 closed |
| Note: On some systems, the data bus timing is not fully IBM PC/AT compatible.  If the card is not initializing, enabling the compatibility mode may allow the card to operate normally. | |

**1356**