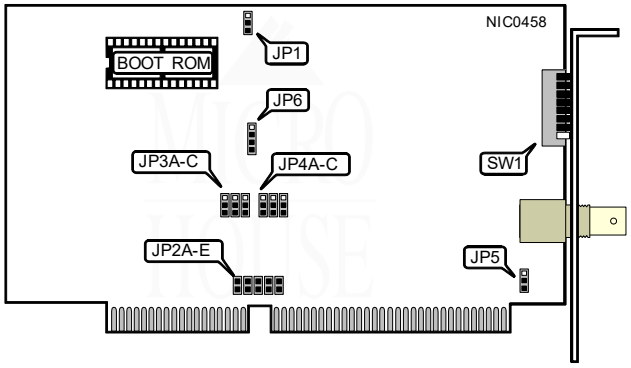
D-LINK SYSTEMS, INC.
D A - 2 0 0 +

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 16-bit ISA
Topology Star
Linear Bus
Wiring Type RG-62A/U 93ohm coaxial
Boot ROM Available



NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	Off	Off	Off	Off	Off	Off	On
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	On	On	On	On	On	On	Off
255	On	On	On	On	On	On	On	On

Note: Node address 0 is used for messaging between nodes and must not be used.
A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=128, 2=64, 3=32, 4=16, 5=8, 6=4, 7=2, 8=1. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, Off=0)

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BOOT ROM	
Setting	JP1
íDisabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed

INTERRUPT REQUEST					
IRQ	JP2A	JP2B	JP2C	JP2D	JP2E
3	Open	Closed	Open	Open	Open
4	Open	Open	Closed	Open	Open
5	Open	Open	Open	Closed	Open
7	Open	Open	Open	Open	Closed
9	Closed	Open	Open	Open	Open

I/O BASE ADDRESS			
Address	JP3A	JP3B	JP3C
260h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
290h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
í2E0h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
300h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
350h	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
380h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
3E0h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

BOOT ROM ADDRESS			
Address	JP4A	JP4B	JP4C
C0000 - C3FFFh	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
C4000 - C7FFFh	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
CC000 - CFFFFh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
D0000 - D3FFFh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
D4000 - D7FFFh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
D8000 - DBFFFh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
DC000 - DFFFFh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

TOPOLOGY	
Setting	JP5
íStar	Pins 2 & 3 closed
Linear bus	Pins 1 & 2 closed
Note: All NICs on the network segment must have this option set the same.	

SHARED MEMORY MODE	
Mode	JP6
í16-bit/2KB	Pins 1 & 2 closed
16-bit/128KB	Pins 2 & 3 closed
8-bit/2KB	Pins 3 & 4 closed