D-LINK
DA-1 20
NIC Type ARCnet
Transfer Rate $\quad 2.5 \mathrm{Mbps}$
Data Bus
Topology
Wiring Type
Boot ROM

8-bit ISA
Linear Bus
Unshielded twisted pair
Available


| NODE ADDRESS |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Node | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 | SW1/8 |
| 0 | - | - | - | - | - | - | - | - |
| 1 | Off | Off | Off | Off | Off | Off | Off | On |
| 2 | Off | Off | Off | Off | Off | Off | On | Off |
| 3 | Off | Off | Off | Off | Off | Off | On | On |
| 4 | Off | Off | Off | Off | Off | On | Off | Off |
| 251 | On | On | On | On | On | Off | On | On |
| 252 | On | On | On | On | On | On | Off | Off |
| 253 | On | On | On | On | On | On | Off | On |
| 254 | On | On | On | On | On | On | On | Off |
| 255 | On | On | On | On | On | On | On | On |

Note: Node address 0 is used for messaging between nodes and must not be used.
A total of 255 node address settings are available. The switches are a binary repres ntation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most jignificant Bit. The switches have the following decimal values: switch $1=128,2=64,3=32,4=16,5=8,=4,7=2,8=1$. Turn on the switches and add the values of the on switches to obtain the correct node ac Iress. (On=1, Off=0)

## THE NETWORK INTERFACE CARD TECHNICAL GUIDE

D-LINK
DA-120
. continued from previous page

| INTERRUPT REQUEST |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| IRQ | JP1A | JP1B | JP1C | JP1D | JP1E |
| 2 | Closed | Open | Open | Open | Open |
| 3 | Open | Closed | Open | Open | Open |
| 4 | Open | Open | Closed | Open | Open |
| 5 | Open | Open | Open | Closed | Open |
| 7 | Open | Open | Open | Open | Closed |


| I/O BASE ADDRESS |  |  |  |
| :---: | :---: | :---: | :---: |
| Address | JP2A | JP2B | JP2C |
| 260 h | Plns 2 \& 3 closed | Plns 2 \& 3 closed | Plns 2 \& 3 closed |
| 290h | Plns 2 \& 3 closed | Plns 2 \& 3 closed | Pins 1 \& 2 closed |
| i2E0h | Plns 2 \& 3 closed | Pins 1 \& 2 closed | Plns 2 \& 3 closed |
| 300h | Pins 1 \& 2 closed | Plns 2 \& 3 closed | Plns 2 \& 3 closed |
| 350h | Pins 1 \& 2 closed | Plns 2 \& 3 closed | Pins 1 \& 2 closed |
| 380h | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Plns 2 \& 3 closed |
| 3E0h | Pins 1 \& 2 closed | Pins 1 \& 2 closed | Pins 1 \& 2 closed |


| RESPONSE TIMEOUTS |  |  |  |
| :---: | :---: | :---: | :---: |
| Response Time | JP4A | JP4B |  |
| $i 78 \mu \mathrm{~s}$ | Pins 1 \& 2 closed | Plns 1 \& 2 closed |  |
| $285 \mu \mathrm{~s}$ | Pins 1 \& 2 closed | Plns 1 \& 2 closed |  |
| $563 \mu \mathrm{~s}$ | Plns 2 \& 3 closed | Plns 2 \& 3 closed |  |
| $1130 \mu \mathrm{~s}$ | Plns 2 \& 3 closed |  |  |
| Note: All NICs on the network segment must have this option set the same. |  |  |  |


| Setting | BOOT ROM |
| :---: | :---: |
| iDisabled | JP5 |
| Enabled | Pins 2 \& 3 closed |

Chapter 5: Jumper Settings
D-LINK
DA-120
. continued from previous page

| BASE MEMORY ADDRESS \& BOOT ROM ADDRESS |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Base | Boot ROM | JP3A | JP3B | JP3C | JP3D | JP3E |
| C0000h | C2000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 |
| C0800h | C2000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 |
| C1000h | C2000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 |
| C1800h | C2000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 |
| C4000h | C6000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 |
| C4800h | C6000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 |
| C5000h | C6000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 |
| C5800h | C6000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 |
| CC000h | CE000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 |
| CC800h | CE000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 |
| CD000h | CE000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 |
| CD800h | CE000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 |
| íD0000h | D2000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 |
| D0800h | D2000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 |
| D1000h | D2000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 |
| D1800h | D2000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 |
| D4000h | D6000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 |
| D4800h | D6000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 |
| D5000h | D6000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 |
| D5800h | D6000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 |
| D8000h | DA000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 |
| D8800h | DA000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 |
| D9000h | DA000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 |
| D9800h | DA000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 |
| DC000h | DE000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 |
| DC800h | DE000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 |
| DD000h | DE000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 |
| DD800h | DE000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 |
| E0000h | E2000h | Pins 2 \& 3 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 |
| E0800h | E2000h | Pins 1 \& 2 | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 |
| E1000h | E2000h | Pins 2 \& 3 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 |
| E1800h | E2000h | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 | Pins 1 \& 2 |
| ote: Pins d | ted should | the closed | tion. |  |  |  |

