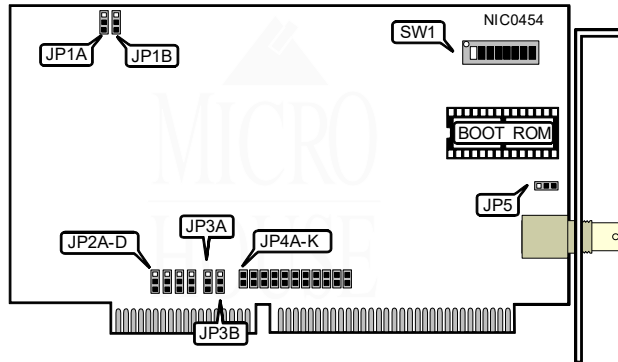


D-LINK
DA-200

NIC Type ARCnet
Transfer Rate 2.5Mbps
Data Bus 16-bit ISA
Topology Star
Wiring Type RG-62A/U 93ohm coaxial
Boot ROM Available



NODE ADDRESS								
Node	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6	SW1/7	SW1/8
0	-	-	-	-	-	-	-	-
1	Off	Off	Off	Off	Off	Off	Off	On
2	Off	Off	Off	Off	Off	Off	On	Off
3	Off	Off	Off	Off	Off	Off	On	On
4	Off	Off	Off	Off	Off	On	Off	Off
251	On	On	On	On	On	Off	On	On
252	On	On	On	On	On	On	Off	Off
253	On	On	On	On	On	On	Off	On
254	On	On	On	On	On	On	On	Off
255	On	On	On	On	On	On	On	On

Note: Node address 0 is used for messaging between nodes and must not be used.
 A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 1 is the Least Significant Bit and switch 8 is the Most Significant Bit. The switches have the following decimal values: switch 1=128, 2=64, 3=32, 4=16, 5=8, 6=4, 7=2, 8=1. Turn on the switches and add the values of the on switches to obtain the correct node address. (On=1, Off=0)

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RESPONSE TIMEOUTS				
Response Time	Idle Time	Reconfig Time	JP1A	JP1B
178 μ s	86 μ s	840ms	Pins 1 & 2 closed	Pins 1 & 2 closed
285 μ s	316 μ s	1680ms	Pins 1 & 2 closed	Pins 2 & 3 closed
563 μ s	624 μ s	1680ms	Pins 2 & 3 closed	Pins 1 & 2 closed
1130 μ s	1237 μ s	1680ms	Pins 2 & 3 closed	Pins 2 & 3 closed

Note: All NICs on the network segment must have this option set the same.

I/O BASE ADDRESS				
Address	JP2A	JP2B	JP2C	JP2D
200h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
240h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
260h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
2E0h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
300h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
340h	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
360h	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
3E0h	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

BOOT ROM ADDRESS		
Address	JP3A	JP3B
C8000 - CBFFFh	Pins 2 & 3 closed	Pins 2 & 3 closed
CC000 - CFFFFh	Pins 1 & 2 closed	Pins 2 & 3 closed
ID8000 - DBFFFh	Pins 2 & 3 closed	Pins 1 & 2 closed
DC000 - DFFFFh	Pins 1 & 2 closed	Pins 1 & 2 closed

INTERRUPT REQUEST											
IRQ	JP4A	JP4B	JP4C	JP4D	JP4E	JP4F	JP4G	JP4H	JP4I	JP4J	JP4K
2/9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open
3	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open
4	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open
5	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open
6	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open
7	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open
10	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open
11	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open
12	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open
13	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open

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15	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Close d
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BOOT ROM	
Setting	JP5
Disabled	Pins 2 & 3 closed
Enabled	Pins 1 & 2 closed