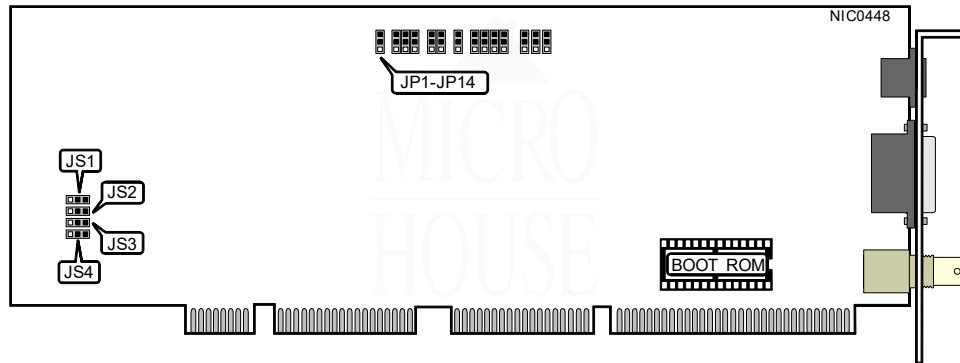


ADVANCED INTEGRATION RESEARCH, INC.
ENET2V VER. 1.0

NIC Type Ethernet
Transfer Rate 10Mbps
Data Bus 32-bit VESA Local bus
Topology Star
 Linear bus
Wiring Type Unshielded twisted pair
 RG-58A/U 50ohm coaxial
 AUI transceiver via DB-15 port
Boot ROM Available



DATA BUS TYPE		
Type	JP1	JP12
EISA	Pins 1 & 2 closed	Pins 1 & 2 closed
ISA	Pins 2 & 3 closed	Pins 2 & 3 closed

Note: If the card is installed in a EISA type machine the configuration utility included with the card must be used and jumpers JP1 through JP14 must be set to Pins 1 & 2 closed.

I/O BASE ADDRESS			
Address	JP2	JP3	JP4
100 - 11Fh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
120 - 13Fh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
140 - 15Fh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
160 - 17Fh	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
300 - 31Fh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
320 - 33Fh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed

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INTERRUPT REQUEST		
IRQ	JP5	JP6
2/9	Pins 2 & 3 closed	Pins 1 & 2 closed
i5	Pins 1 & 2 closed	Pins 1 & 2 closed
10	Pins 1 & 2 closed	Pins 2 & 3 closed
11	Pins 2 & 3 closed	Pins 2 & 3 closed

BOOT ROM	
Setting	JP7
iDisabled	Pins 1 & 2 closed
Enabled	Pins 2 & 3 closed

BOOT ROM ADDRESS				
Address	JP8	JP9	JP10	JP11
iC0000 - C3FFFh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
C4000 - C7FFFh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
C8000 - CBFFFh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
CC000 - CFFFFh	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
D0000 - D3FFFh	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
D4000 - D7FFFh	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
D8000 - DBFFFh	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
DC000 - DFFFFh	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed

CABLE TYPE	
Type	JP13
iRG-58A/U 50ohm coaxial	Pins 2 & 3 closed
Twisted pair/AUI transceiver via DB-15 port	Pins 1 & 2 closed

CARD ENABLE	
Setting	JP14
iEnabled	Pins 2 & 3 closed
Disabled	Pins 1 & 2 closed

Note: This setting is the equivalent of taking the card out of the computer.

HOST SYSTEM CPU SPEED				
Speed	JS1	JS2	JS3	JS4
33MHz or less	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
50MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

FACTORY CONFIGURED SETTINGS	
Jumper	Setting
JP12	Pins 2 & 3 closed