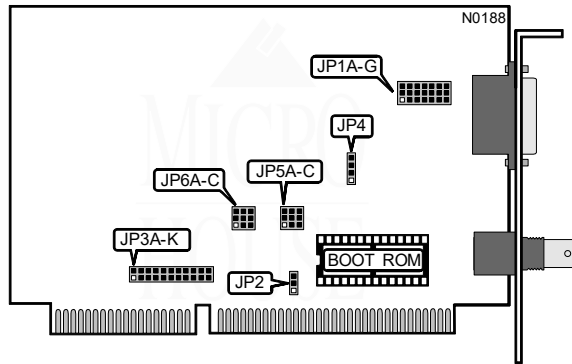


D-LINK DE-200 for PC AT

NIC Type Ethernet
Transfer Rate 10Mbps
Data Bus 16-bit ISA
Topology Linear Bus
Wiring Type RG--58A/U 50ohm coaxial
 AUI transceiver via DB-15 port
Boot ROM Available



BOOT ROM	
Setting	JP2
Disabled	Pins 1 & 2 Closed
Enabled	Pins 2 & 3 Closed

INTERRUPT REQUEST												
IR Q	JP3A	JP3B	JP3C	JP3D	JP3E	JP3F	JP3G	JP3H	JP3I	JP3J	JP3K	
3	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	
4	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	Open	
5	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	Open	
6	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	Open	
7	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	Open	
9	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	Open	
10	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	Open	
11	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	Open	
12	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	Open	
14	Open	Open	Open	Open	Open	Open	Open	Open	Open	Closed	Open	

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15	Open	Open	Open	Open	Open	Open	Open	Open	Open	Open	Close d
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DE-200 for PC AT

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I/O BASE ADDRESS			
Address	JP6A	JP6B	JP6C
200h	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
220h	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 1 & 2 Closed
240h	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
260h	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
i300h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
320h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
340h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed
360h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed

CHIP SET	
Type	JP4
iStandard	Pins 1 & 2 Closed
Non-standard 1	Pins 2 & 3 Closed
Non-standard 2	Pins 3 & 4 closed

Note: Non-standard PC AT systems and early versions of Chips & Technologies chipsets do not use timing compatible with the IBM AT bus for the I/O CHRDY signal. If the NIC does not work with the standard setting, try one of the two non-standard settings.

CABLE TYPE	
Type	JP1A-G
iRG-58A/U 50ohm coaxial	Pins 2 & 3 Closed
AUI transceiver via DB-15 port	Pins 1 & 2 Closed

BOOT ROM ADDRESS			
Address	JP5A	JP5B	JP5C
C8000h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
CC000h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 1 & 2 Closed
iD0000h	Pins 1 & 2 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
D4000h	Pins 2 & 3 Closed	Pins 1 & 2 Closed	Pins 2 & 3 Closed
D8000h	Pins 1 & 2 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed
DC000h	Pins 2 & 3 Closed	Pins 2 & 3 Closed	Pins 2 & 3 Closed