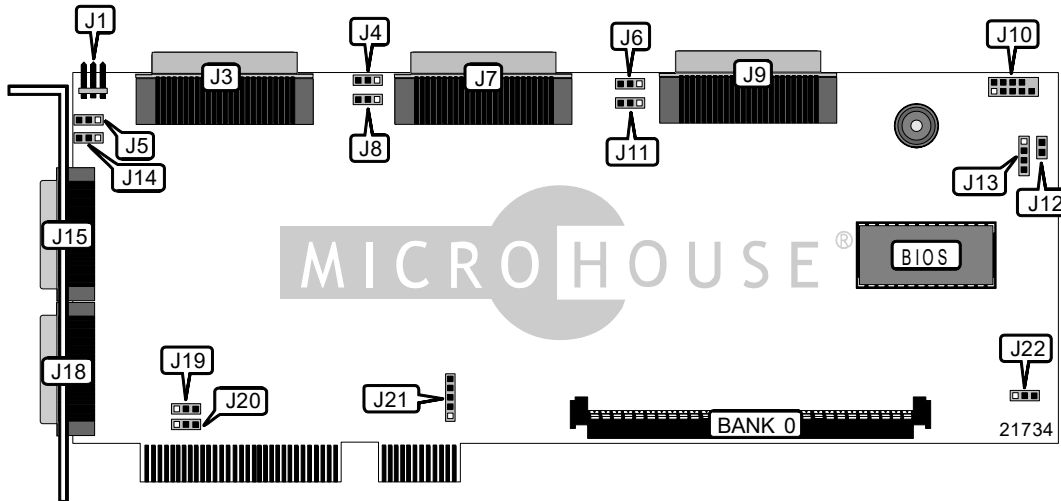


# AMERICAN MEGATRENDS, INC. MEGARAID LVDS

Card Type	Hard drive controller card
Processor	Intel i960RD
Maximum Onboard Memory	128MB DRAM
Hard Drives supported	Up to 45 SCSI devices
Floppy drives supported	None
Data Bus	32-bit PCI
Card Size	Full-length, full-height card



CONNECTIONS			
Function	Location	Function	Location
Auxiliary power connector	J1	2-pin connector - dirty cache LED	J12
I2C connector	J2	4-pin connector - drive active LED	J13
68-pin SCSI connector - internal	J3	68-pin SCSI connector - external	J15
68-pin SCSI connector - internal	J7	EEPROM connector	J16
68-pin SCSI connector - internal	J9	68-pin SCSI connector - external	J18
9-pin serial port 1 - internal	J10	External battery	J21

USER CONFIGURABLE SETTINGS			
Function	Label	Position	
i960RD processor initialization mode 0	J22	Pins 2 & 3 closed	
i960RD processor initialization mode 3	J22	Pins 1 & 2 closed	

CHANNEL 1 TERMINATION SELECTION			
Setting	J5	J14	
i960RD processor provides termination power channel 1	Pins 1 & 2 closed	Pins 1 & 2 closed	
Auxiliary power source provides termination power channel 1	Pins 2 & 3 closed	Pins 2 & 3 closed	
SCSI bus provides termination power channel 1	Open	Open	

CHANNEL 2 TERMINATION SELECTION			
Setting	J4	J6	
i960RD processor provides termination power channel 2	Pins 1 & 2 closed	Pins 1 & 2 closed	
Auxiliary power source provides termination power channel 2	Pins 2 & 3 closed	Pins 2 & 3 closed	
SCSI bus provides termination power channel 2	Open	Open	

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## MEGARAID LVDS

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CHANNEL 3 TERMINATION SELECTION		
Setting	J8	J11
PCI bus provides termination power channel 3	Pins 1 & 2 closed	Pins 1 & 2 closed
Auxiliary power source provides termination power channel 3	Pins 2 & 3 closed	Pins 2 & 3 closed
SCSI bus provides termination power channel 3	Open	Open

DRAM CONFIGURATION	
Size	Bank 0
4MB	(1) 1MB x 36
16MB	(1) 4MB x 36
64MB	(1) 16MB x 36
128MB	(1) 32MB x 36

SKYSCAPE CONFIGURATION		
Setting	J19	J20
Standalone RAID controller in a standard PCI slot	Pins 1 & 2 closed	Open
RAID upgrade of motherboard SCSI controller	Pins 2 & 3 closed	Pins 1 & 2 closed
Software configuration control	Pins 2 & 3 closed	Pins 2 & 3 closed

MISCELLANEOUS TECHNICAL NOTES
The location of J2 and J16 are unidentified.