Q4/95

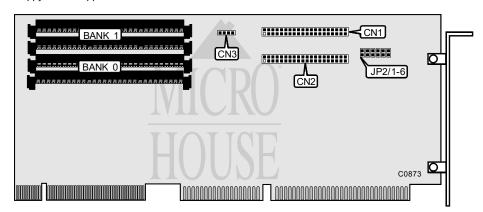
ATRONICS INTERNATIONAL, INC. ATI-IDEAL VL-CACHE

Data bus: 32-bit VL-bus

Three-quarter length, full-height card Size:

Four IDE(AT) drives

Hard drive supported: Floppy drives supported: None



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) connector	CN1
40-pin secondary IDE(AT) connector	CN2
4-pin connector - drive active LED	CN3

USER CONFIGURABLE SETTINGS			
Function	Location	Setting	
í Factory configured - do not alter	JP2/4	open	
í IDE primary address is 1F0h	JP2/5	closed	
IDE secondary address is 170h	JP2/5	open	
í Factory configured - do not alter	JP2/6	closed	

Continued on next page. . .

ATRONICS INTERNATIONAL, INC. ATI-IDEAL VL-CACHE

. . .continued from previous page

BIOS ADDRESS SELECTION				
Address	JP2/1	JP2/2	JP2/3	
í DC000-DFFFFh	closed	closed	closed	
C8000-CBFFFh	closed	open	open	
CC000-CFFFFh	open	closed	open	
D0000-D3FFFh	closed	closed	open	
D4000-D7FFFh	open	open	closed	
D8000-DBFFFh	closed	open	closed	
Disabled	open	open	open	

	DRAM CONFIGURATION		
Size	Bank 0	Bank 1	
512KB	(2) 256 x 9	none	
1MB	(2) 256 x 9	(2) 256 x 9	
2MB	(2) 1MB x 9	none	
2.5MB	(2) 256 x 9	(2) 1MB x 9	
4MB	(2) 1MB x 9	(2) 1MB x 9	
8MB	(2) 4MB x 9	none	
8.5MB	(2) 4MB x 9	(2) 256 x 9	
10MB	(2) 4MB x 9	(2) 1MB x 9	
16MB	(2) 4MB x 9	(2) 4MB x 9	
Note: Bank 1 & Bank 0 are interchangeable.			