Q4/95

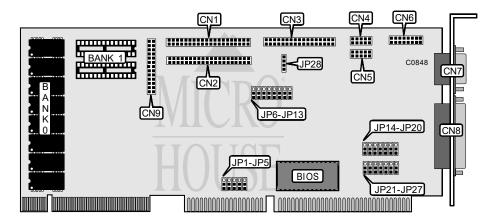
ACER, INC. L50

Data bus: 32-bit VL-bus

Size: Three-quarter length, full-height card

Hard drive supported:

Four IDE(AT) drives Two 360KB, 720KB, 1.2MB, 1.44MB, or 2.88MB drives Floppy drives supported:



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) connector	CN1
40-pin secondary IDE(AT) connector	CN2
34-pin cable connector - floppy drive	CN3
10-pin serial port 1	CN4
10-pin serial port 2	CN5
16-pin game port	CN6
15-pin video port	CN7
25-pin parallel port	CN8
VESA feature connector	CN9
4-pin connector - drive active LED	JP28

	VGA CONFIGURATION	
Mode	JP4	JP5
í Enabled	pins 1 & 2 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í IRQ9 disabled	JP3/pins 1 & 2	closed
IRQ9 enabled	JP3/pins 2 & 3	closed
í Primary IDE(AT) interface enabled	JP6/pins 1 & 2	closed
Primary IDE(AT) interface disabled	JP6/pins 2 & 3	closed
í Floppy drive interface enabled	JP11/pins 1 & 2	closed
Floppy drive interface disabled	JP11/pins 2 & 3	closed
í Ignores cable select configuration for floppy drives	JP12/pins 1 & 2	closed
Checks for cable select configuration	JP12/pins 2 & 3	closed
í Game port enabled	JP13/pins 1 & 2	closed
Game port disabled	JP13/pins 2 & 3	closed
í Parallel port IRQ 7 select	JP18/pins 1 & 2	closed
Parallel port IRQ 5 select	JP18/pins 2 & 3	closed

PARALLEL PORT ADDRESS SELECTION		
LPT	JP14	JP15
í LPT1 (378h)	pins 1 & 2 closed	pins 1 & 2 closed
LPT2 (278h)	pins 1 & 2 closed	pins 2 & 3 closed
LPT3 (3BCh)	pins 2 & 3 closed	pins 2 & 3 closed
Disabled	pins 2 & 3 closed	pins 1 & 2 closed

SER	IAL PORT 1 ADDRESS SELECTION	ON
COM	JP21	JP22
í COM1 (3F8h)	pins 1 & 2 closed	pins 1 & 2 closed
COM2 (2F8h)	pins 1 & 2 closed	pins 2 & 3 closed
COM3 (3E8h)	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SER	IAL PORT 2 ADDRESS SELECTION	ON
COM	JP23	JP24
í COM2 (2F8h)	pins 1 & 2 closed	pins 1 & 2 closed
COM1 (3F8h)	pins 1 & 2 closed	pins 2 & 3 closed
COM4 (2E8h)	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT SELECTION			
IRQ	JP25/Pins 1 & 2	JP26/Pins 1 & 2	JP27/Pins 1 & 2
í IRQ4	closed	open	open
IRQ3	open	closed	open
IRQ5	open	open	closed

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SERIAL PORT 2 INTERRUPT SELECTION			
IRQ	JP25/Pins 2 & 3	JP26/Pins 2 & 3	JP27/Pins 2 & 3
í IRQ3	open	closed	open
IRQ4	closed	open	open
IRQ5	open	open	closed

PARALLEL PORT MODE CONFIGURATION		
Mode JP16 JP17		
í SPP	pins 1 & 2 closed	pins 1 & 2 closed
EPP pins 1 & 2 closed pins 2 & 3 closed		pins 2 & 3 closed
ECP	pins 2 & 3 closed	pins 1 & 2 closed

DMA SELECTION FOR ECP MODE		
Channel JP19 JP20		
í DMA 3 pins 1 & 2 closed pins 1 & 2 closed		
DMA 1	pins 2 & 3 closed	pins 2 & 3 closed

	MEMORY ADDRESSING	
Linear Address	JP1	JP2
í 64MB	pins 1 & 2 closed	pins 1 & 2 closed
2GB	pins 2 & 3 closed	pins 2 & 3 closed

SECONDARY IDE ADDRESS SELECTION		
Address JP7 JP8		
í Secondary	pins 1 & 2 closed	pins 1 & 2 closed
Primary pins 1 & 2 closed pins 2 & 3 closed		
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SECONDARY IDE INTERRUPT SELECTION			
IRQ	JP9	JP10	
í 15	pins 1 & 2 closed	pins 1, 2 & 3 open	
10	pins 1, 2 & 3 open	pins 1 & 2 closed	
12	pins 1, 2 & 3 open	pins 2 & 3 closed	
14	pins 2 & 3 closed	pins 1, 2 & 3 open	

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	
1MB	(8) 256K x 4	None	
2MB	(8) 256K x 4	(2) 256K x 16	