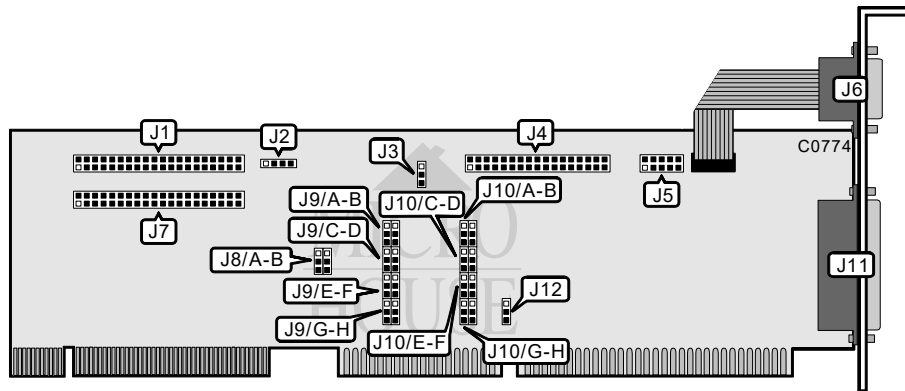


BOCA RESEARCH, INC. IDE PLUS: IDEVL2

Q2/95

Data bus: 32-bit, VL-bus
Size: Three-quarter-length, half-height card
Hard drive supported: Four IDE (AT) Interface drives
Floppy drives supported: Two 360KB, 720KB, 1.2MB, 1.44MB, or 2.88MB drives



CONNECTIONS		
Function	Location	
40-pin IDE(AT) interface connector A	J1	
4-pin connector - drive active LED	J2	
34-pin control cable connector - floppy drive	J4	
10-pin serial port 2 - internal	J5	
10-pin serial port 1 - external	J6	
40-pin IDE(AT) interface connector B	J7	
25-pin parallel port	J11	

USER CONFIGURABLE SETTINGS			
Function	Location	Setting	
<input type="checkbox"/> Floppy drive enabled <input type="checkbox"/> Floppy drive disabled	J3	pins 1 & 2 closed	
<input type="checkbox"/> Parallel port uses IRQ7 <input type="checkbox"/> Parallel port uses IRQ5	J12	pins 1 & 2 closed	
	J12	pins 2 & 3 closed	

IDE PORTS A & B CONFIGURATION - J8							
Port A (J1)		Port B (J7)		Jumper A		Jumper B	
<input type="checkbox"/> Primary address	Disabled	<input type="checkbox"/> Primary address	Disabled	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 1 & 2 closed
<input type="checkbox"/> Secondary address	Disabled	<input type="checkbox"/> Secondary address	Disabled	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 2 & 3 closed
<input type="checkbox"/> Primary address	Secondary address	<input type="checkbox"/> Primary address	Secondary address	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 2 & 3 closed
Disabled	Disabled	Disabled	Disabled	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 2 & 3 closed	<input type="checkbox"/> pins 1 & 2 closed	<input type="checkbox"/> pins 1 & 2 closed

Continued on next page . . .

BOCA RESEARCH, INC.
IDE PLUS: IDEVL2

... continued from previous page

IDE PORTS A & B INTERRUPT SELECT - J10			
Port A (J1)	Port B (J7)	Jumper G	Jumper H
IRQ14	Disabled	pins 1 & 2 closed	all pins open
IRQ15	Disabled	pins 2 & 3 closed	all pins open
IRQ14	IRQ15	pins 1 & 2 closed	pins 2 & 3 closed
IRQ15	IRQ14	pins 2 & 3 closed	pins 1 & 2 closed

SERIAL PORT 1 CONFIGURATION - J9		
COM/Address	Jumper A	Jumper B
COM1/3F8h	pins 2 & 2 closed	pins 2 & 2 closed
COM2/2F8h	pins 2 & 2 closed	pins 2 & 3 closed
COM3/3E8h	pins 2 & 3 closed	pins 2 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT SELECT - J9		
IRQ	Jumper E	Jumper F
IRQ4	pins 2 & 2 closed	all pins open
IRQ3	all pins open	pins 2 & 2 closed
IRQ5	all pins open	pins 2 & 3 closed
IRQ7	pins 2 & 3 closed	all pins open
Disabled	all pins open	all pins open

SERIAL PORT 2 CONFIGURATION - J9		
COM/Address	Jumper C	Jumper D
COM2/2F8h	pins 2 & 2 closed	pins 2 & 2 closed
COM1/3F8h	pins 2 & 2 closed	pins 2 & 3 closed
COM4/2E8h	pins 2 & 3 closed	pins 2 & 2 closed
Disabled	pins 2 & 3 closed	pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT SELECT - J9		
IRQ	Jumper G	Jumper H
IRQ3	all pins open	pins 2 & 2 closed
IRQ4	pins 2 & 2 closed	all pins open
IRQ5	all pins open	pins 2 & 3 closed
IRQ7	pins 2 & 3 closed	all pins open
Disabled	all pins open	all pins open

Continued on next page ...

BOCA RESEARCH, INC.
IDE PLUS: IDEVL2

... continued from previous page

PARALLEL PORT CONFIGURATION - J10		
LPT/Address	Jumper A	Jumper B
Port 1/378h	pins 1 & 2 closed	pins 1 & 2 closed
Port 2/278h	pins 1 & 2 closed	pins 1 & 3 closed
Disabled	pins 1 & 3 closed	pins 1 & 2 closed

ENHANCED PARALLEL PORT MODES - J10		
Mode	Jumper C	Jumper D
Standard	pins 1 & 2 closed	pins 1 & 2 closed
Enhanced Parallel Port (EPP)	pins 1 & 2 closed	pins 1 & 3 closed
Extended Capabilities Port (ECP)	pins 1 & 3 closed	pins 1 & 2 closed
IEEE 1284 all of the above)	pins 1 & 3 closed	pins 1 & 3 closed

ENHANCED PARALLEL PORT DMA CHANNEL SELECT - J10		
DMA Channel	Jumper E	Jumper F
Disabled	all pins open	all pins open
DMA1	pins 1 & 2 closed	pins 1 & 2 closed
DMA3	pins 1 & 3 closed	pins 1 & 3 closed